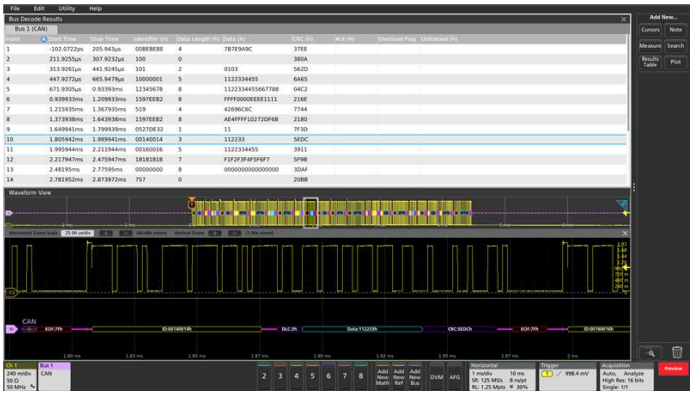
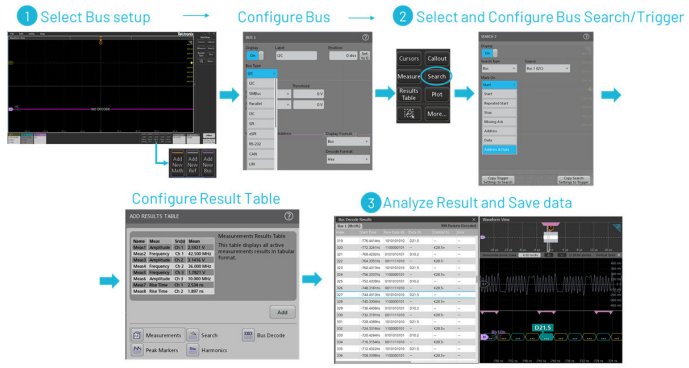
Serial Triggering and Analysis

3 Series MDO, 4/5/6 Series MSO, and 7 Series DPO Applications Datasheet



On a serial bus, signal often includes address, control, data, and clock information. This can make isolating events of interest difficult. Optional serial applications transform the oscilloscope into a robust tool for debugging serial buses with automatic decode and analysis for I2C, SPI, eSPI, CAN, CAN FD, CAN XL, LIN, FlexRay, 10BASE-T1S, 100BASE-T1, SENT, RS-232/422/485, UART, Parallel, USB 1.0, USB 1.1, USB 2.0, USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1, USB 3.2 Gen 2, USB4 v1.0, Ethernet, I3C, SPMI, Spacewire, 8B10B, NFC, NRZ, MIL-STD-1553, ARINC 429, I2S, LJ, RJ, TDM, PCIe Gen 1/Gen 2/Gen 3/Gen 4, PSI5, CPHY, CXPI, DPHY, SMBus, 1-Wire, EtherCAT, and 128B/132B.



Key features

- Automated Serial Decode and Analysis Options for I2C, SPI, eSPI, CAN, CAN FD, CAN XL, LIN, FlexRay, 10BASE-T1S, 100BASE-T1, SENT, RS-232/422/485, UART, Parallel, USB 1.0, USB 1.1, USB 2.0, USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1, USB 3.2 Gen 2, USB4 v1.0, Ethernet, I3C, SPMI, Spacewire, 8B10B, NFC, NRZ, MIL-STD-1553, ARINC 429, I2S, LJ, RJ, TDM, PCIe Gen 1/Gen 2/Gen 3/Gen 4, PSI5, CPHY, CXPI, DPHY, SMBus, 1-Wire, EtherCAT, and 128B/132B.
- Trigger on critical serial bus elements —address, data, etc.
- Decode all the critical elements of each message. No more counting 1s and 0s
- Search through long acquisitions with user-defined criteria to find specific messages
- Event Table shows decoded serial bus activity in a tabular, time-stamped format for quick summary of system activity

Serial Triggering and Analysis Applications

The serial applications support automatic decode and trigger for I2C, SPI, CAN, CAN FD, CAN XL, LIN, FlexRay, 10BASE-T1S, 100BASE-T1, SENT, RS-232/422/485, UART, Parallel, USB 1.0, USB 1.1, USB 2.0, USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1, USB 3.2 Gen 2, USB4 v1.0, Ethernet, I3C, SPMI, Spacewire, 8B10B, NRZ, MIL-STD-1553, ARINC 429, I2S, LJ, RJ, TDM, PCIe Gen 1/Gen 2/Gen 3/Gen 4, PSI5, SMBus, 1-Wire, EtherCAT, and 128B/132B, making it easier to locate, analyze, and debug events of interest.

Serial triggering

Trigger on packet content such as start of packet, specific addresses, specific data content, unique identifiers, etc. on popular serial interfaces such as I2C, Parallel, I3C, SPI, CAN, CAN FD, CAN XL, LIN, FlexRay, SENT, RS-232/422/485, UART, USB 1.0, USB 1.1, USB 2.0, USB 3.0, USB 3.1 Gen 1, USB 3.2 Gen 1, Ethernet, SPMI, MIL-STD-1553, ARINC 429, I2S, LJ, RJ, TDM, Audio, and SVID.

Bus display

The bus display provides a higher-level, combined view of the individual signals (clock, data, chip enable, and so on) that make up your bus, making it easy to identify where packets begin and end and identifying sub-packet components such as address, data, errors, and so on.

Bus decoding

Tired of having to visually inspect the waveform to count clocks, determine if each bit is a 1 or a 0, combine bits into bytes, and determine the hex value?

Let the oscilloscope with a serial application do it for you! Once you've set up a bus, the oscilloscope decodes each packet on the bus, and displays the value in hex, binary, ASCII, or decimal (certain buses only) in the bus waveform.

Results table

In addition to seeing decoded packet data on the bus waveform itself, you can view all captured packets in a tabular view much like you would see in a software listing. Packets are time stamped and listed consecutively with columns for each component (Address, Data, and so on).

Wave Inspector® search

Serial triggering is very useful for isolating the event of interest, but once you've captured it and need to analyze the surrounding data, what do you do?

In the past, users had to manually scroll through the waveform counting and converting bits and looking for what caused the event. With a serial application, you can enable the oscilloscope to automatically search through the acquired data for user-defined criteria including serial packet content. Each occurrence is highlighted by a search mark. Rapid navigation between marks is as simple as pressing the ← and → arrow buttons on the oscilloscope front panel or the Search badge. The 3 Series MDO uses the arrows in the Search badge to navigate.

1-Wire Characteristics

Bus setup options

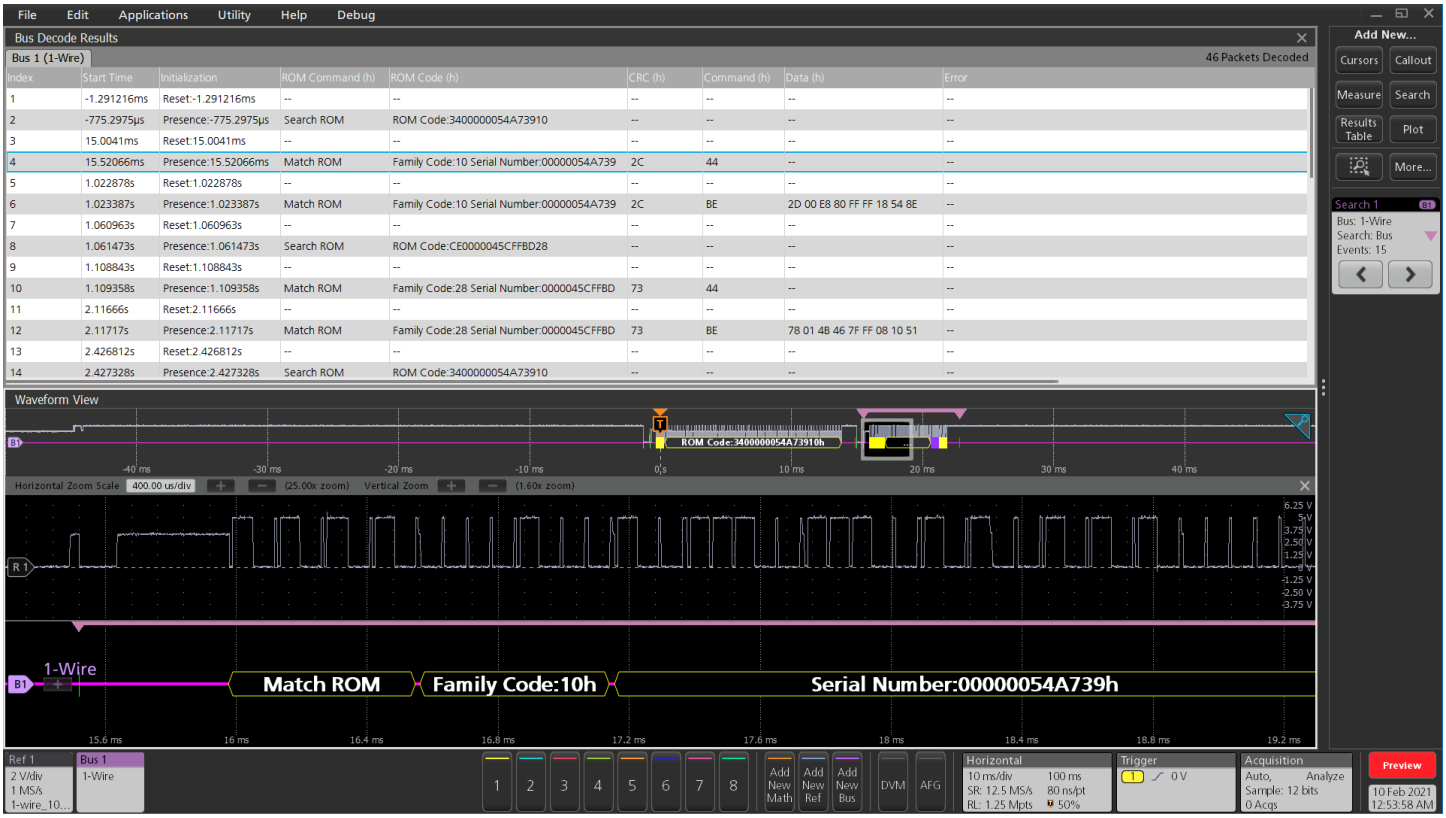
Characteristic	Description
1-Wire Sources	Analog channels Digital Channels Active Math channels Active Reference channels
Salient Features	Decode capability in for 1-Wire protocol. Decode capability for Standard mode. Decode capability for Overdrive mode. Search capability for Reset, Presence events Search capability for Command, Data Search capability for different ROM packets such as Read/Match/Skip/Search ROM and Alarm based on the Standard or Overdrive mode chosen. Search capability for CRC Error
Formats Available	Hex Binary Mixed Hex
Mode	Specifies the mode of operation – Standard (15.4 kbits/s) or Overdrive (125 kbits/s).
Recommended Probing	Single Ended passive probe Differential passive probe

Display modes

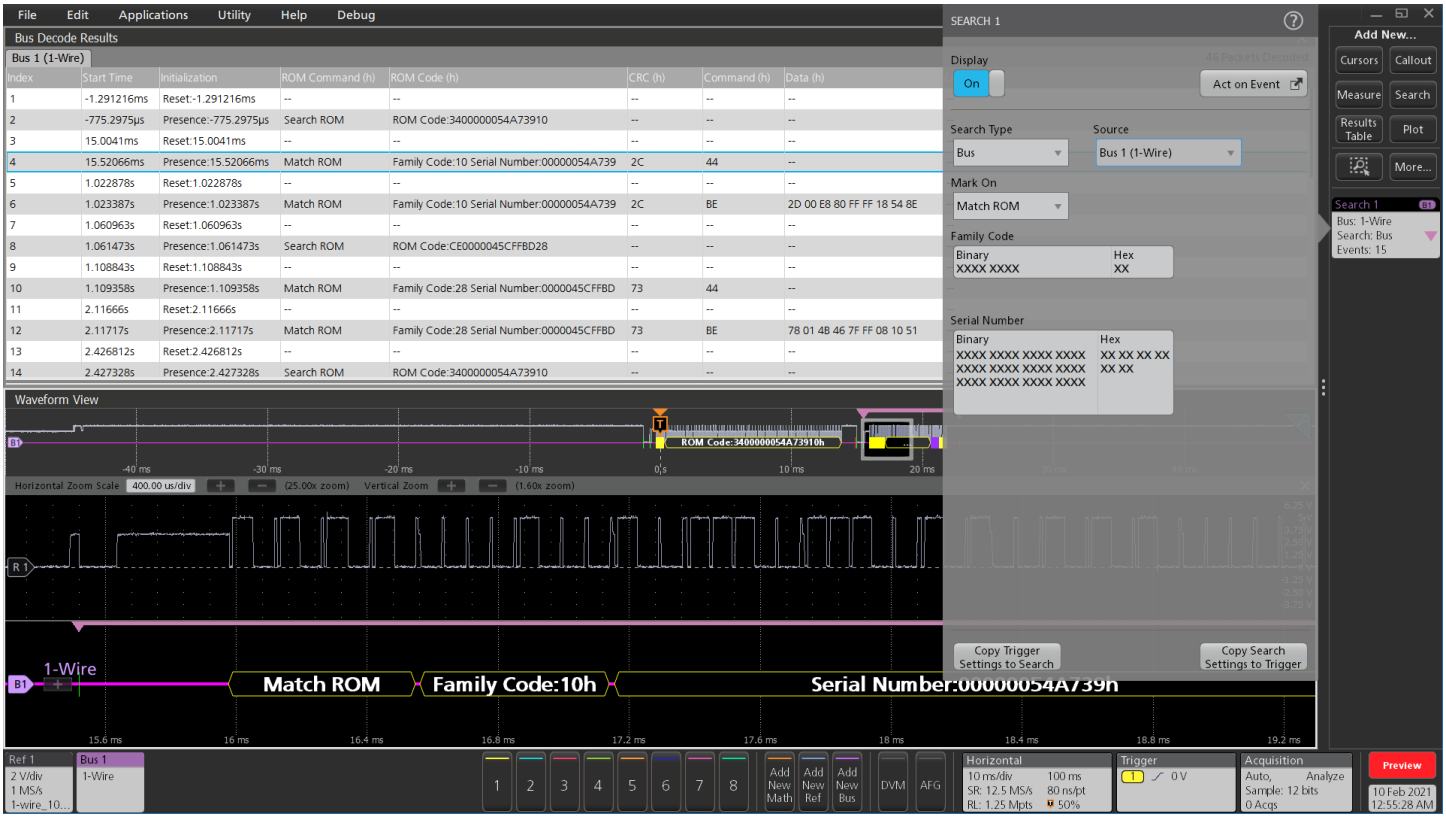
Characteristic	Description
Bus	Bus Only
Result Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On 1-Wire	Reset – Searches for the Reset event. Reset is the default trigger on condition. Presence – Searches for the Presence event. Command – Searches for Command. Data – Searches for the Data. Read ROM – Searches for the Family code and Serial number of Read ROM. Match ROM – Searches for the Family code and Serial number of Match ROM. Overdrive Match ROM – Searches for the Family code and Serial number of Match ROM. Skip ROM – Searches for Skip ROM packet. Overdrive Skip ROM – Searches for the Overdrive Skip ROM packet. Search ROM – Searches for the ROM code. Alarm Search – Searches for the Alarm packet. CRC Error specifies the search condition as CRC Error.



The protocol decode results table provides a time-stamped, tabular view of all captured pixel packets on the 1-Wire bus.



Searching on a MATCH ROM packet with Family Code and Serial Number on the 1-Wire bus.

128B132B Characteristics (Line Decode)

Bus setup options

Characteristic	Description
128B132B Sources	Analog channels Digital channels Active Math channels Active Reference channels
Threshold	Per-channel thresholds
Bit Rate	1-25 Gbps
Bit Order	Most Significant Bit (MSB), Least Significant Bit (LSB)
Symbol Size	Nibble, 1 byte, 2 bytes, 4 bytes, 8 bytes
Symbols	Nibble (8-32) 1 byte (4-16) 2 bytes (2-8) 4 bytes (1-4) 8 bytes (1-2)
Pattern	0, 1, or X
Formats Available	Hex Binary

Display modes

Characteristic	Description
Bus	Bus Bus and Waveforms
Result Table	Decoded packet data in a tabular view

Bus Search options

Characteristic	Description
Search On	▪ Symbols: Symbol, Pattern ▪ Sync Header: Pattern ▪ Error: Invalid Sync

128B132B bus showing decode with LSB. Sync Header was searched with 568 occurrences.

128B132B bus showing decode with MSB. Symbol was searched with 41 occurrences.

64B66B characteristics (Line decode)

Bus setup options

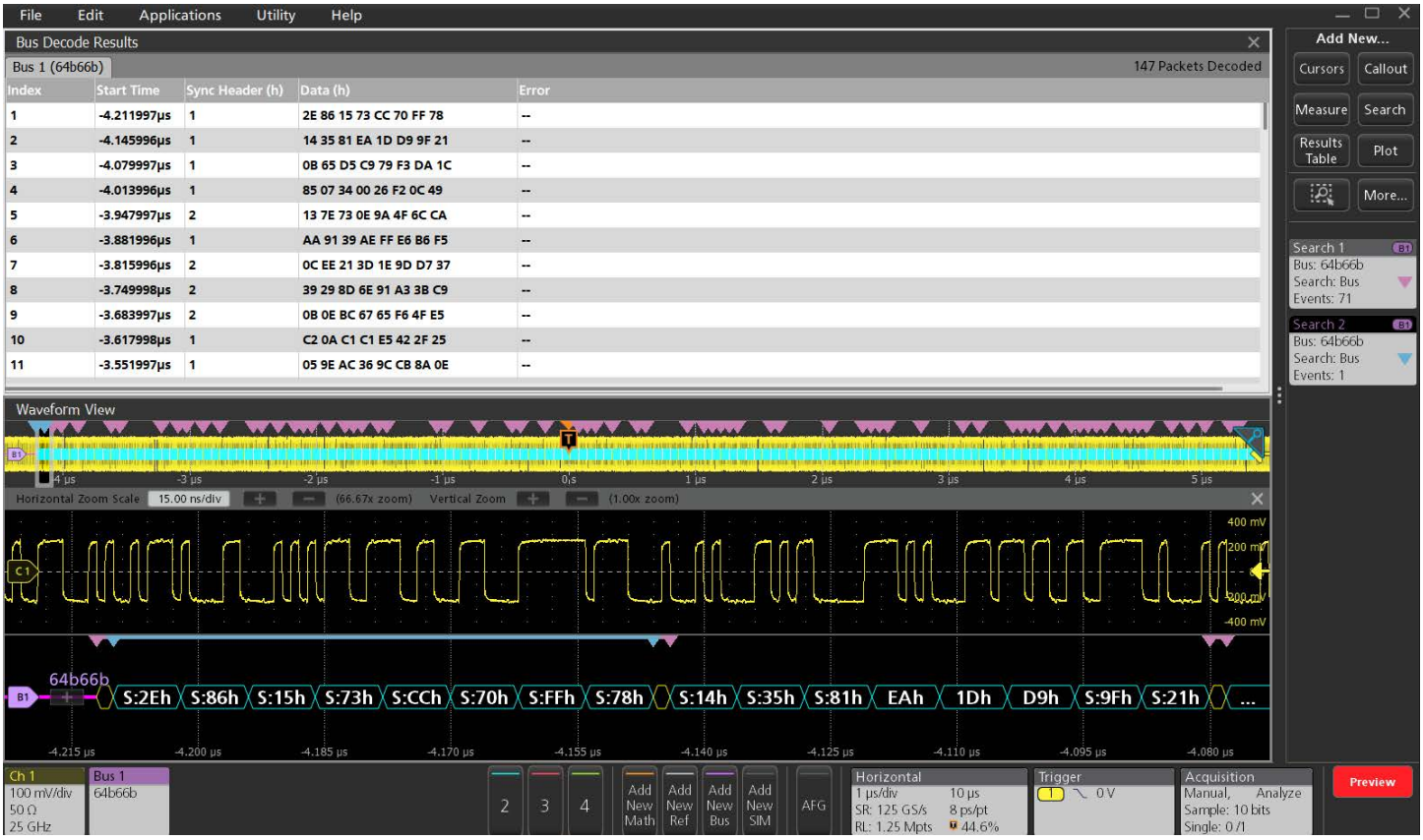
Characteristic	Description
64B66B Sources	Analog Channels Digital Channels Active Math Channels Active Reference Channels
Thresholds	Per-Channel Thresholds
Bit Rate	1-25 Gbps
Bit Order	Most Significant Bit (MSB) Least Significant Bit (LSB)
Symbol Size	Nibble, 1 byte, 2 bytes, 4 bytes, 8 bytes
Symbols	Nibble (8-16) 1 byte (4-8) 2 bytes (2-4) 4 bytes (1-2) 8 bytes (1)
Pattern	0, 1, or X
Formats Available	Hex Binary

Display modes

Characteristic	Description
Bus	Bus Bus and Waveforms
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	• Symbols: Symbol, Pattern • Sync Header: Pattern • Error: Invalid Sync



64B/66B line decode with MSB first. Sync Header searched with 71 occurrences marked in pink on the graticule. 1 occurrence of the symbol pattern 2E 86 15 73 CC 70 FF 78 in Hex marked in blue.

8B10B Characteristics (Line Decode)

Bus setup options

Characteristic	Description
8B10B Sources	Analog Channels Digital Channels Active Math Channels Active Reference Channels
Thresholds	Per-Channel Thresholds
Recommended Probing	Differential
Formats Available	Hex Binary Symbolic
Bit Rate	1b/s - 1Tb/s

Display modes

Characteristic	Description
Bus	Bus Only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus search options

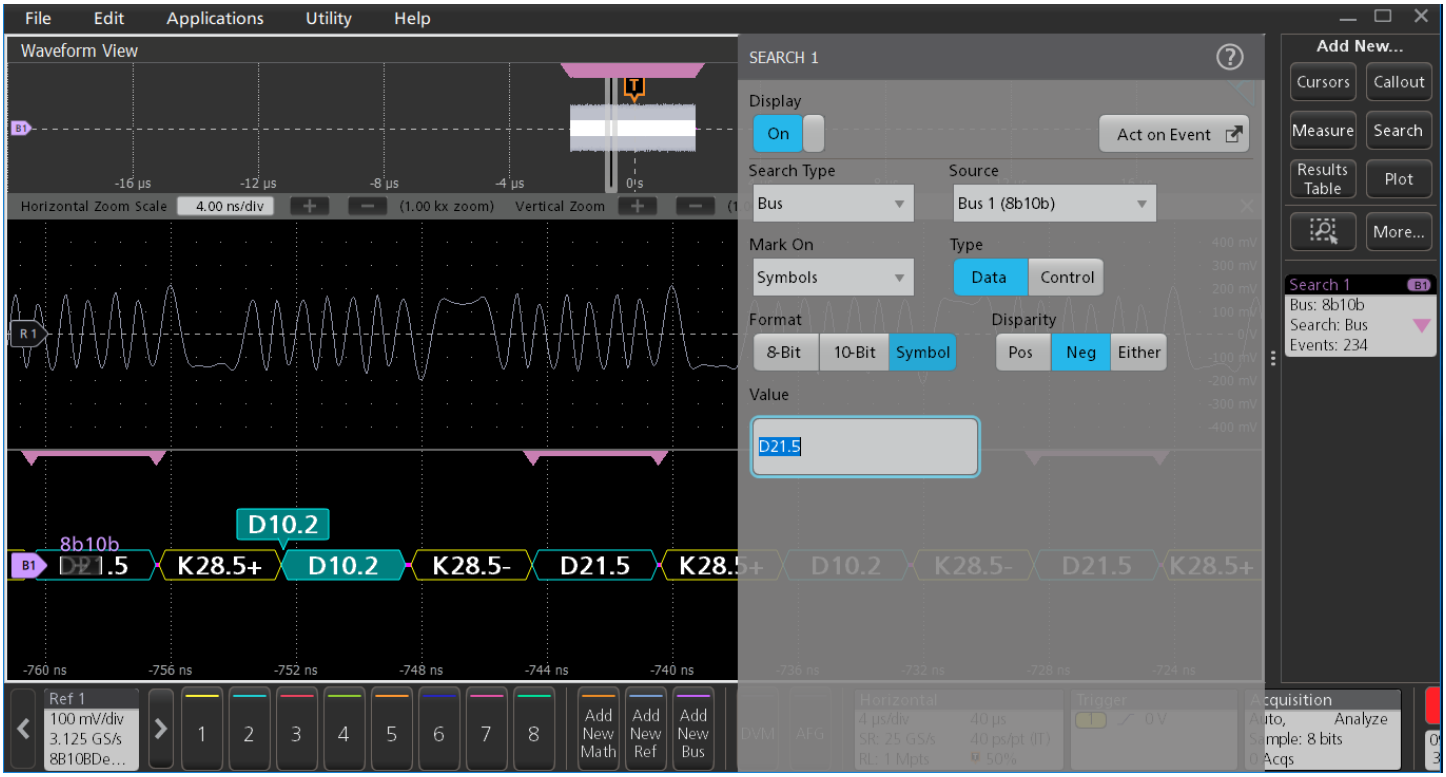
Characteristic	Description
Search On	Symbols [Format: 8bit, 10bit&Symbols] Errors: Symbol and Disparity

Bus decode

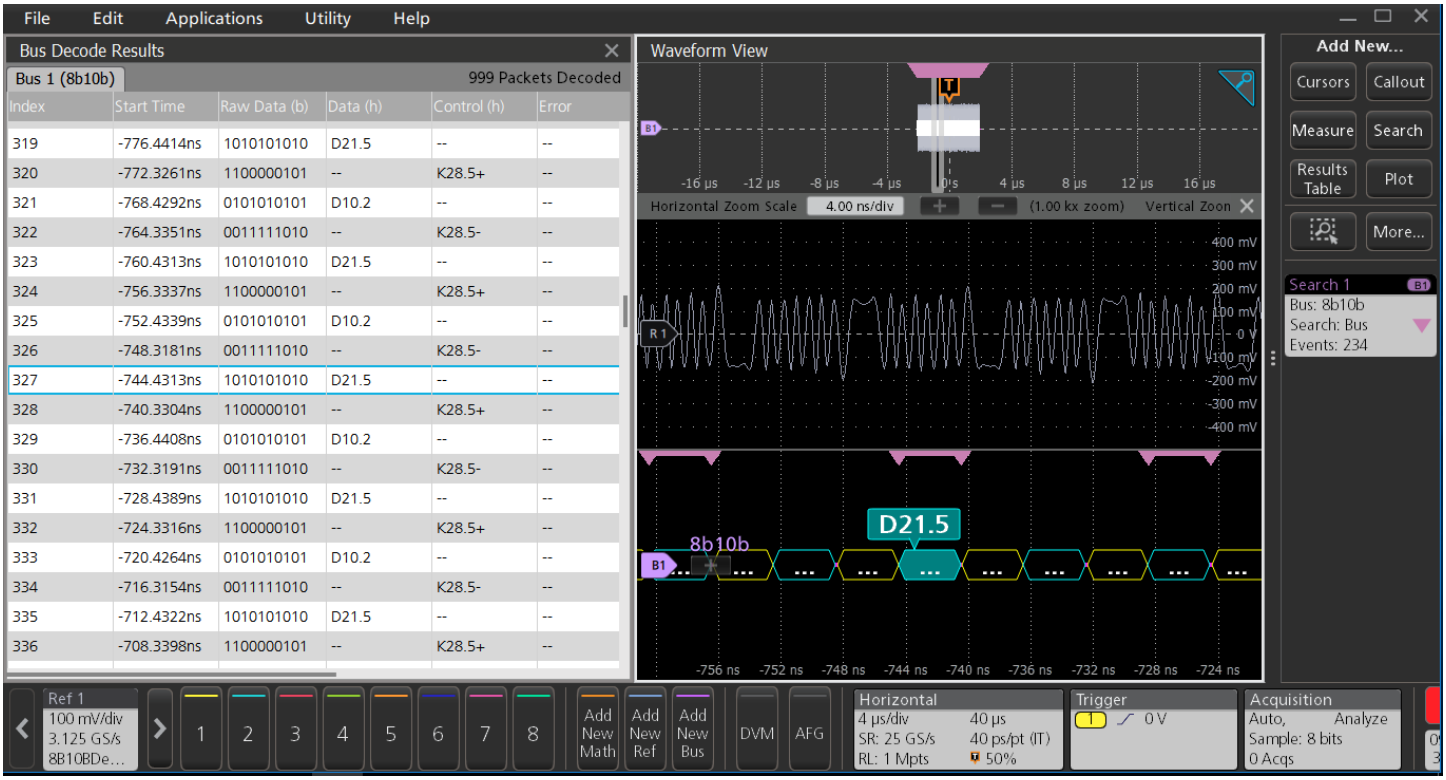
Characteristic	Description
Maximum Clock/Data Rate	1 Tbits/sec

Bus decode

Characteristic	Description
Decode Display	Control Symbol (yellow packet) Data Symbol (cyan packet)
Error Handling	Invalid Symbols Running Disparity (6 bit and 4 bit)



Searching on a specific data symbol in symbol format on a 8B10B bus.



The Protocol Decode results table provides time-stamped, tabular view of all captured packets on a 8B10B bus.

ARINC 429 characteristics (ARINC Specs 429 PART 1-17)

Bus setup options

Characteristic	Description
ARINC 429 Source	Analog channels Active Math channels Active Reference channels
Signal Type	Differential
Polarity	Normal Inverted
Thresholds	High and low thresholds
Recommended Probing	Differential
Bit Rate Selection: Predefined list of rates Custom	12.5 kb/s, 100 kb/s 10 kb/s - 1 Mb/s
Data Format	Data (19 bits) SDI+Data (21 bits) SDI+Data+SSM (23 bits)
Formats Available	Mixed Hex Hex Binary

Display modes

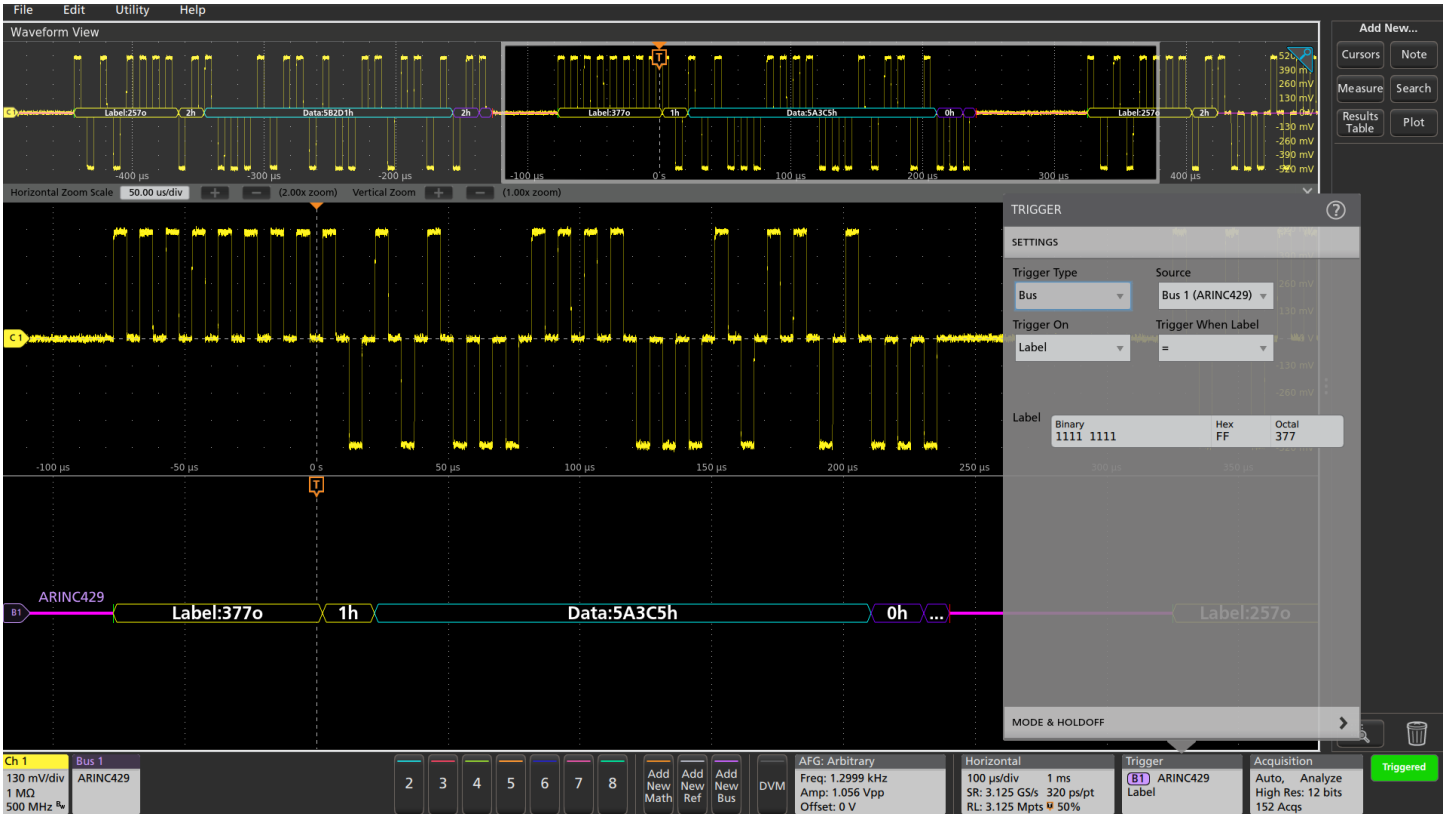
Characteristic	Description
Bus	Bus only
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

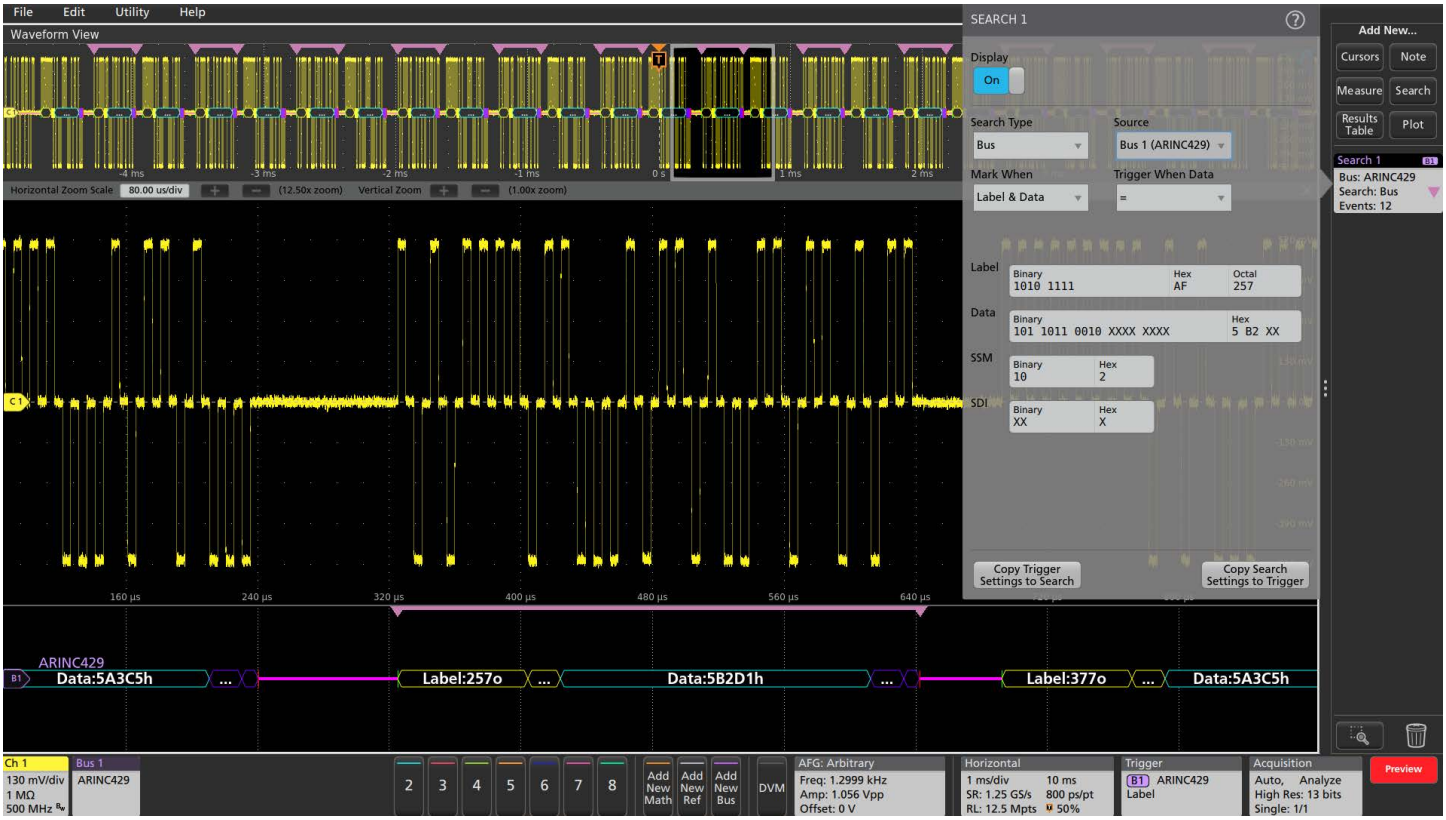
Characteristic	Description
Trigger and/or Search On	Word Start Label (when =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) Data (when =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) Label and Data (Label value and Data =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) Word End Error : Any Error, Parity Error, Word Error, Gap Error

Bus decode

Characteristic	Description
Decode Display	Start (green bracket) Label (yellow box) Source Destination Identifier (yellow box) Data (cyan box) Sign/Status Matrix (purple box) Parity (purple box) Stop (red bracket) Error (red box)



Decoded ARINC 429 bus, with the acquisition triggered on a specified label value.



Decoded ARINC 429 bus, with all data values in a specific range marked with pink brackets.

Audio characteristics

Bus setup options

Characteristic	Description
Audio Sources (Bit Clock, Word Select, Data)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Thresholds	Per-channel thresholds
Bit Clock Polarity	Rising Edge Falling Edge
Word Select Polarity	Normal Invert
Data Polarity	Active High Active Low
Word Size	4 - 32 bits
Formats Available	Hex Binary Signed Decimal

Display modes

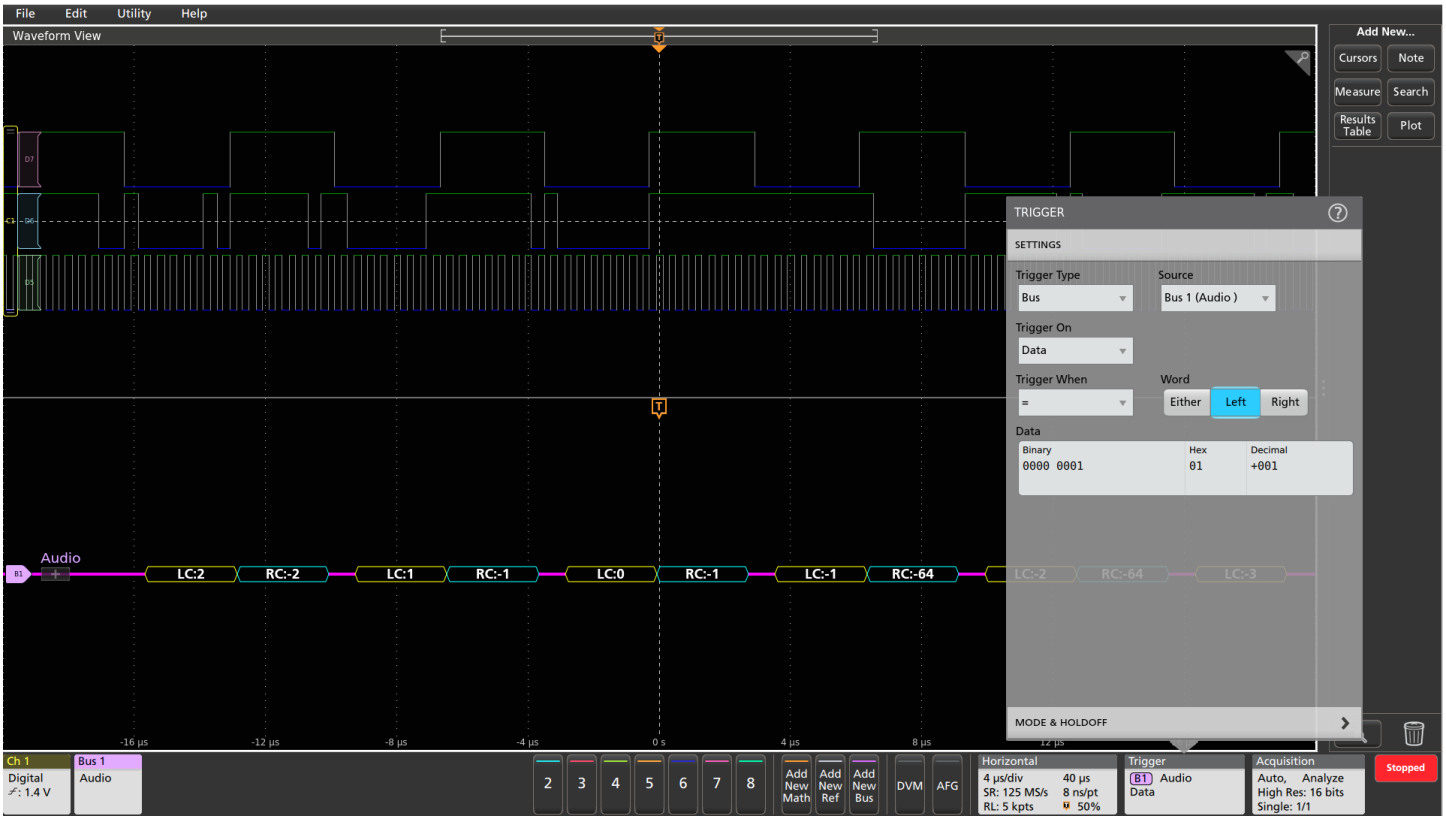
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Word Select (I^2S, LJ, RJ only) Frame Sync (TDM only) Data (when =, ≠, <, >, ≤, ≥, Inside Range, Outside Range; Left, Right, or Either Word)

Bus decode

Characteristic	Description
Maximum Clock/Data Rate	All but 3 Series MDO: Up to 10 Mb/s (for automated decoding of bus) 3 Series MDO: Up to 12.5 Mb/s (for automated decoding of I2S/LJ/RJ bus) 3 Series MDO: Up to 25 Mb/s (for automated decoding of TDM bus)
Decode Display	Left Channel Data (I^2S, LJ, RJ) (yellow box) Right Channel Data (I^2S, LJ, RJ) (cyan box) Channel 1 Data (TDM) (yellow box) Channel 2 - N Data (TDM) (cyan box)



Decoded I2S bus, with data values displayed in signed decimal format, and the MSO triggered on a specific data value.

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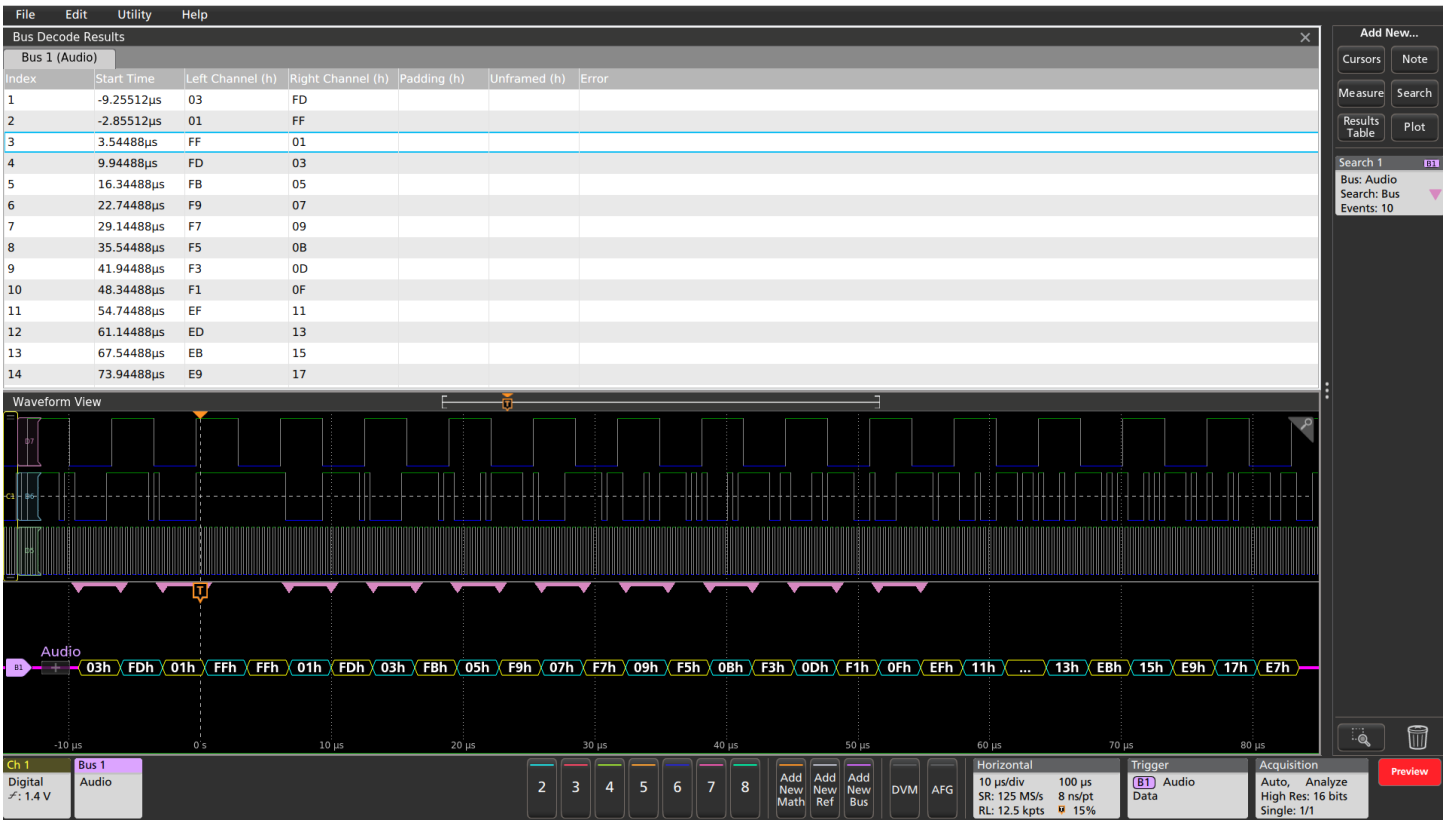


Decoded I2S bus, with data values displayed in hex and Results Table format, and the Wave Inspector automatic search marking all occurrences of the data values equal to 0X hex.

Automotive Ethernet 10BASE-T1S (Version- IEEE 802.3cg-2019) characteristics

Bus setup options

Characteristic	Description
10Base-T1S Sources	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel Thresholds
Probing	Single-ended Differential
Bit Rate	1 b/s - 1 Gb/s
Packet View	On Off
Formats Available	Hex Binary Mixed Hex

Display modes

Characteristic	Description
Bus	Bus only
Results Table	Decoded packet data in a tabular view

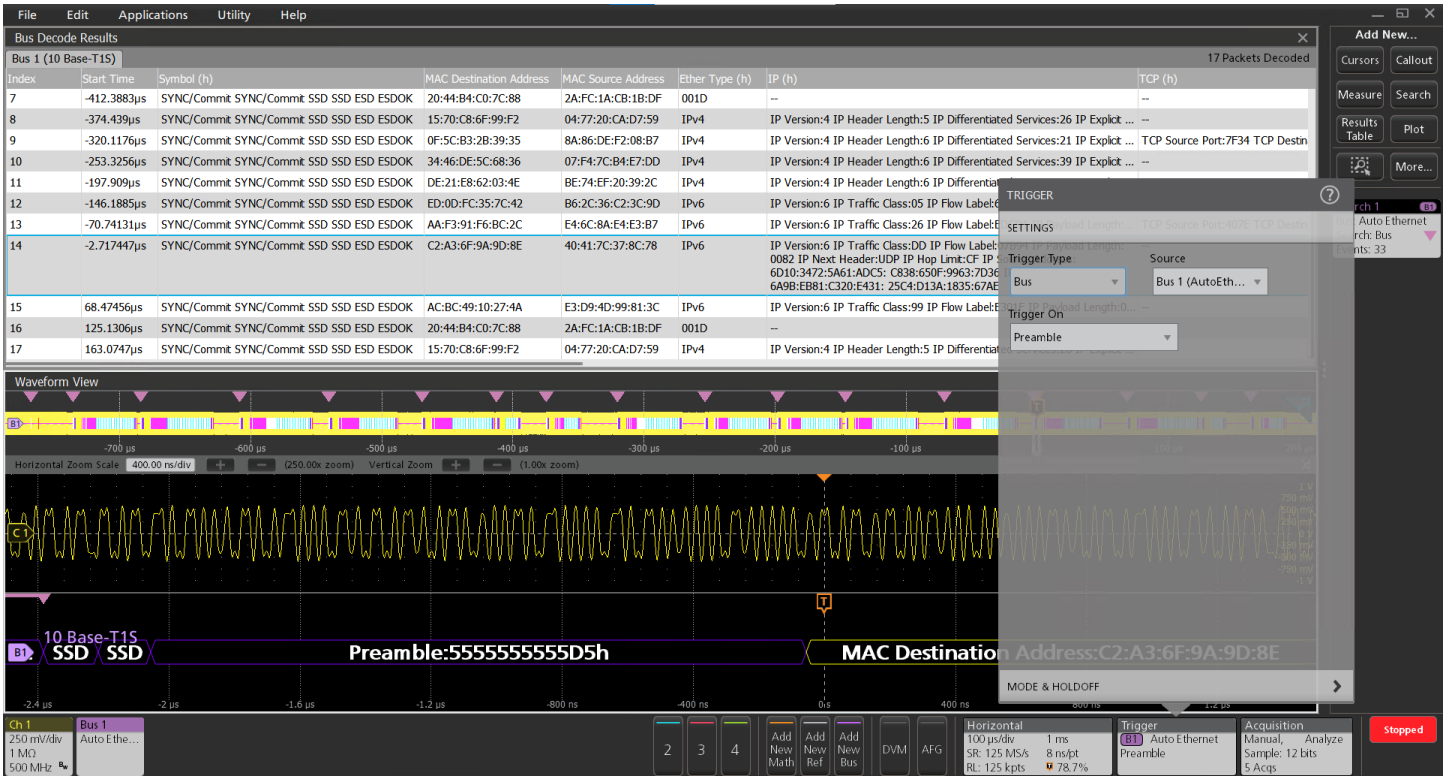
Bus search options

Characteristic	Description
Search On	SYNC/Commit Start of Stream Delimiter MAC address: Mac Address Source, Mac Address destination Beacon Ether Type: IPv4 and IPv6 with Header Type: TCP, UDP, ICMP Payload: Payload Bytes, Payload value Data: Data Bytes, Data End of stream Delimiter. End of stream Delimiter Ok Errors: CRC, ESDERR, ESDJAB, Checksum, Any

Bus trigger options

Characteristic	Description
Trigger On	SYNC/Commit Start of Stream Delimiter Preamble Beacon End of stream Delimiter End of stream Delimiter Ok Errors: ESDERR, ESDJAB

10BASE-T1S bus decode and results table in a tabular format. Searching the 10BASE-T1S bus decode with search on Sync/ Commit with 34 occurrences shown on the search badge.



10BASE-T1S bus decode with trigger on the preamble.

Automotive Ethernet (100BASE-T1) characteristics (Version BRR V3.2)

Bus setup options

Characteristic	Description
Ethernet Source(s)	Analog Channels Active Math Channels Active Reference Channels
Thresholds	Per-channel Thresholds
Recommended Probing	Differential
Formats Available	Mixed Hex Hex Binary Mixed ASCII

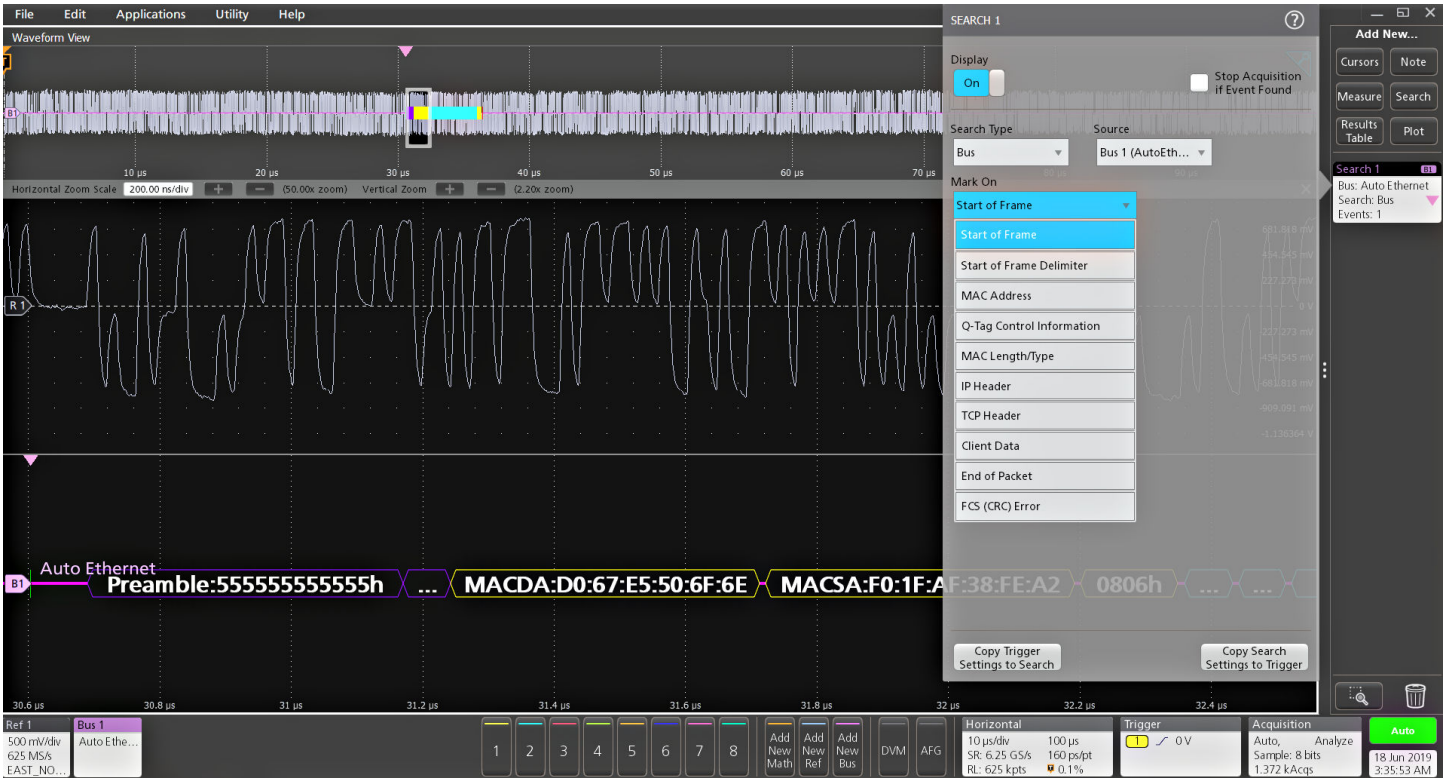
Display modes

Characteristic	Description
Bus	Bus Only
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	Start of Frame Start of Frame Delimiter MAC Addresses Q-Tag Control Information MAC Length/Type IPv4 Header TCP Header MAC Data TCP-IPv4 Client Data End of Packet Frame Check Sequence (CRC) Error

The Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the Automotive Ethernet (100BASE-T1) bus.



Searching on a specific data pattern on the Automotive Ethernet (100BASE-T1) bus and automatically searching on Start of Frame.

CAN characteristics (Version 2.0)

Bus setup options

Characteristic	Description
Source for CAN_H, CAN_L, Rx, or Tx (single-ended probing)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Source for Diff (differential probing)	Analog channels Active Math channels[1] Active Reference channels[1]
Thresholds	Per-channel thresholds
Recommended Probing: CAN_H, CAN_L, Rx, Tx Diff	Single-ended Differential
Bit Rate Selection: Predefined list of rates Custom	10 kb/s - 1 Mb/s All but 3 Series MDO: 1 kb/s - 1 Mb/s 3 Series MDO: 10 kb/s - 1 Mb/s
Sample Point	All but 3 Series MDO: 0% - 100% of bit period of unit interval 3 Series MDO: 5% - 95% of bit period of unit interval
Formats Available	Mixed Hex Hex Binary Symbolic (.dbc)[1]

Display modes

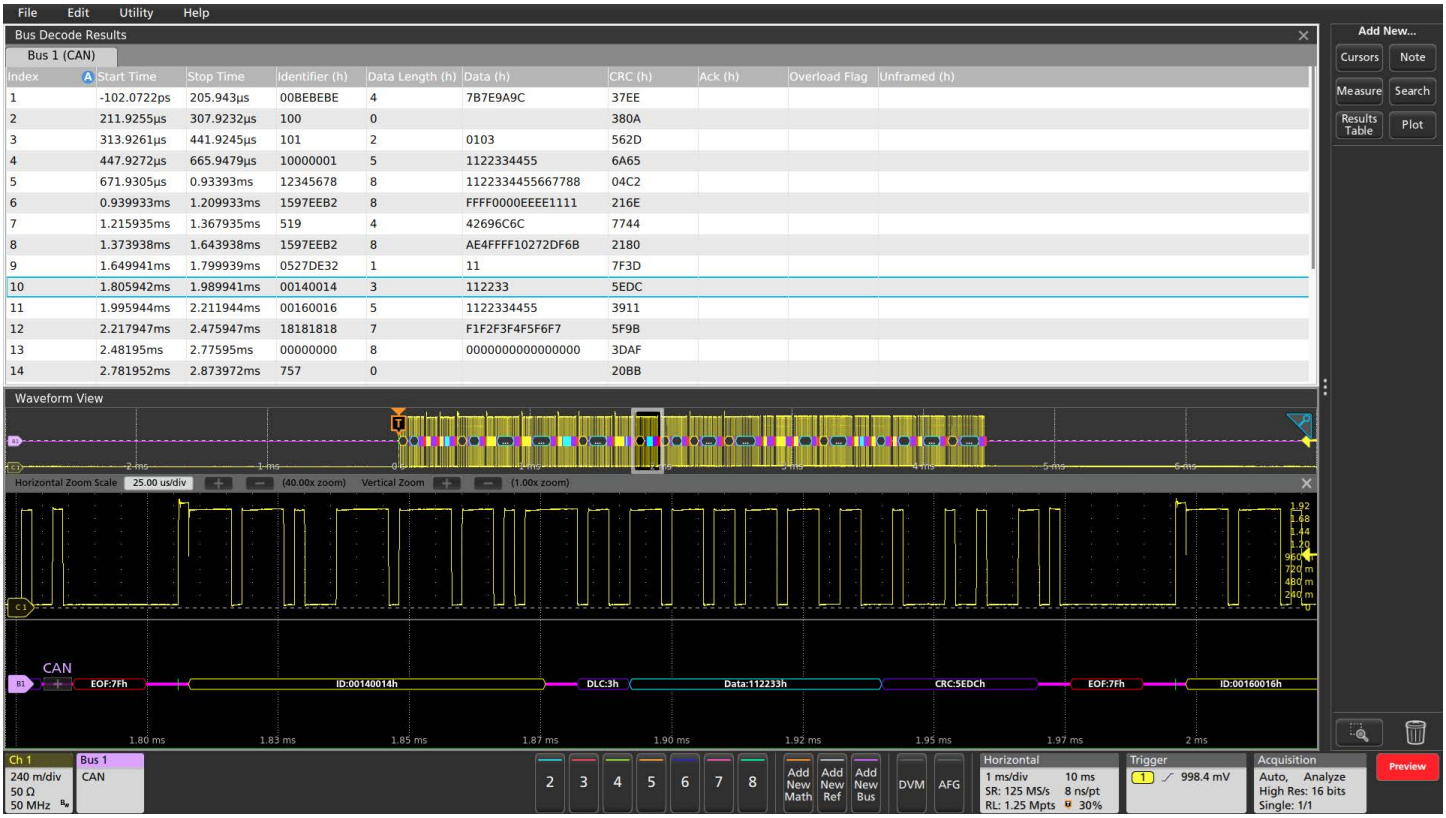
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Start of Frame Type of Frame (Data, Remote, Error, Overload) Identifier (Standard or Extended) Data (number of bytes 1-8, trigger or search when =, ≠, <, ≤, >, ≥) Identifier and Data EOF Missing Ack Bit Stuff Error

Symbolic bus search options

Characteristic	Description
Message	As defined by the .dbc file[1]
Message and Signal	As defined by the .dbc file[1]



Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the CAN bus.

Triggering on a specific extended Identifier value on the CAN bus.

CAN FD (ISO and non-ISO) characteristics

Bus setup options

Characteristic	Description
Source for CAN_H, CAN_L, Rx, or Tx (single-ended probing)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Source for Diff (differential probing)	Analog channels Active Math channels[1] Active Reference channels[1]
Thresholds	Per-channel thresholds
Recommended Probing: CAN_H, CAN_L, Rx, or Tx Diff	Single -ended Differential
Version	ISO non-ISO
SD Bit Rate Selection: Predefined list of rates Custom	10 kb/s - 1 Mb/s All but 3 Series MDO: 50 kb/s - 10 Mb/s 3 Series MDO: 10 kb/s - 1 Mb/s
FD Bit Rate Selection: Predefined list of rates Custom	All but 3 Series MDO: 1 Mb/s - 16 Mb/s 3 Series MDO: 1 Mb/s - 7 Mb/s All but 3 Series MDO: 500 kb/s - 16 Mb/s 3 Series MDO: 500 kb/s - 7 Mb/s
Sample Point	All but 3 Series MDO: 55% - 95% of bit period of unit interval 3 Series MDO: 15% - 95% of bit period of unit interval
Formats Available	Mixed Hex Hex Binary Symbolic (.dbc)[1]

Display modes

Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

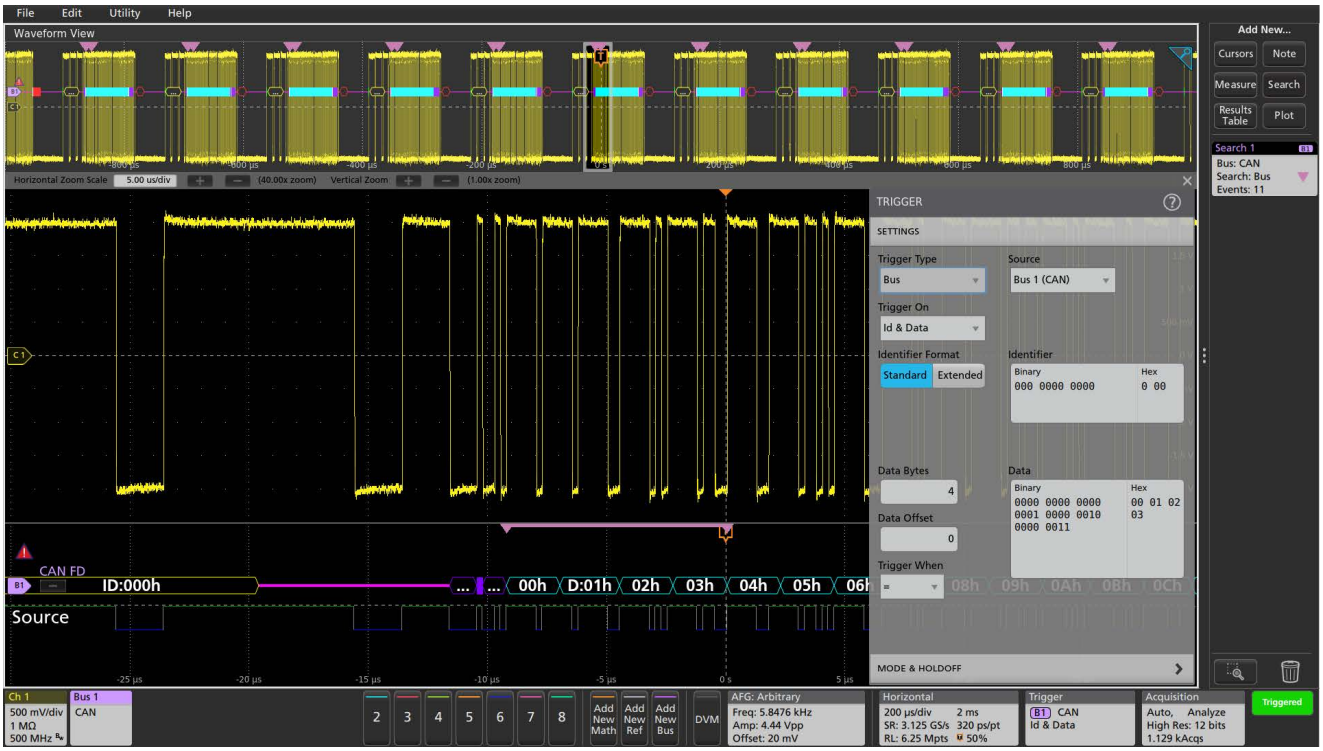
Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Start of Frame Type of Frame (Data, Remote, Error, Overload) FD Bits (Bit Rate Switch bit, Error State Indicator bit) Identifier (Standard or Extended) Data (1-8 bytes, trigger or search when =, ≠, <, ≤, >, ≥) Identifier and Data End of Frame Error (Missing Ack, Bit Stuffing Error, FD Form Error, Any Error)

Symbolic bus search options

Characteristic	Description
Message	As defined by the .dbc file[1]
Message and Signal	As defined by the .dbc file[1]

Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the CAN FD bus.



Triggering on a specific Identifier value and data pattern on the CAN FD bus and automatically searching on the same data pattern.

CAN XL characteristics

Bus setup options

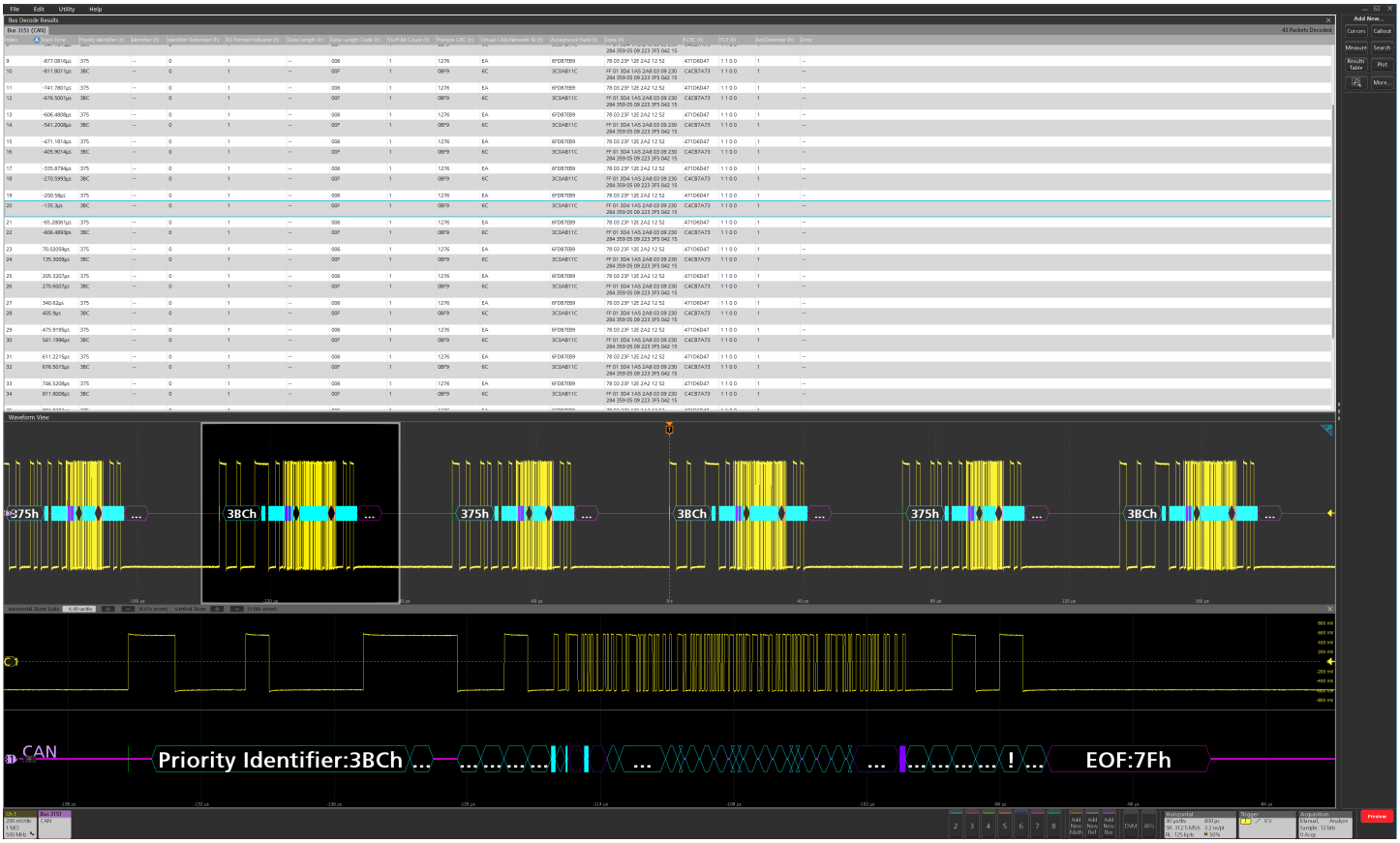
Characteristic	Description
Source for CAN_H, CAN_L, Rx, or Tx (Single-ended probing)	Analog channels Digital channels Active Math channels Active Reference channels
Source for Diff (Differential probing)	Analog channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Recommended Probing:	
CAN_H, CAN_L, Rx, Tx Diff	Single-ended Differential
Nominal Bit Rate Selection:	
Predefined list of rates Custom	10 kb/s - 1 Mb/s 50 kb/s - 1 Mb/s
XL Bit Rate Selection:	
Predefined list of rates Custom	1 Mb/s - 20 Mb/s 500 kb/s - 20 Mb/s
Sample Point	55% - 95% of bit period of unit interval
Formats Available	Mixed Hex Hex Binary

Display modes

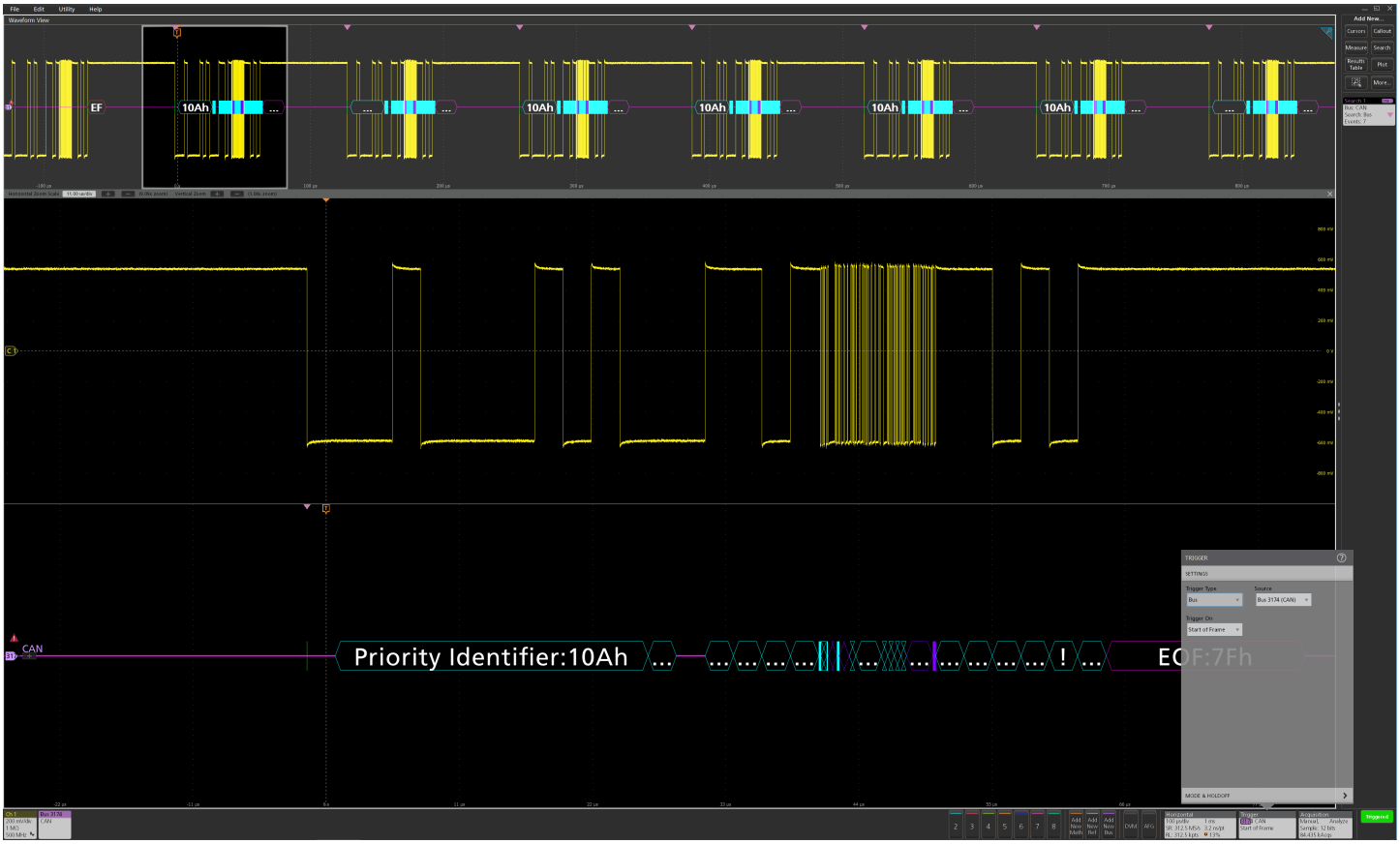
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger On	Start of Frame End of Error
Search On	Start of Frame Type of Frame (XL Data Frame) Priority Identifier Data (1 byte) XL Bits (Acceptance Field, Virtual CAN Network ID, SDU Type, Simple Extended Content, Stuff Bit Count, Arbitration to Data Sequence, Data to Arbitration Sequence) ADS Type (Arbitration to Data High Bit, Data High Bit 1, Data High Bit 2, Data Low Bit) DAS Type (DAH, Active High 1, Active High 2, Active Low 1) End of Frame Error (Missing Ack, XL Form Error, CRC, Any Error) CRC Type (PCRC, FCRC)



Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the CAN XL bus.



Triggering on Start of Frame on the CAN XL bus and searching on it.

CPHY (CSI2.0/DSI 2.0) characteristics (version 1.2)

Bus setup options

Characteristic	Description
CPHY Sources	Analog channels Digital channels Math channels Active reference channels
Salient Features	Decode capability in for CSI/DSI protocols. Decode capability for Escape mode. Decode capability for High speed burst mode. Decode capability for Word/Symbol Mode. Decode capability in single ended and differential mode Search capability for SoT/EoT Search capability for long and short packets Search capability for Escape mode Search capability for CRC Errors Search capability on Pixel value and Pixel number in CSI/DSI packet search
Sub Type	CSI DSI Word (16 Bit data word decode) Symbol (Symbol level decode of cphy data)
Signal Type	Single Ended: No. of probes: 3 Differential: No. of probes: 5 Minimum BW of probe: As minimum bit rate of HS is set to 4 Mbps, almost all probe should work. But considering the general CPHY HS speed is about 1 GHz and speed can vary depending on customer, the probe need to based on what speed the end customer want to test.
Formats Available	Hex Binary Mixed Hex
Bit Rate	4 Ms/s - 10 Gs/s

Display modes

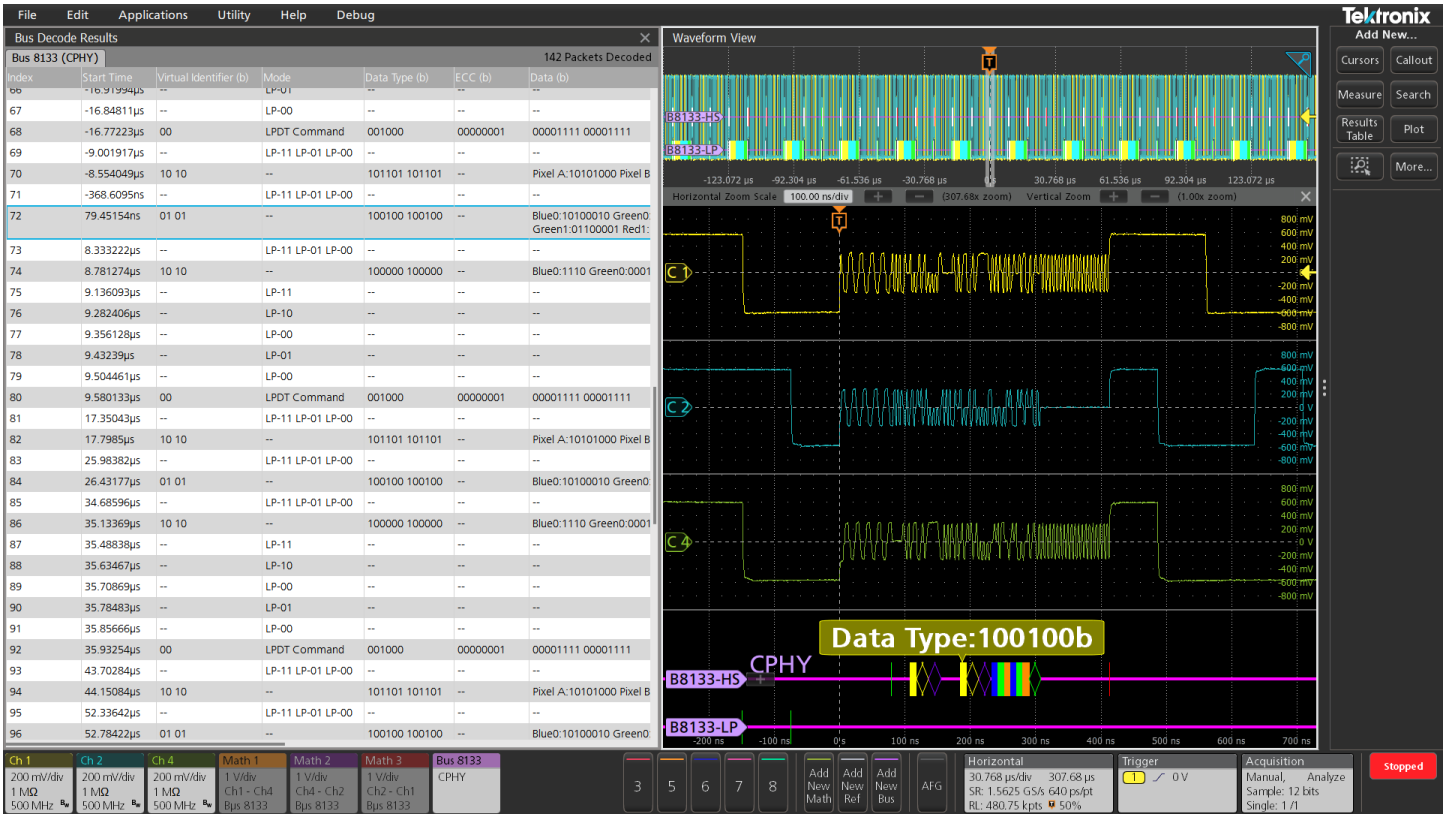
Characteristic	Description
Bus	Bus Only
Result Table	Decoded packet data in a tabular view

Bus decode

Characteristic	Description
Maximum Clock/Data Rate	10 Gbps
Decode Display	Control Field (yellow) ECC/CRC (Green) Pixel Fields (Red, Green, Blue, Yellow) Data Symbol (cyan) Raw Fields (Cyan) Word and Symbol Decode (cyan)
Error Handling	PHCRC CRC SOT Sync
Sub type	CSI (CSI packet decode) DSI (DSI packet decode in HS/LP) Word (16 bit word decode) Symbol Decode

Bus search options

Characteristic	Description
Search On (CSI/DSI)	SoT – Searches SoT of each transmission in HS mode EoT – Searches EoT of each transmission in HS mode. Data – Data search (HS/LP) Scrambling – Search for scrambling mode command Compression – Search for Compression mode command.
	**Packets – Searches for Short and long packets Escape – Search for Escape entry mode Errors – Search for CRC and PHCRC errors. **Can select from the list of standard packet names
Word / Symbols Decode	Search for Words/Symbols respectively



The protocol decode results table provides a time-stamped, tabular view of all captured pixel packets on the CPHY bus.

CPHY search results display.

CXPI characteristics (Version: JASO D 015-3: 2014/J3076_201510)

Bus setup options

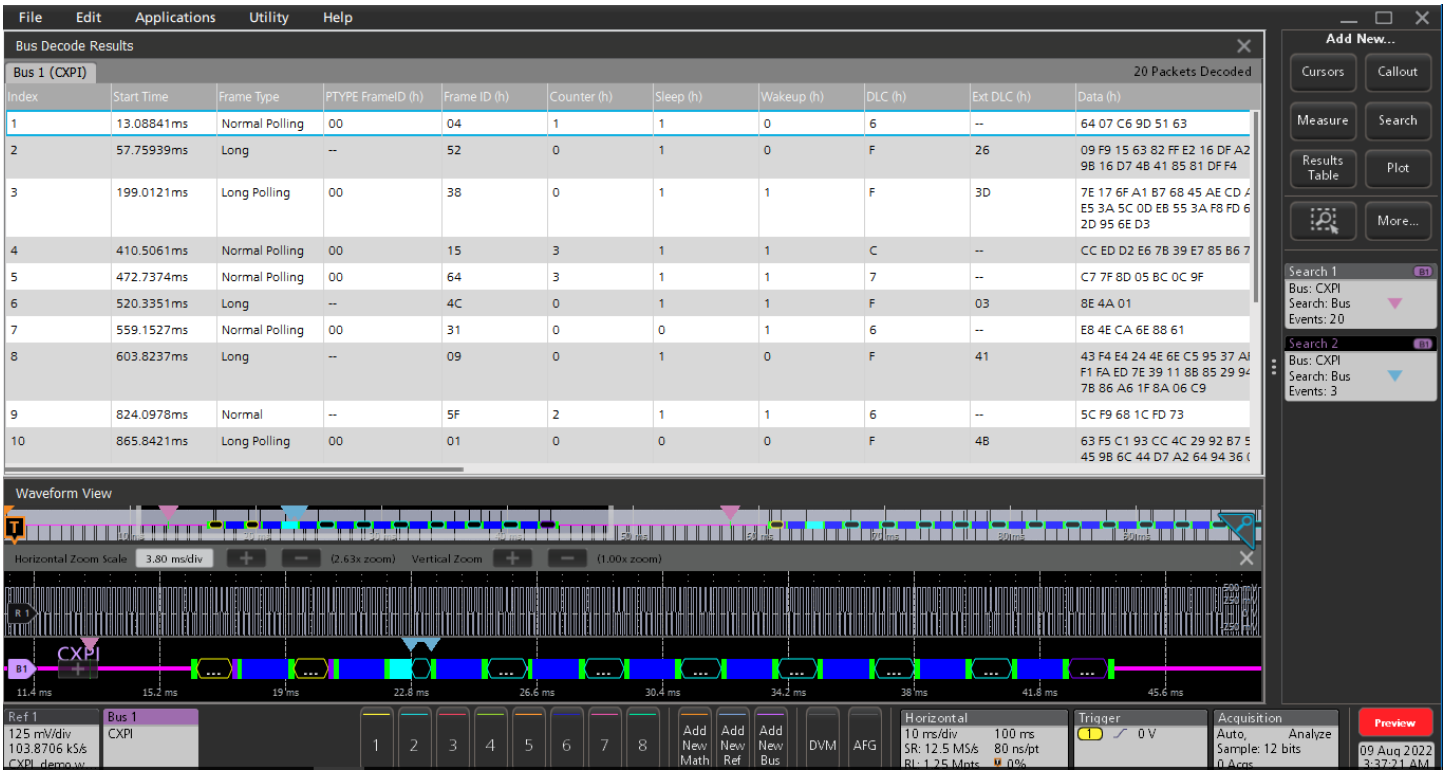
Characteristic	Description
CXPI sources (signal source)	▪ Analog channels- 1 ▪ Active Reference channels- 1 ▪ Digital channels ▪ Math channels
Recommended Probes	It is a low speed protocol with voltage between 1.8 V-3.3 V ▪ Active Probes P7240 ▪ TPP1500 ▪ Low Voltage Single Ended Probes
Product differentiator	Display IBS bits on decoded bus for Inter byte spacing clarity.
Salient features	CXPI source has recessive threshold level for signal decode. i.e. TH(rec) is 70% peak-to-peak of the signal. Transmitting node transmits data to the communication bus, it transmits to encoding circuit after converting the data to UART format.
Formats available	Hex Binary Mixed Hex
Bit rate	1b/s - 20kb/s

Display modes

Characteristic	Description
Bus	Bus only
Result table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	▪ Start ▪ Frame ▪ Frame ID ▪ PTYPE ▪ DLC ▪ ExtDLC ▪ Network management: Wakeup and sleep ▪ Counter ▪ Data ▪ Errors: Parity, CRC, IBS, Frame.



The Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets with frame type along with supported errors on the CXPI bus.

Searching on a DLC field in packets with value 6(110) on the CXPI bus.

DPHY(DSI2.0/CSI2.0) characteristics (version 1.2)

Bus setup options

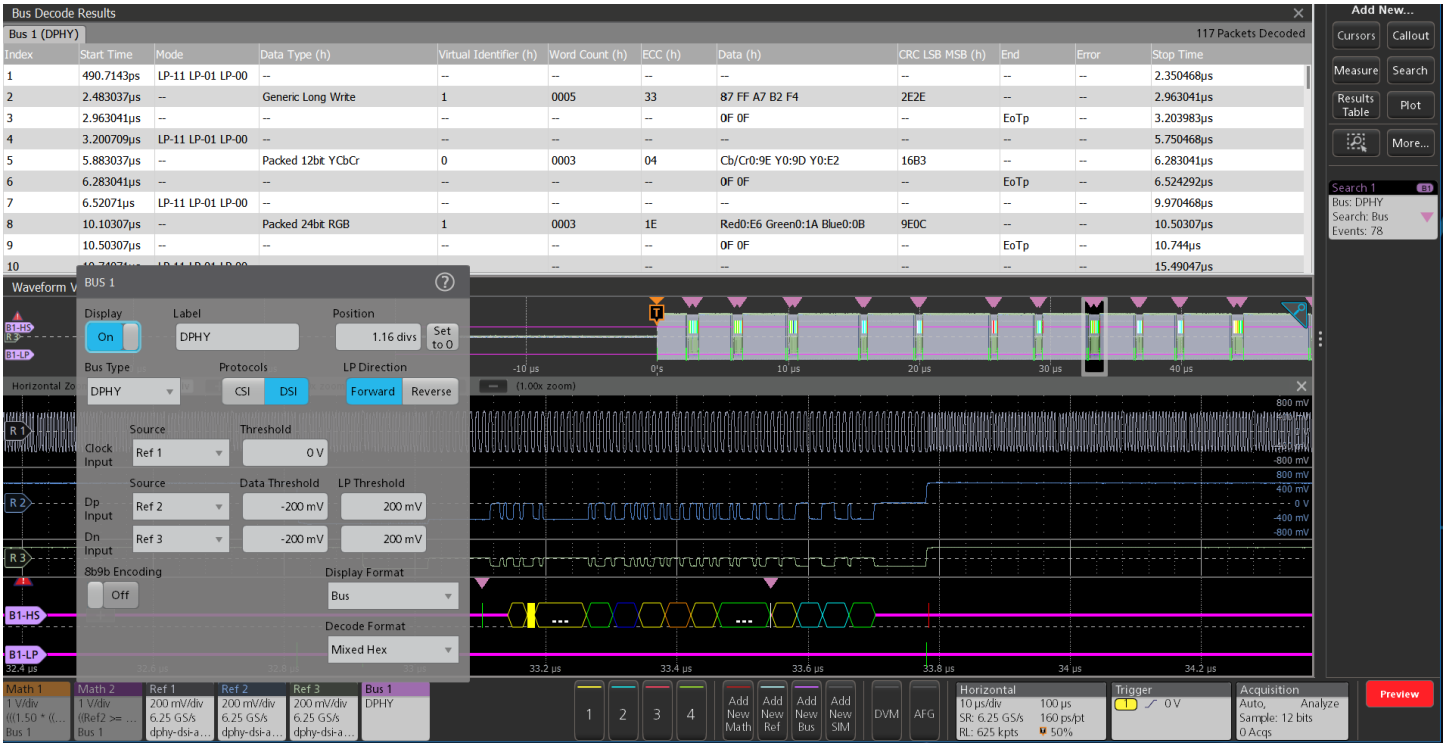
Characteristic	Description
DPHY Sources	Analog channels Math channels Active Reference channels
Salient Features	Decode capability in for CSI/DSI protocols. Decode capability for Escape mode. Decode capability for High speed burst mode. Decode capability for 8b9b line encoding in LPDT and HS mode. Search capability for SoT/EoT Search capability for long and short packets Search capability for Escape mode Search capability for Errors like ECC, CRC, and Any
Protocols	CSI and DSI
Recommended Probing	Clock – Single Ended/Differential Data – Single Ended Single ended probe: No. of probes: 3 (D+ and D- by default) Differential probe: No. of probes: Not supported
8b9b encoding mode	Select line encoding in LPDT and HS mode.
Formats Available	Hex Binary Mixed Hex

Display modes

Characteristic	Description
Bus	Bus Only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Result Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On (CSI/DSI)	SoT – It searches SoT of each transmission in HS mode EoT – It searches EoT of each transmission in HS mode. Data – Data search (HS/LP) Scrambling – Search for scrambling mode command Compression – Search for Compression mode command. **Packets – Searches for Short and long packets Escape – Search for Escape entry mode STOP – Search for Escape mode exit Errors – Search for CRC and ECC errors. **Can select from the list of standard packet names



DPHY bus decode and results table in a tabular format. Searching the DPHY bus decode with search on SOT with 78 occurrences shown on the search badge.

Enhanced serial peripheral interface (eSPI) characteristics (Version 1.0)

Bus setup options

Characteristic	Description
eSPI Sources	▪ Analog channels ▪ Digital Channels ▪ Active Math channels ▪ Active Reference channels
Salient features	▪ Decode capability for eSPI protocol. ▪ Decode capability for Single I/O mode with Alert as optional. ▪ Decode capability for Dual I/O mode with Alert as optional. ▪ Search capability for Start and End events ▪ Search capability for Status and Wait state ▪ Search capability for different channels: Channel Independent, Peripheral, OOB, Virtual Wire, and Flash Access based on command or response phase. Further, search capability for Command phase based on different channel related command opcodes and Response phase based on with/without header. Both phases support sub field search based on corresponding cycle type. Search capability for Errors based on the phase: CRC/Cycle type/Command opcode/Defer/Fatal/Non-Fatal/No Response.
Formats Available	Hex Binary Mixed Hex
I/O Mode	Specifies the mode of operation: ▪ Single mode (CMD and RSP on different lanes) ▪ Dual Mode (CMD and RSP on same lane)
Alert	Optional Alert channel- off by default
Polarity	Specifies the polarity of the input sources
Channels required for decode	4+1 (Clock, Chip Select, Command Input, Response Input + Alert)

Bus setup options

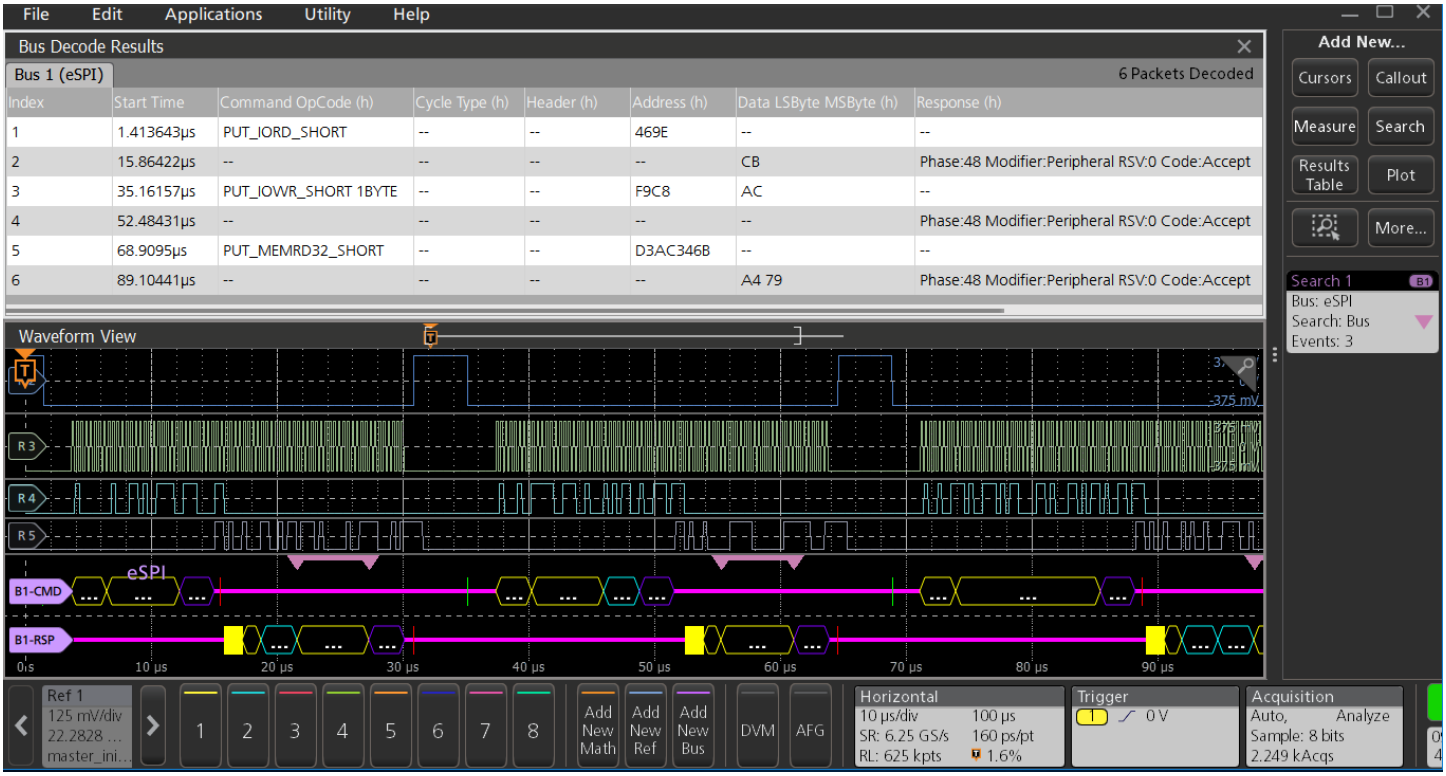
Characteristic	Description
Recommended Probes	It is a low speed protocol with voltage between 1.8 V-3.3 V ▪ Active Probes P7240 ▪ TPP1500 ▪ Low Voltage Single Ended Probes
Differentiators	▪ Protocol Search options (additional search options available under protocol decode): • Start and End Events • Wait States • Data • Errors – Invalid command type, Invalid cycle type, Fatal/Non-Fatal Errors. ▪ Decode formats in Mixed Hex.

Display modes

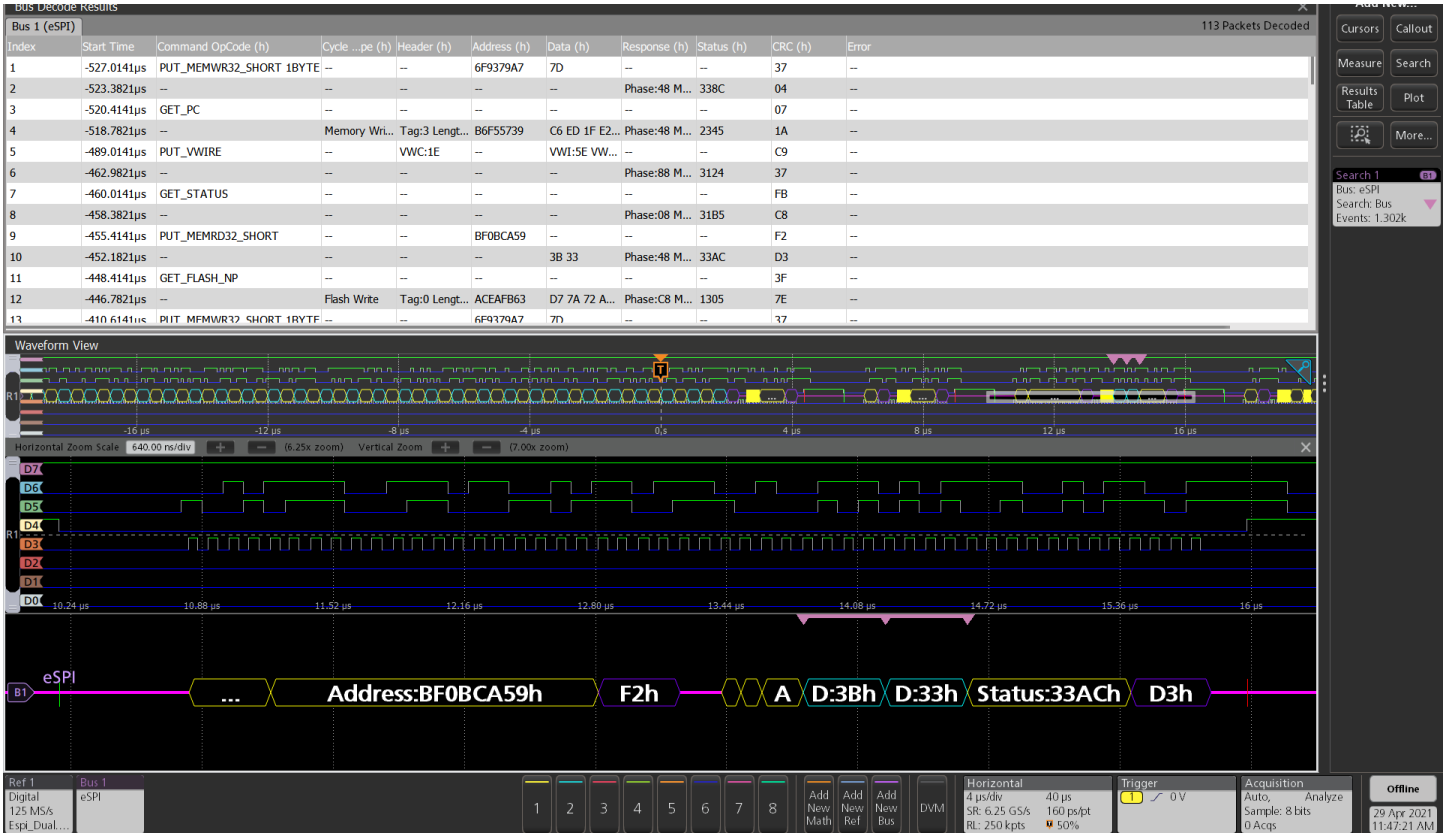
Characteristic	Description
Bus	Bus only
Result Table	Decoded packet data in a tabular view

Bus search options

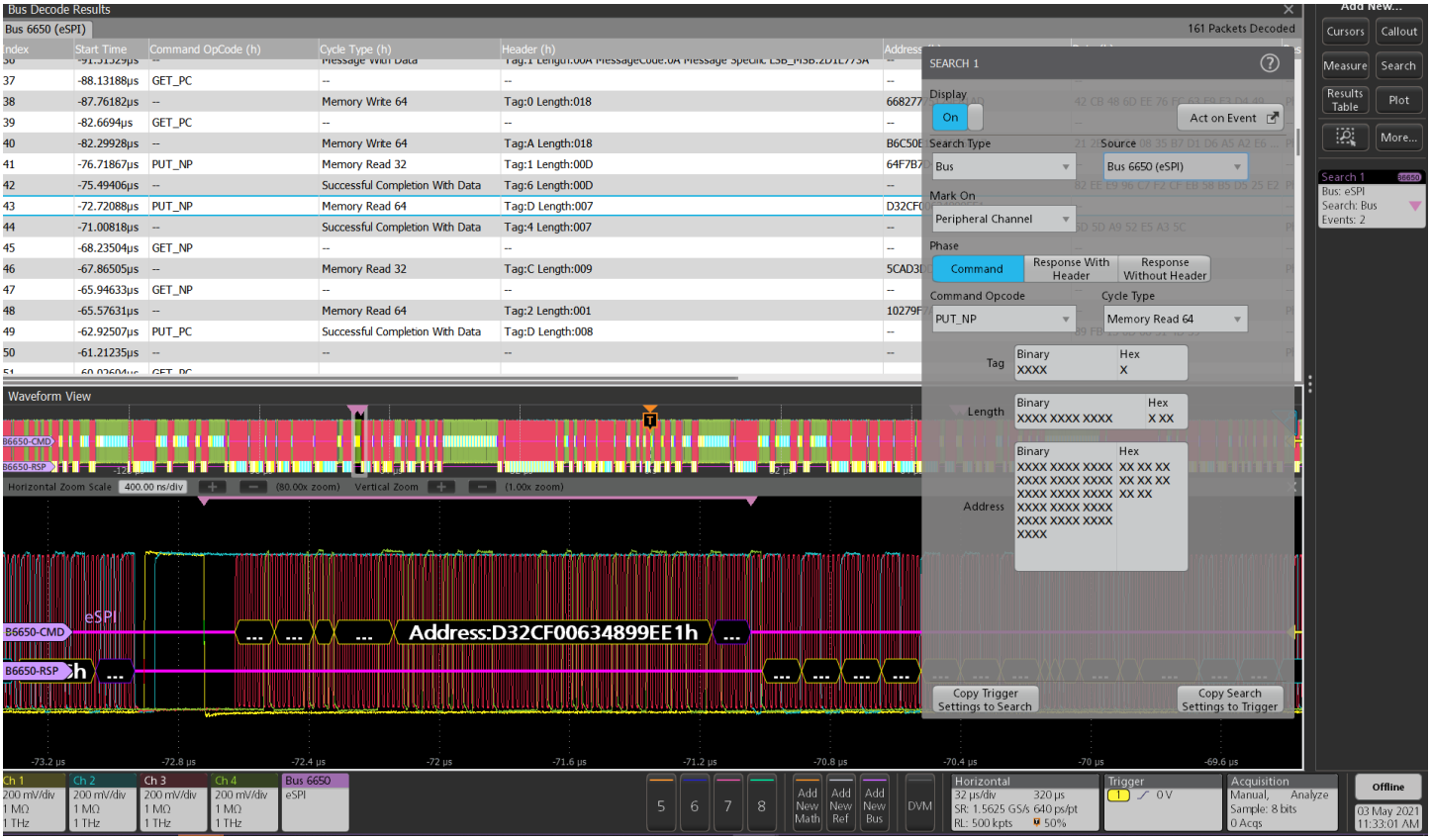
Characteristic	Description
Search On eSPI	**Start**: Enables to search the start event of the packet decode. **Channel Independent**: Enables search on Channel Independent command and responses packets. **Peripheral Channel**: Enables search on different types of Peripheral channel command and responses packets. **OOB Channel**: Enables search on different Out-Of-Band (OOB) channel command and Responses packets. **Virtual Wire Channel**: Enables search on different Virtual Wire channel command and responses packets. **Flash Access Channel**: Enables search on different Flash access channel command and responses packets. **Data**: Sets the data value for which to search. Searches based on command and response. **Status**: Enables search on the status field of the response packets. **Error Type**: Sets the error type for which to search based on command or response phase. **Wait**: Enables to search on the wait state that appears after the TAR window. **End**: Enables to search on the End events when the packet decode ends.



The Protocol Decode Results Table provides a time-stamped, tabular view of all captured pixel packets on the eSPI bus. (Single I/O Mode).



The Protocol Decode Results Table provides a time-stamped, tabular view of all captured pixel packets on the eSPI bus. (Dual I/O Mode).



Searching on a Peripheral Channel packet with command OpCode as PUT_NP and cycle type as Memory Read 64 on the eSPI bus. (Single I/O Mode).

Searching on the Start/End event on the eSPI bus (Dual I/O Mode).

EtherCAT characteristics

Bus setup options

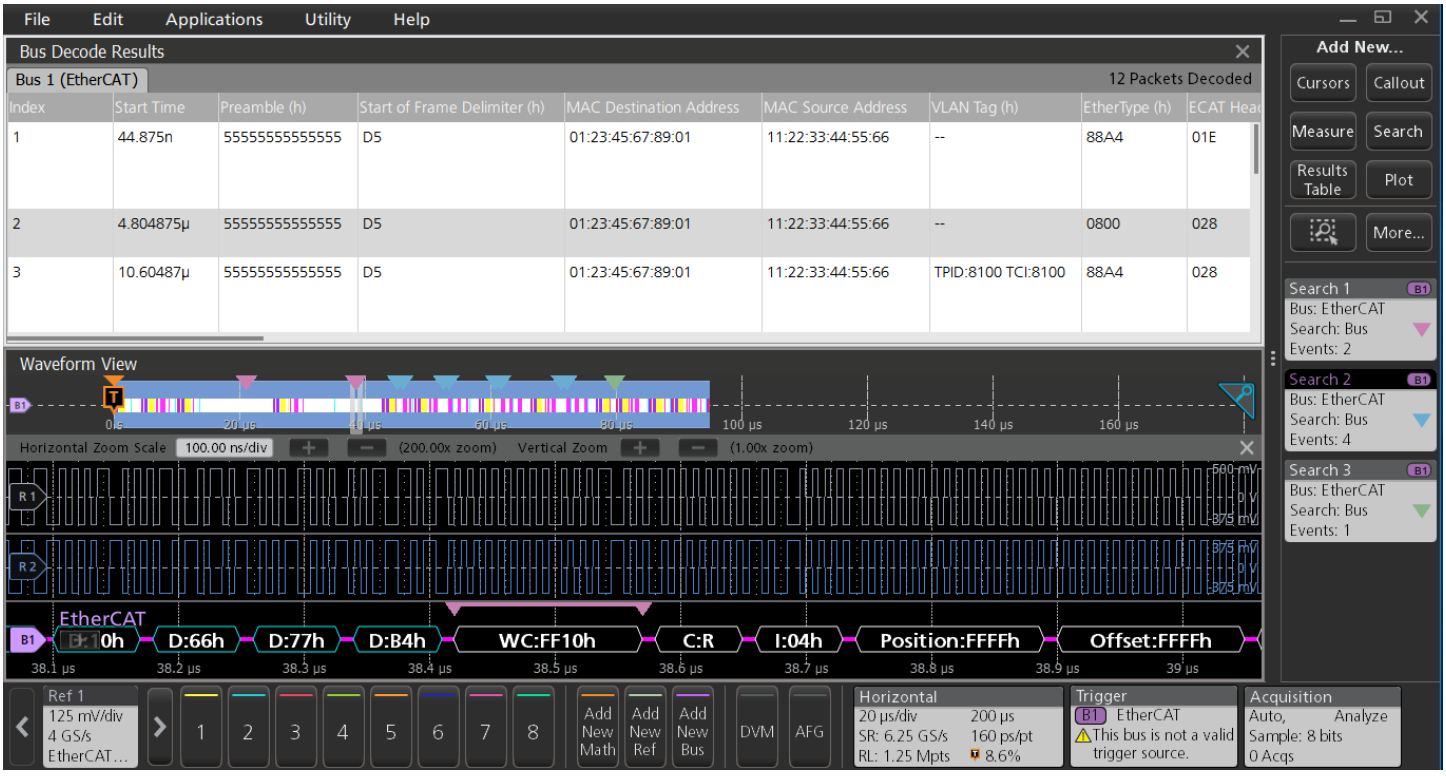
Characteristic	Description
Ethernet sources	Analog channels Digital channels Active math channels Active reference channels
Salient features	Decode capability for EtherCAT protocol in both single ended and differential modes
Formats available	Hex Binary Mixed Hex
Signal Type	Single ended (default) Differential

Display modes

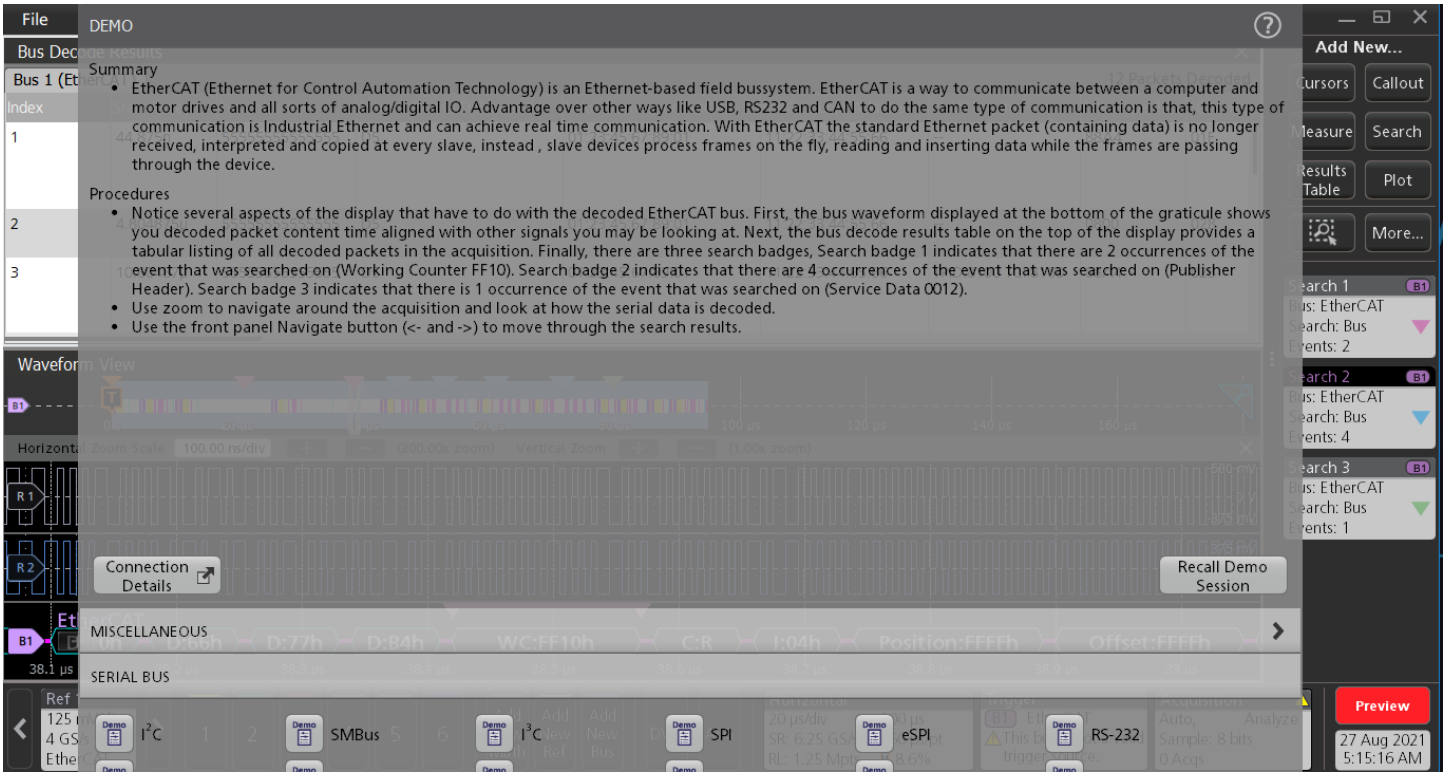
Characteristic	Description
Bus	Bus only
Results table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	**Start:** Select to search on Start of Frame. **Protocol:** Select to search on Protocol Types and then Frame type of each Protocol respectively. **IP Header:** Select to search on IP Header based on Identification, Source, and Destination Address Values. **UDP Header:** Set the 16-bit Source Port that you want to search. **MAC Address:** Select to search on Packets having the combination of Source and Destination Address Values. **Tag Control Information:** Sets the 16-bit tag control information that you want to search. **EtherCAT Header Length:** Sets the 11-bit ethercat header length that you want to search. **Datagram:** Select to search on sub-fields of datagram including Datagram Header, Data, and Working Counter. **Network Variable:** Select to Search on sub-fields of network variable including Publisher Header, NV Header, and Data. **Mailbox:** Select to search on sub-fields of mailbox including Mailbox Header, Service Data, and Error Reply Service Data. **FCS Error:** Select to search on FCS Error if any. **End of Frame:** Select to search on end of frames.



Protocol Decode Results table provides a time-stamped, tabular view of all captured packets on the EtherCAT bus.



The DEMO file content provides the information of the EtherCAT bus.

Ethernet characteristics1

Bus setup options

Characteristic	Description
Ethernet Source(s)	Analog channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Speed	10BASE-T 100BASE-TX
Recommended Probing	Differential
Formats Available	Mixed Hex Hex Binary Mixed ASCII

Display modes

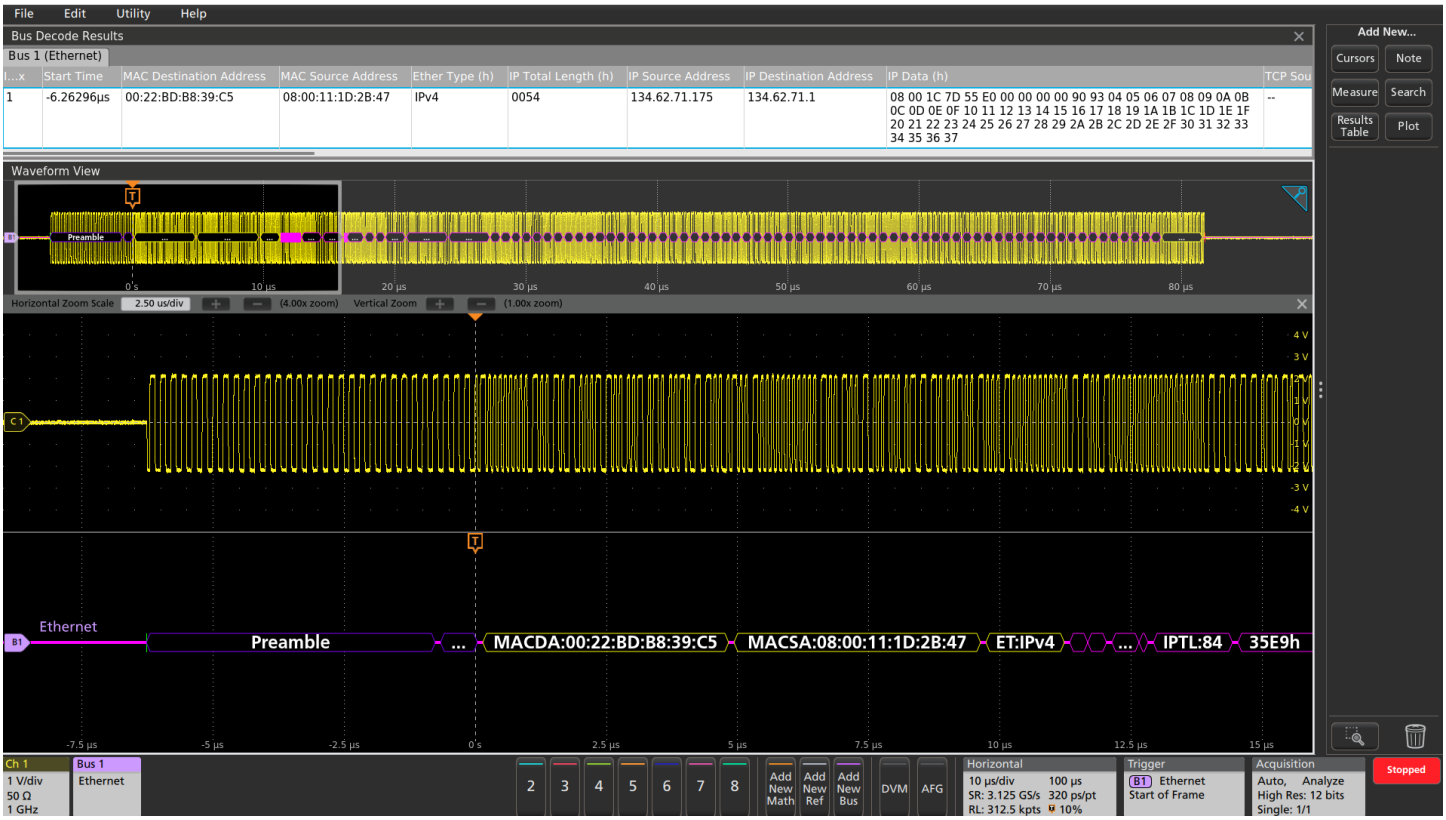
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger options

Characteristic	Description
Trigger On	Start Frame Delimiter MAC Addresses Q-Tag Control Information MAC Length/Type IPv4 Header TCP Header MAC Data TCP-IPv4 Client Data Idle End of Packet Frame Check Sequence (CRC) Error

Bus search options

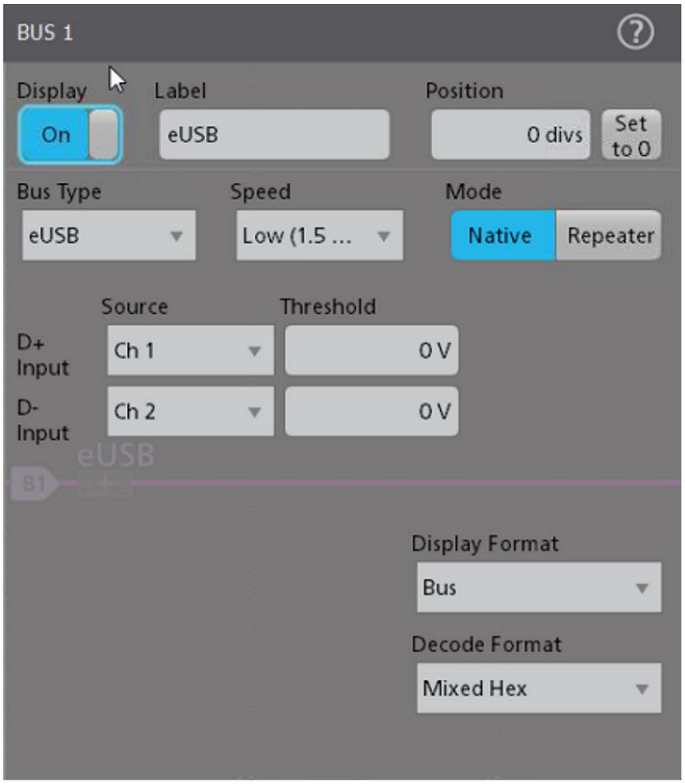
Characteristic	Description
Search On	Start Frame Delimiter MAC Addresses Q-Tag Control Information MAC Length/Type IPv4 Header TCP Header MAC Data TCP-IPv4 Client Data Idle End of Packet Frame Check Sequence (CRC) Error



Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the 10BASE-T Ethernet bus.

Triggering and automatically searching on the 100BASE-TX Ethernet bus.

e-USB2 (Version 2.0)



Bus setup options

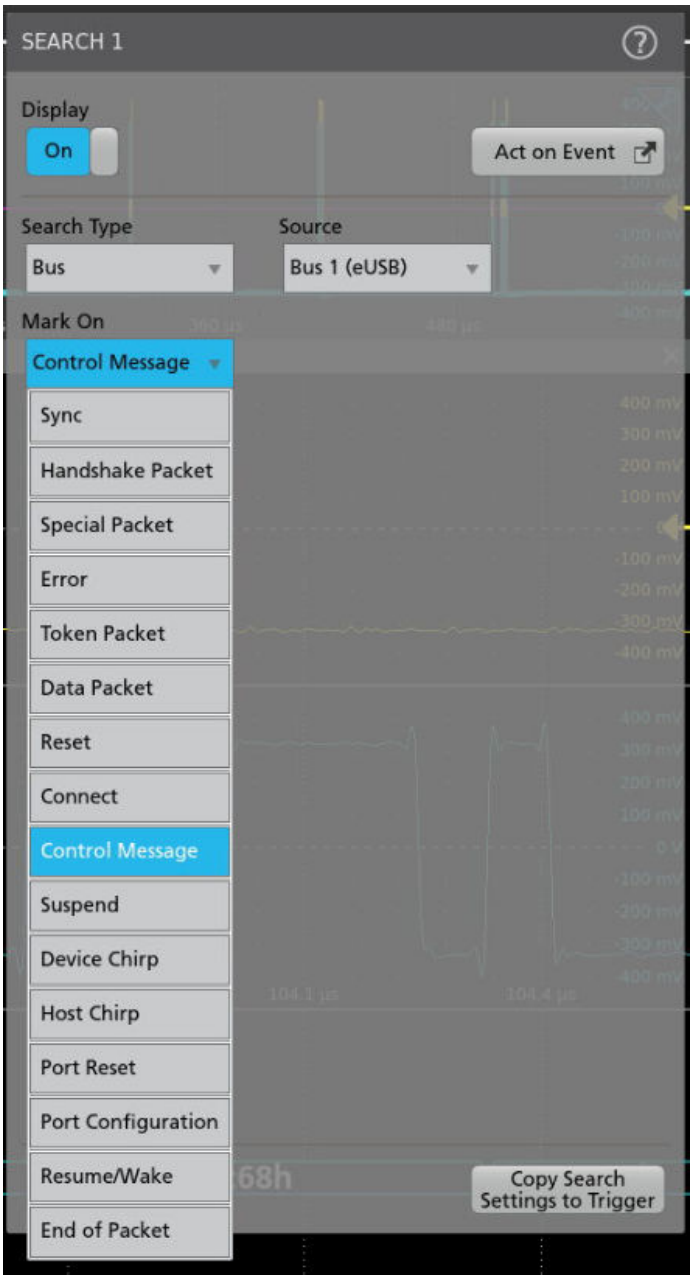
Characteristic	Description
Source(s)	Analog channels Digital channels(single-ended) Active Math channels Active Reference channels
Thresholds	Pre-channel thresholds
Speeds	Speed High Speed (480 Mb/s) Full Speed (12 Mb/s) Low Speed (1.5 Mb/s)
Recommended Probing, HS, LS, and FS	Single-ended [Active Single Ended TAP1500]
Formats Available	Mixed Hex Hex Binary Mixed ASCII

Display modes

Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	Characteristic Description
	Search On Sync
	Reset
	Suspend
	Resume/Wake
	Connect
	Control Message
	Port Reset
	Port Configuration
	Device Chirp
	Host Chirp
	End of Packet
	Token (address) Packet
	Data Packet
	Handshake Packet: ACK, NAK, STALL, NYET (HS only)
	Special Packet: PRE (FS only), ERR, SPLIT, PING
	Reserved
	Error: PID check, CRC5 or CRC16, Bit stuffing (LS and FS only)

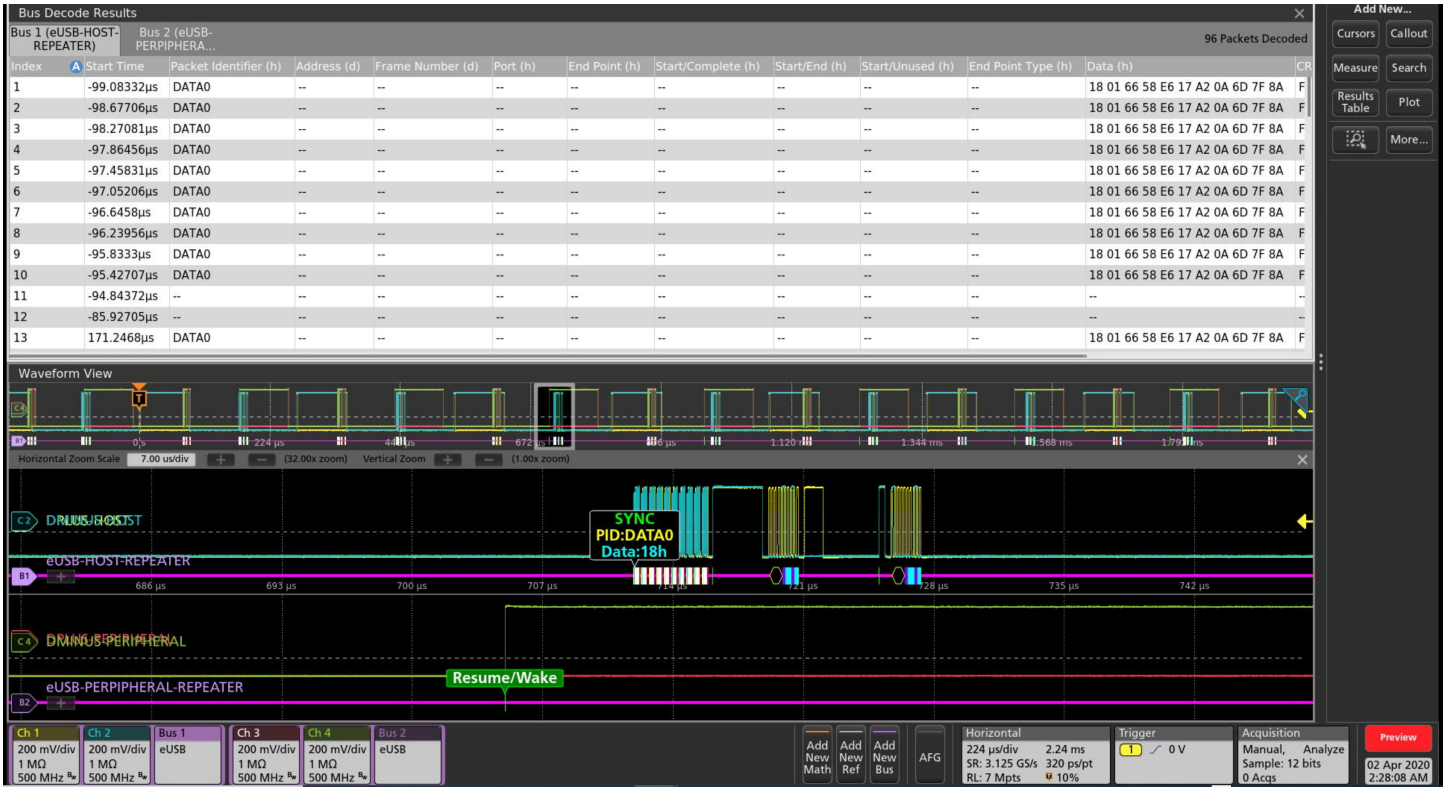


Bus decode

Characteristic	Description
Decode Display	Start of packet (green bar) Sync (green packet) PID (yellow packet) Token (address) (yellow packet) Data (cyan packet) CRC (purple packet) Error (red packet) End of packet (red bar) Control Message (Yellow packet) Zeros (Blue packet) Ack (Purple packet) Port Reset (Red Bar) Port Configuration(Green Bar) Connect (Green Bar) Resume/Wake(Green Bar) Device Chirp(Green Bar) Host Chirp (Green Bar) End Of reset(Red Bar)

Results & other features

Characteristic	Description
Table view * Depends on the Model	View more than 10000* points
Save	Save Result table as CSV
Sessions	Save sessions of your protocol setup
Simultaneous Buses * Depends on the Model	Load multiple Buses simultaneously*
Upcoming Future addition	Timing Measurements for Protocols
Search Table	Displays the Search hits along with Delta time difference between hits



Results table with decoded waveform.

FlexRay characteristics (Version 2.0)

Bus setup options

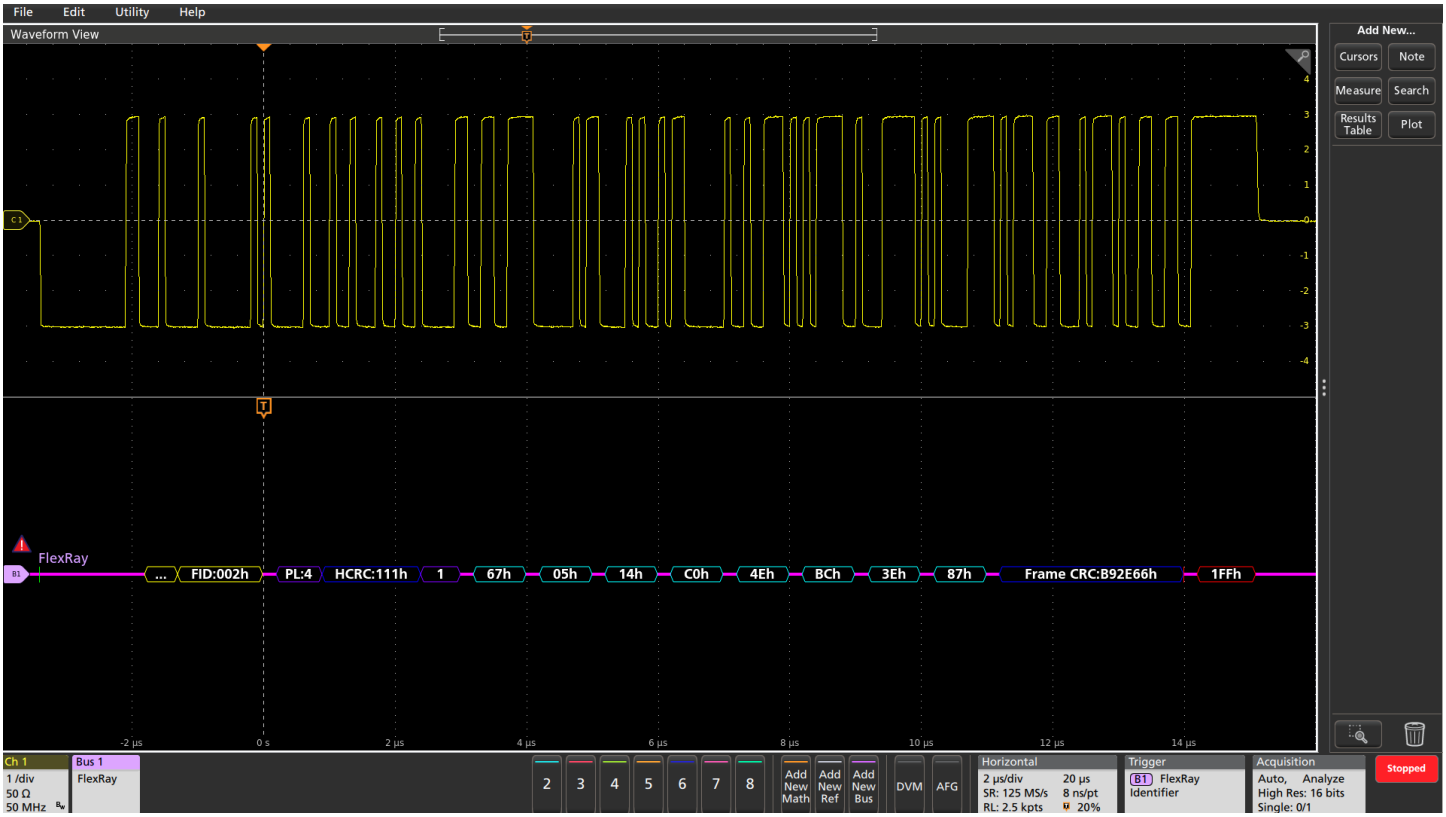
Characteristic	Description
Source for Differential Probing (Bdiff)	Analog channels Active Math channels[1] Active Reference channels[1]
Source for Single-ended Probing (BP, BM)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Source for Single-ended Probing (Tx, Rx)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Thresholds: Bdiff BP, BM (analog channels) BP, BM (digital channels) Tx, Rx	High and Low thresholds High and Low thresholds Single threshold Single threshold
Recommended Probing: Bdiff, BP, BM Tx, Rx	Differential Single-ended
Channel Type	A B
Bit Rate Selection: Predefined list of rates Custom	2.5 Mb/s, 5 Mb/s, 10 Mb/s 1 Mb/s - 10 Mb/s
Formats Available	Hex Binary Mixed Hex (Decimal: ID, Len, and Count; Hex: Data and CRCs)

Display modes

Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Start of Frame Indicator Bits (Normal, Payload, Null, Sync, Startup) Cycle Count (when =, ≠, <, ≤, >, ≥) Header Fields (Indicator Bits, Identifier, Payload Length, Header CRC, and Cycle Count) Identifier (when =, ≠, <, ≤, >, ≥) Data (when =, ≠, <, >, ≤, ≥) Identifier and Data End Of Frame (Static, Dynamic) Error (Header CRC, Trailer CRC, NULL Frame in Static, NULL Frame in Dynamic, Sync Frame in Dynamic, Start Frame No Sync)



Decoded FlexRay bus, with the acquisition triggered on a specified identifier value.

Decoded FlexRay bus, with all data values in a specific range marked with pink brackets.

I2C characteristics

Bus setup options

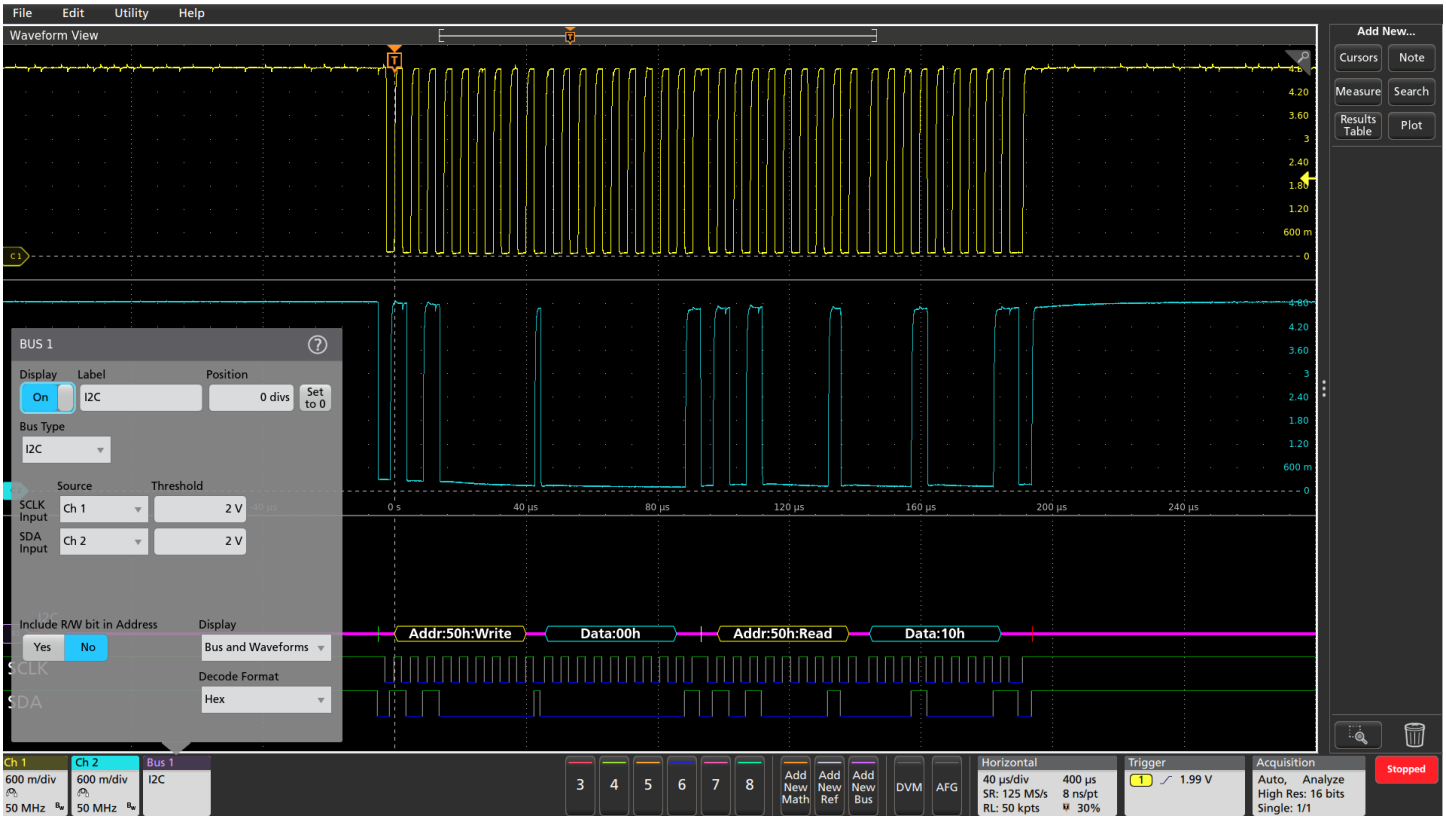
Characteristic	Description
I^2C Sources (Clock and Data)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Thresholds	Per-channel thresholds
Recommended Probing	Single-ended
Include R/W in Address	Yes or No
Address/Data Formats Available	Hex Binary

Display modes

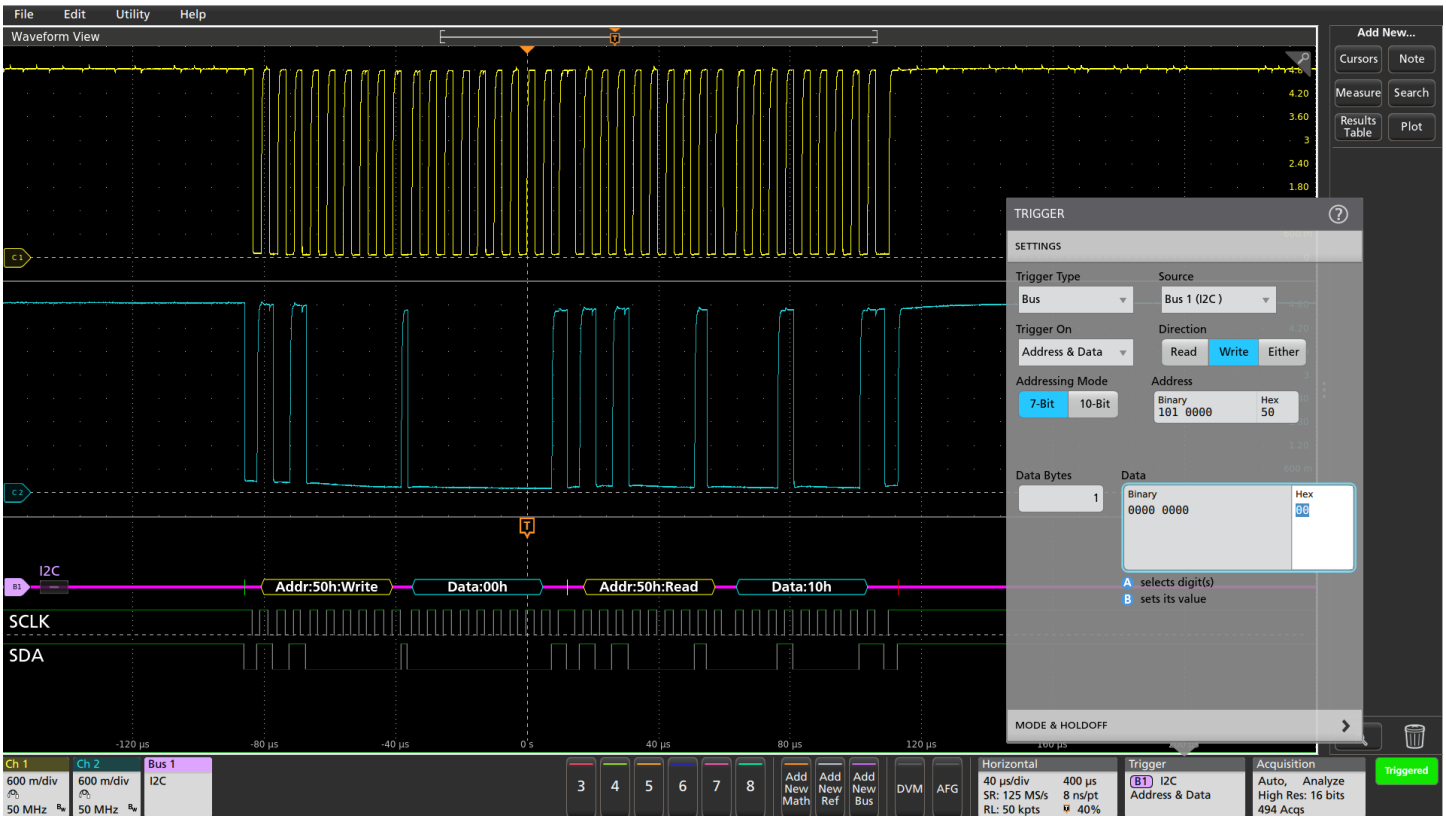
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Start Repeated Start Stop Missing Ack Address (7 or 10 bit) Data (1-5 bytes) Address and Data



Color-coded I2C bus display, using hexadecimal display format.



Triggering on a specific address value on the I2C bus.

I3C characteristics1 (Version 1.0/1.1)

Bus setup options

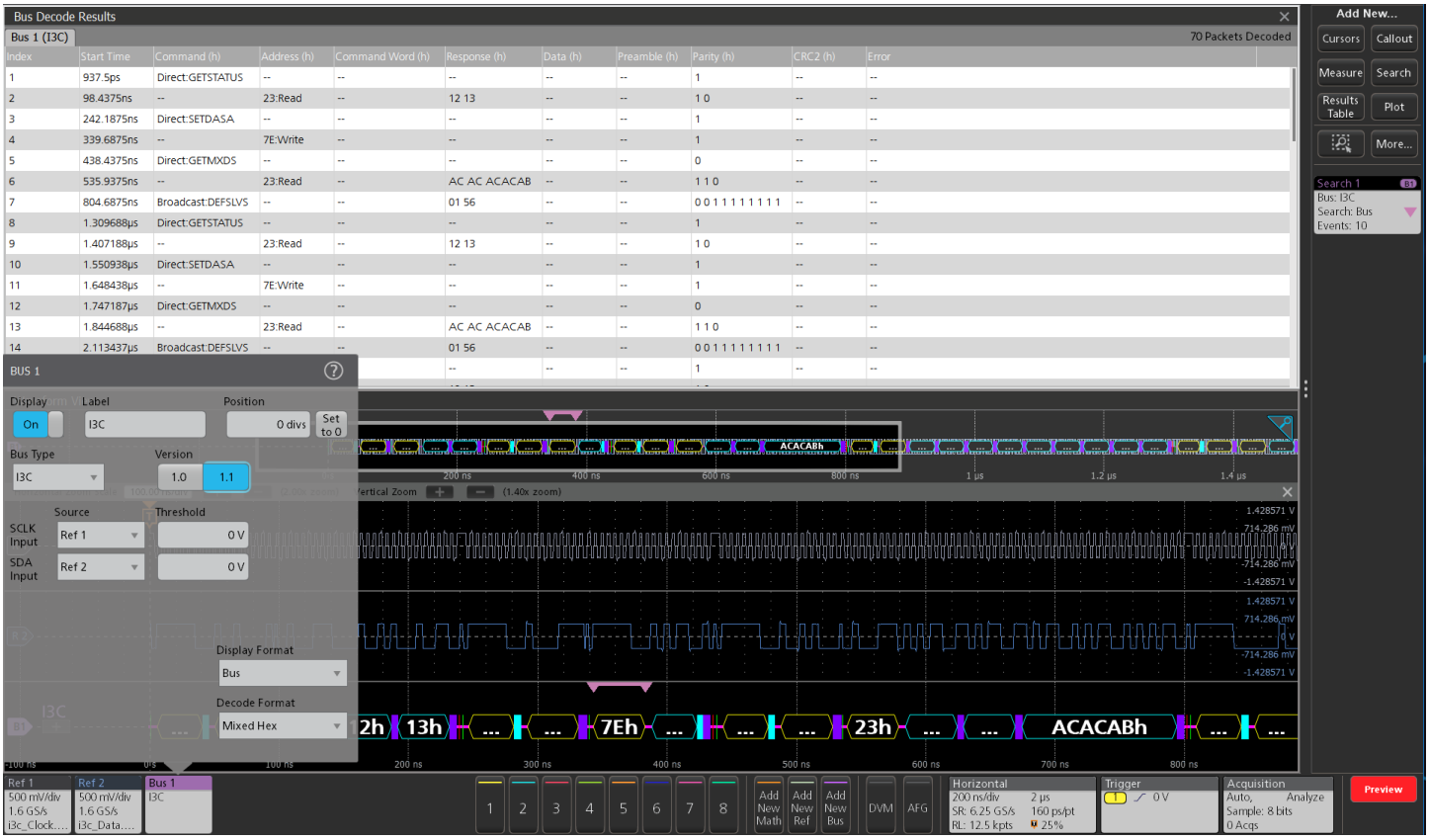
Characteristic	Description
I3C Sources (Clock and Data)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Speed	High Speed (480 Mb/s) Full Speed (12 Mb/s) Low Speed (1.5 Mb/s)
Recommended Probing	Single-ended
Formats Available	Hex Binary Mixed Hex
Version	1.0 1.1

Display modes

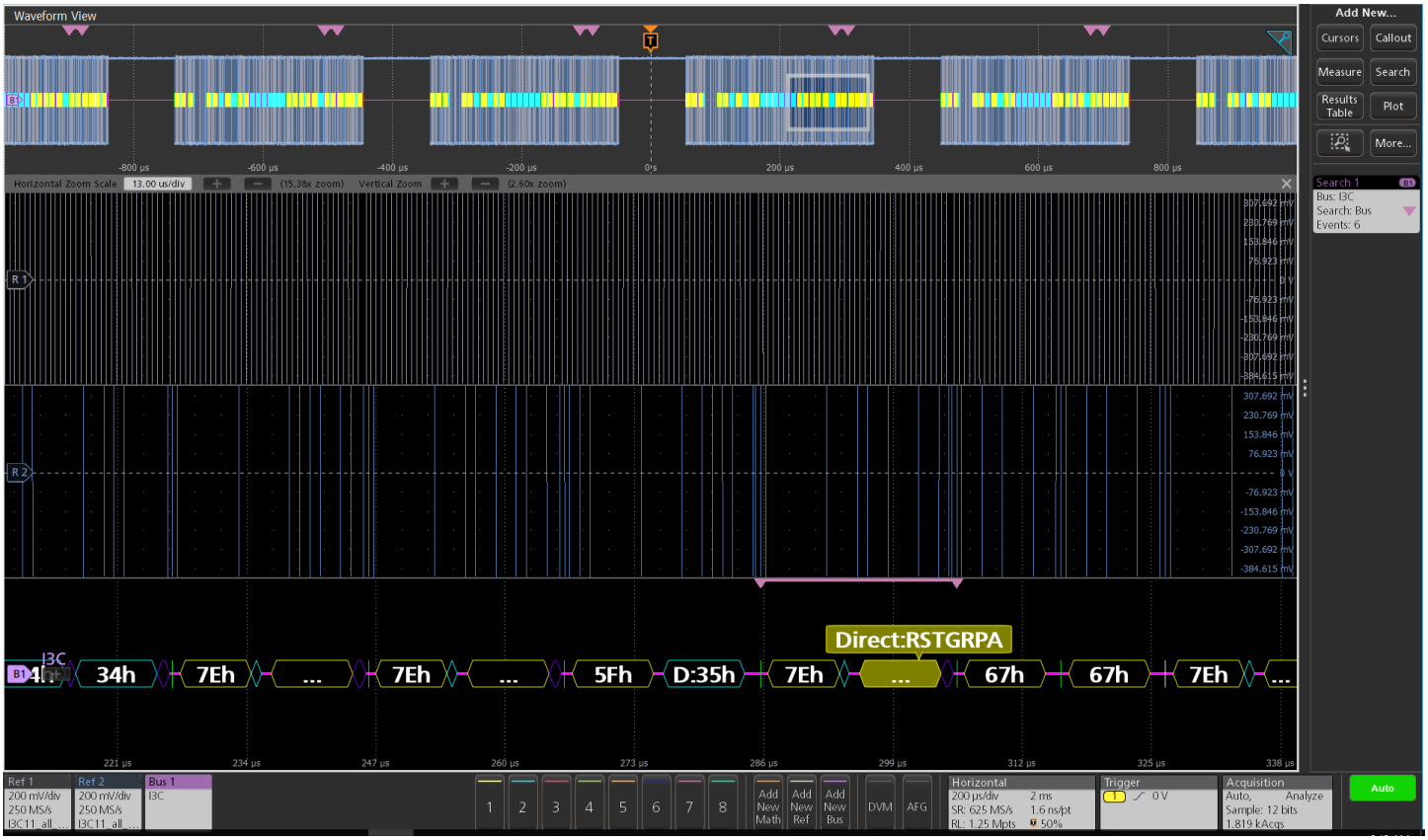
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	Start Repeated Start Address Data I3C SDR Direct Message I3C SDR Broadcast Message I3C DDR Message Errors Hot-Join Direct Message End Stop HDR Restart HDR Exit



I3C bus setup and MixedHex display, showing decode with version 1.1.



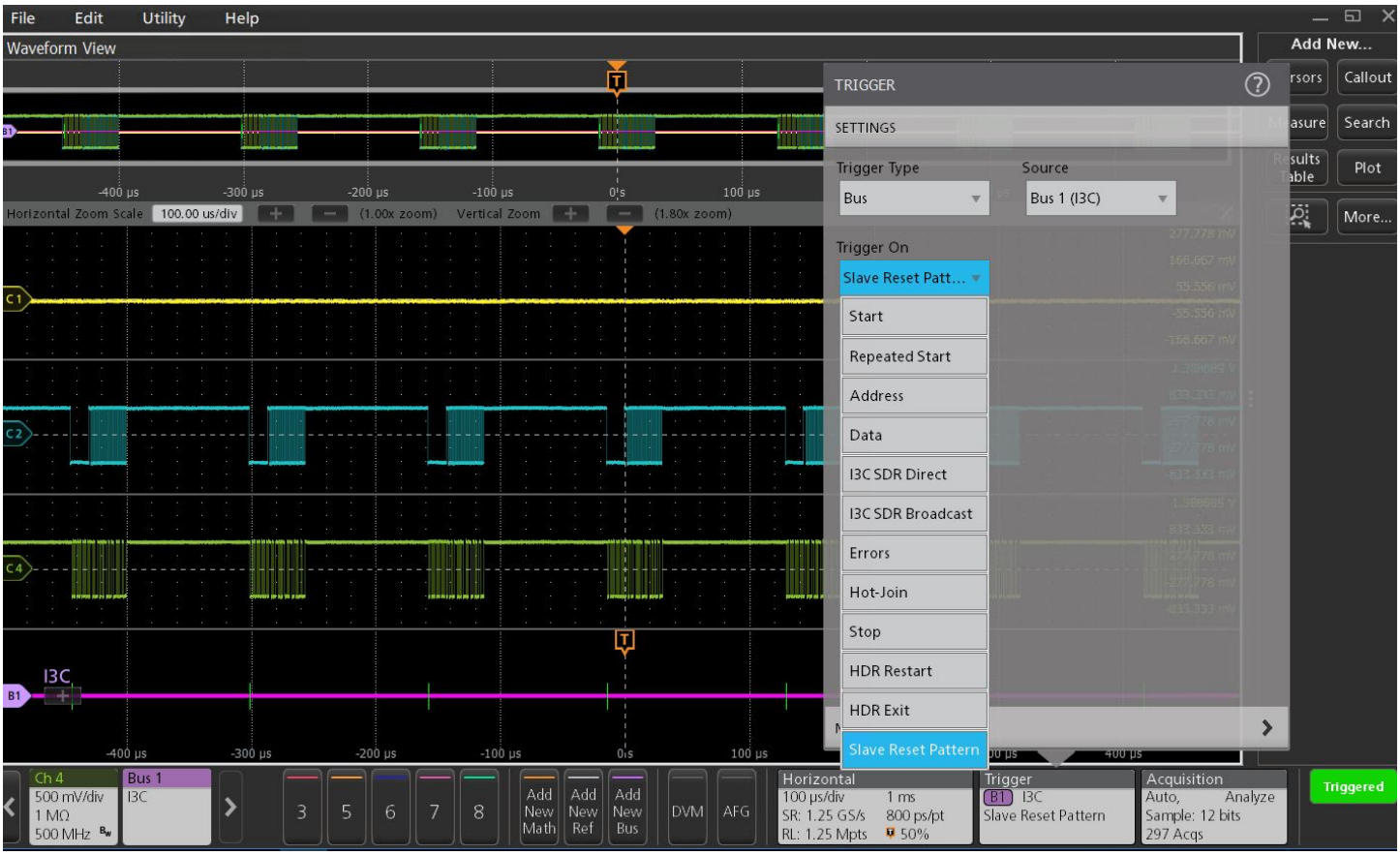
Searching the I3C bus with decode version 1.1 for the packet with Reset Group Address.

The Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the I3C bus.

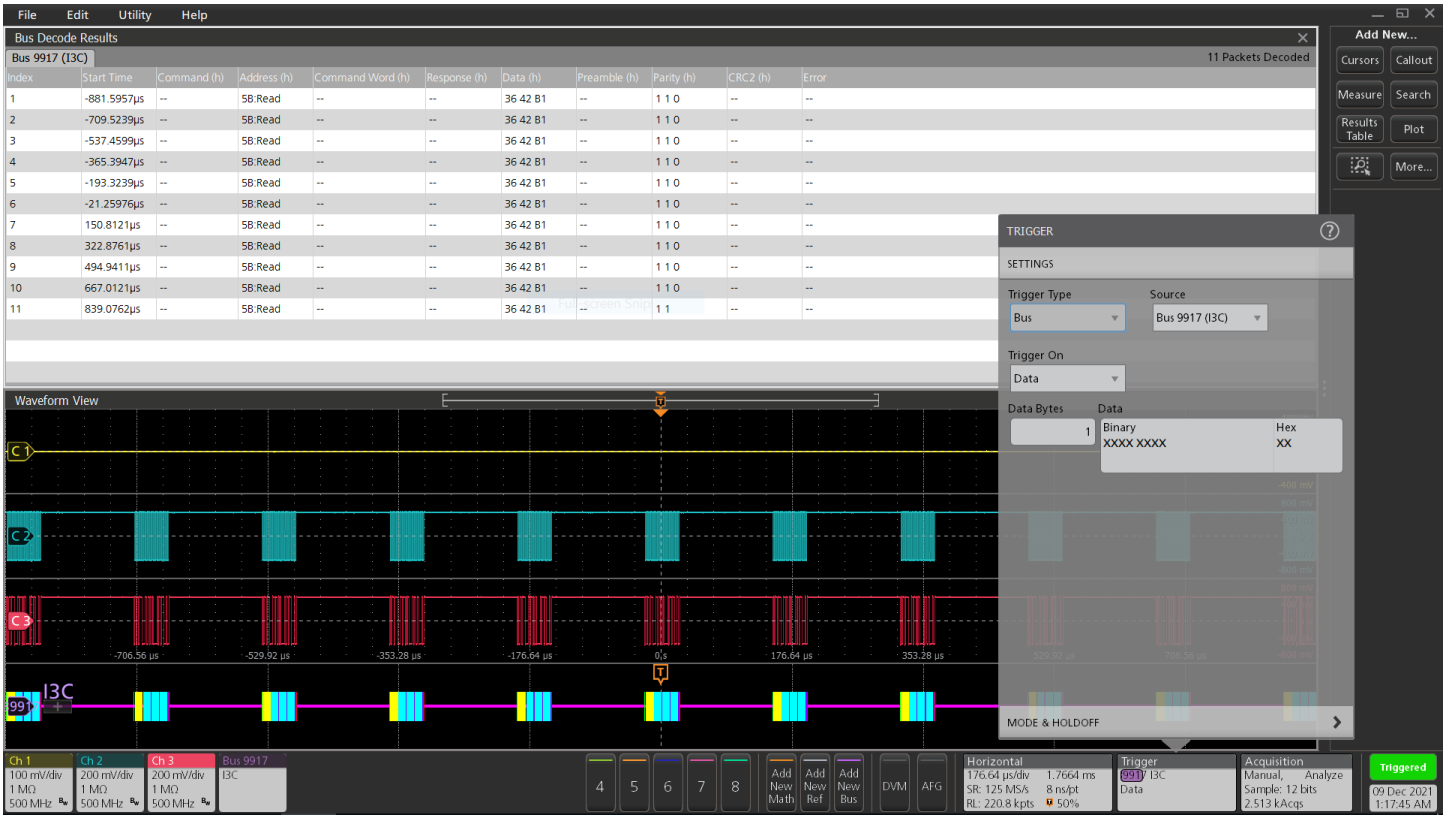
Searching on a specific data pattern on the I3C bus and automatically searching on Sync.

I3C (Trigger) characteristics

Characteristic	Description
I3C Sources	▪ Select the **I3C** bus on which to trigger. ▪ **Trigger On** Select the type of information on which to trigger.
Trigger On	▪ Start ▪ Repeated Start ▪ Address ▪ Data ▪ I3C SDR Direct ▪ I3C SDR Broadcast ▪ Hot join ▪ Errors ▪ HDR Exit ▪ HRD Restart ▪ Stop ▪ Slave Reset Pattern



I3C 1.1 version Slave Reset pattern trigger.



Triggering on a specific 7-Bit read address value on the I3C bus.

LIN characteristics (Version 2.0)

Bus setup options

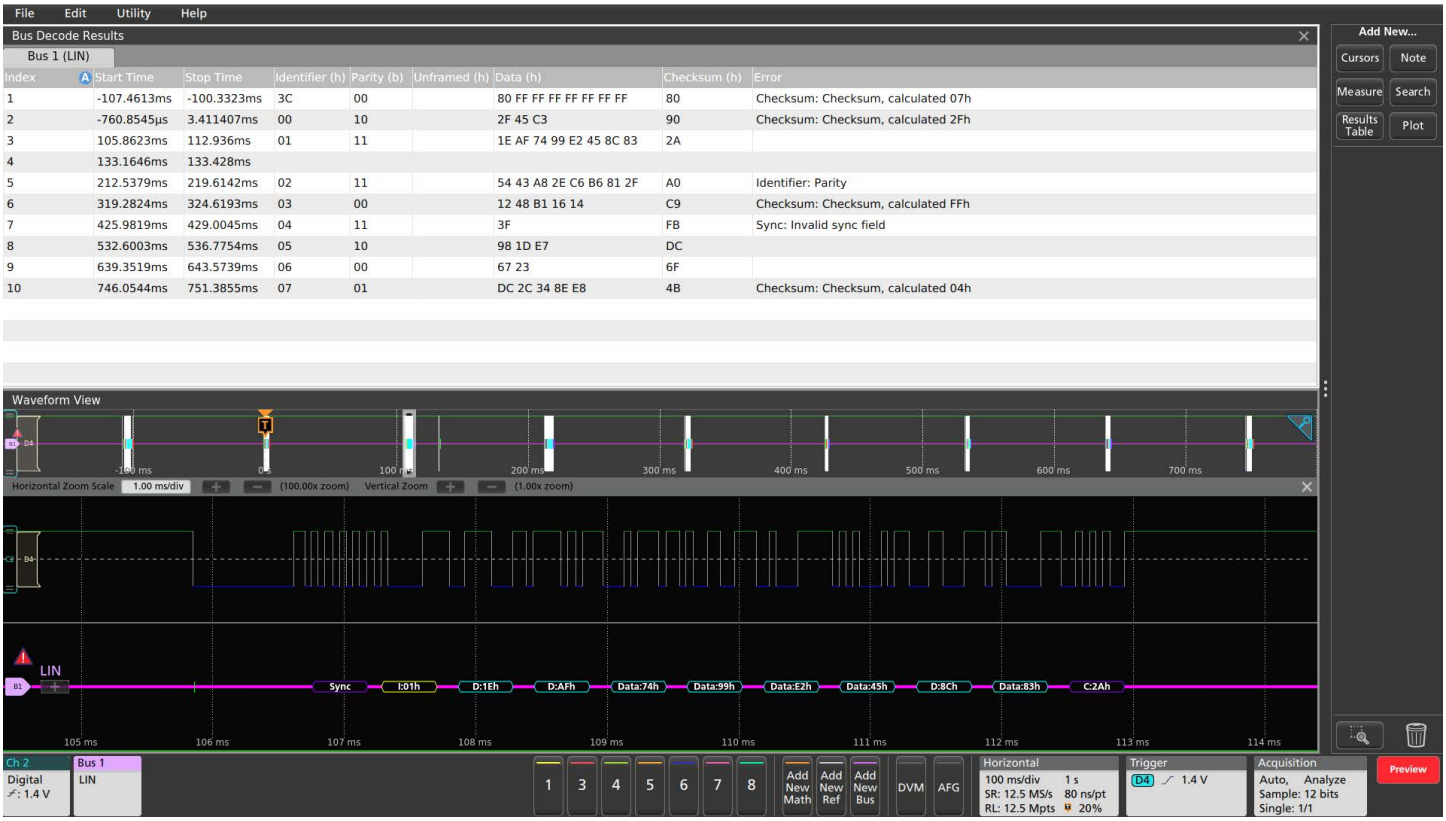
Characteristic	Description
LIN Source	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Thresholds	Per-channel thresholds
Recommended Probing	Single-ended
Polarity	Normal Inverted
Bit Rate Selection: Predefined list of rates Custom	1.2 kb/s - 19.2 kb/s All but 3 Series MDO: 1 kb/s - 100 kb/s 3 Series MDO: 800 b/s - 100 kb/s
Sample Point	All but 3 Series MDO: 0% - 100% of bit period of unit interval 3 Series MDO: 10% - 90% of bit period of unit interval
LIN Standard	V 1.x V 2.x Both
Include Parity Bits with ID	Yes No
Formats Available	Hex Binary Mixed

Display modes

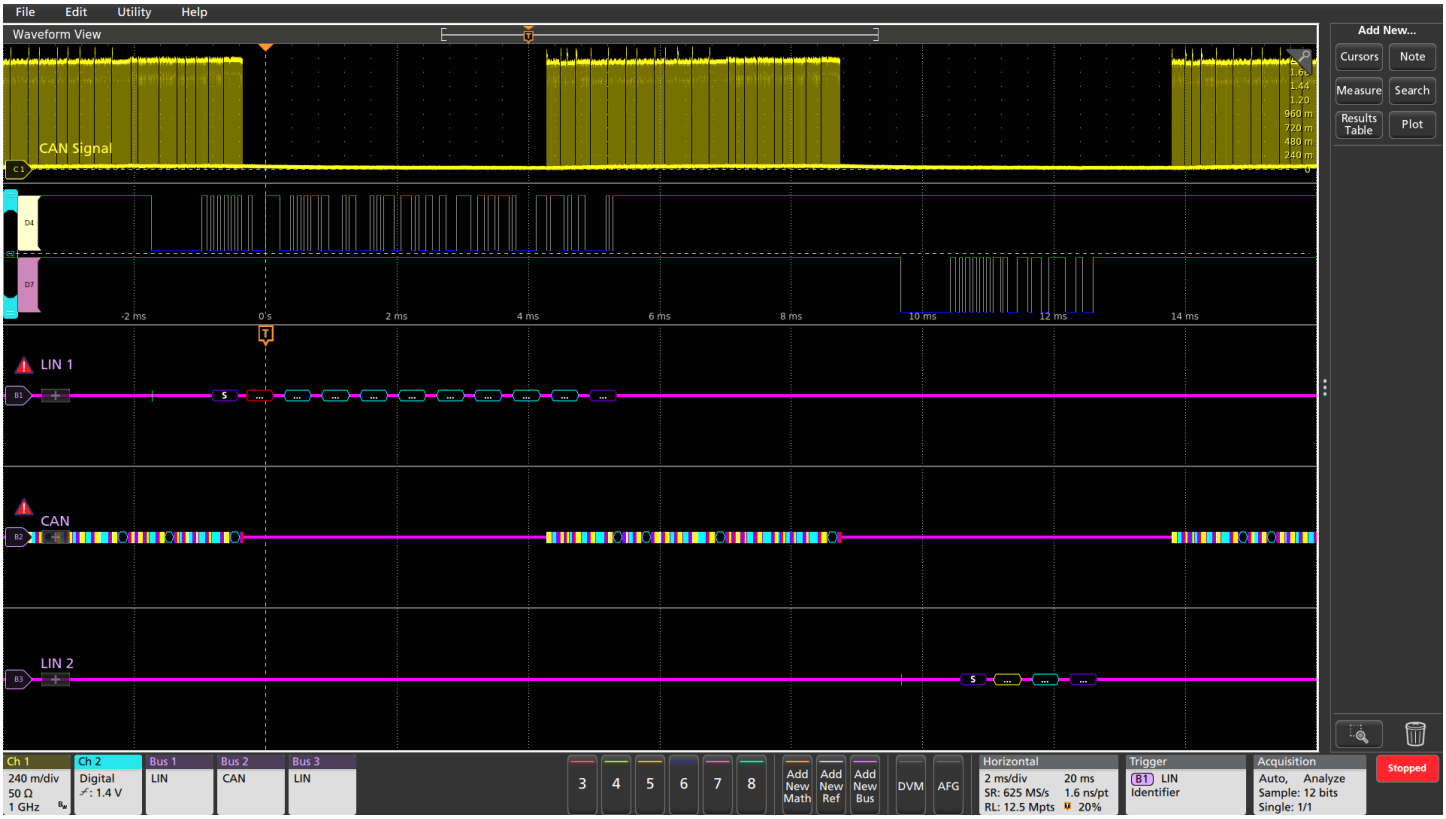
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Sync Identifier Data (number of bytes 1-8, trigger or search when =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) ID and Data Wakeup Frame Sleep Frame Error (Sync, ID Parity, Checksum)



Protocol Decode Results Table provides a time-stamped, tabular view of all captured LIN packets.



Display of multiple LIN and CAN buses, showing timing between the buses.

Manchester Characteristics (Line encoding)

Bus setup options

Characteristic	Description
Manchester Sources	Analog channels Digital channels(single-ended) Active Math channels Active Reference channels
Transition for '0'	Rise to Fall Fall to Rise
Recommended Probing	Differential/Single ended
Formats Available	Hex Binary
Bit Rate	1b/s - 1Gb/s
Packet View	On Off

Display modes

Characteristic	Description
Bus	Bus Only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	Characteristic Description Search On Sync Reset Suspend Resume/Wake Connect Control Message Port Reset Port Configuration Device Chirp Host Chirp End of Packet Token (address) Packet Data Packet Handshake Packet: ACK, NAK, STALL, NYET (HS only) Special Packet: PRE (FS only), ERR, SPLIT, PING Reserved Error: PID check, CRC5 or CRC16, Bit stuffing (LS and FS only)

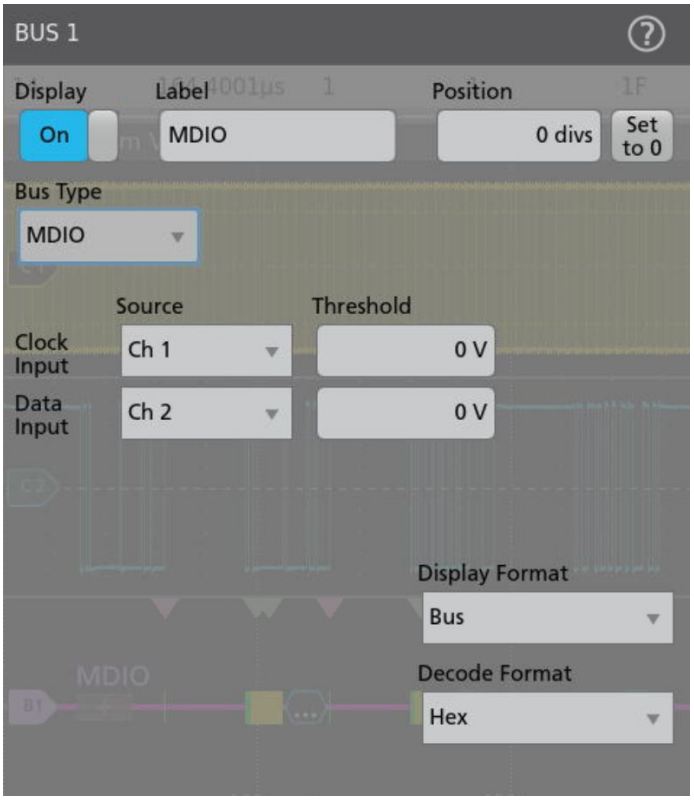
Bus search options

Characteristic	Description
Search On (Packet View ON)	Sync Bits Header Data Trailer Errors
Search On (Packet View OFF)	Data Errors

MDIO Characteristics

Bus setup options

Characteristic	Description
MDIO Sources (Clock, Data)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Pre-channel thresholds
Recommended Probing	Single-ended
Formats Available	Hex Binary Mixed Hex



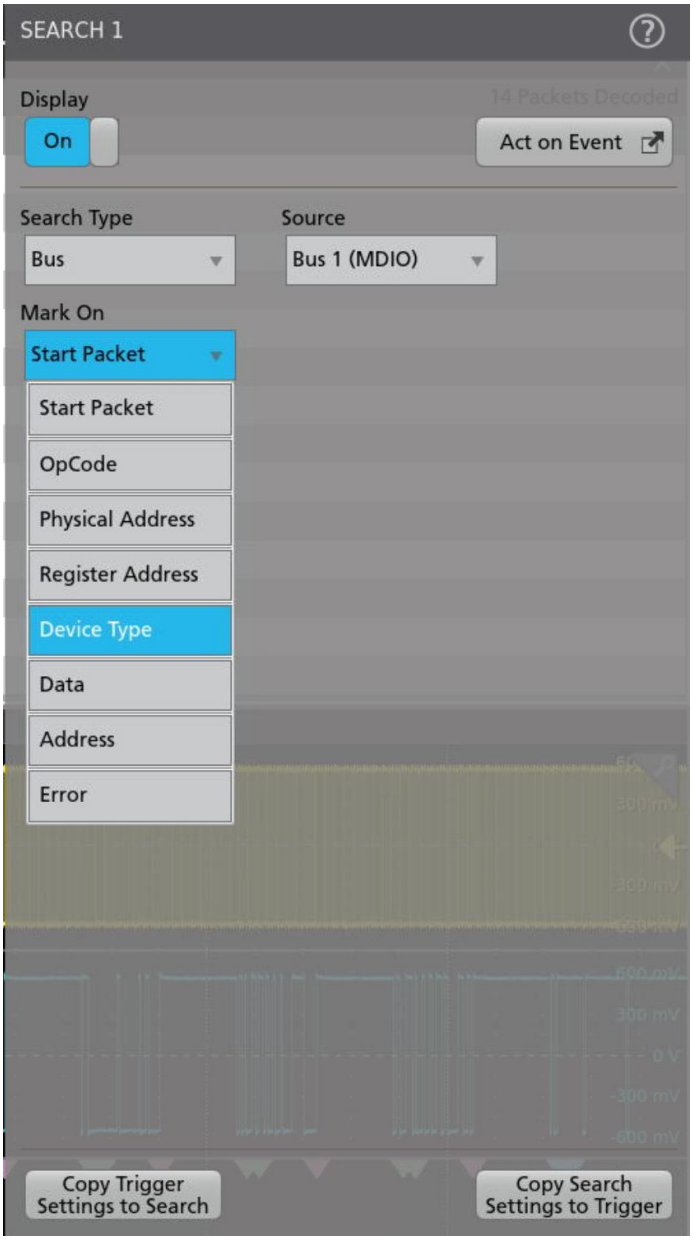
Bus configuration.

Display modes

Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Result Table	Decoded packet data in tabular view

Bus search options

Characteristic	Description
Search On	Start Packet OpCode Physical Address Register Address Data Error: Any, OpCode Error, Device Type Error



Search configuration.

Bus decode

Characteristic	Description
Maximum Clock/Data Rate	Maximum frequency of up to 2.5 MHz
Decode Display	Start Packet (Green) Clause (Green) OpCode (Yellow) Physical Address (Yellow) Register Address (Yellow) Device Type (Yellow) Data/Address (Cyan) Error: Any, OpCode Error, Device Type Error (Red)


File
Edit
Utility
Help
Bus Decode Results
Bus 1 (MDIO)
4 Packets Decoded
Index
Start Time
Clause (h)
OpCode (h)
Physical Address (h)
Device Type (h)
Data (h)
Address (h)
Error
1 93.75ns Clause: 45 Address 06 Reserved -- 21F0 --
2 32.59375µs Clause: 45 Write 0E Reserved 3619 -- --
3 65.09375µs Clause: 45 Address 1F PMD/PMA -- FFFF --
4 97.59375µs Clause: 45 Read 14 12 9200 -- Device Type: Unexpected Device Type for Clause 45
Waveform View
MDIO
C:45
OC:A
PhysicalAddress:06h
DT:R
Address:21F0h
Add New...
Cursors
Callout
Measure
Search
Results Table
Plot
More...
Total Packets
Total Errors
Ref 1
Ref 2
Bus 1
MDIO
Horizontal
Trigger
Acquisition
DVM
AFG

MIL-STD-1553 characteristics

Bus setup options

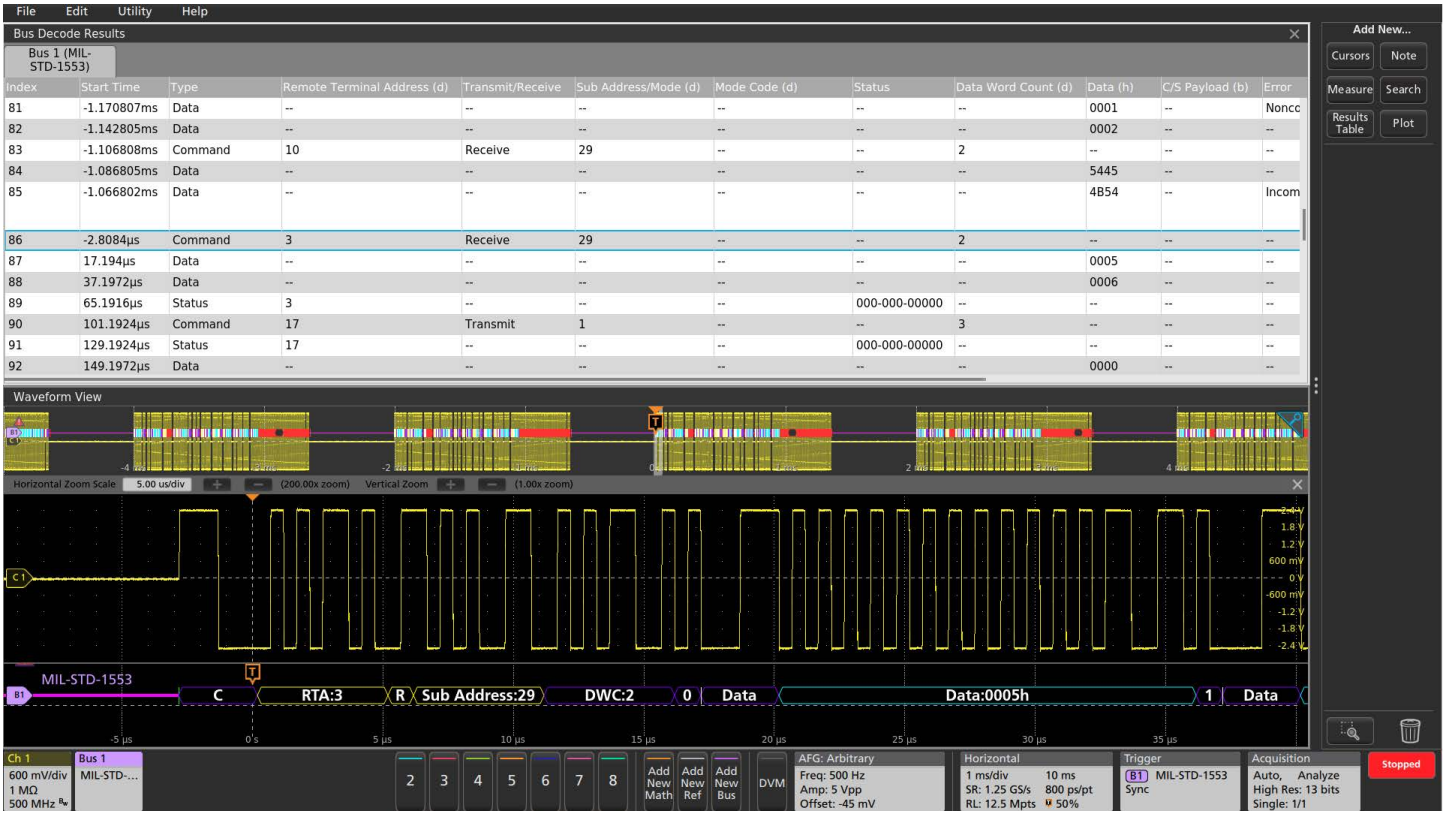
Characteristic	Description
MIL-STD-1553 Source	Analog channels Active Math channels Active Reference channels
Polarity	Normal Inverted
Thresholds	Single-ended: Per-channel thresholds Differential: High and low thresholds
Recommended Probing	Single-ended or differential
Bit Rate	1 Mb/s per the standard
Response Time	2 µs-100 µs
Formats Available	Mixed Hex Mixed ASCII Hex Binary

Display modes

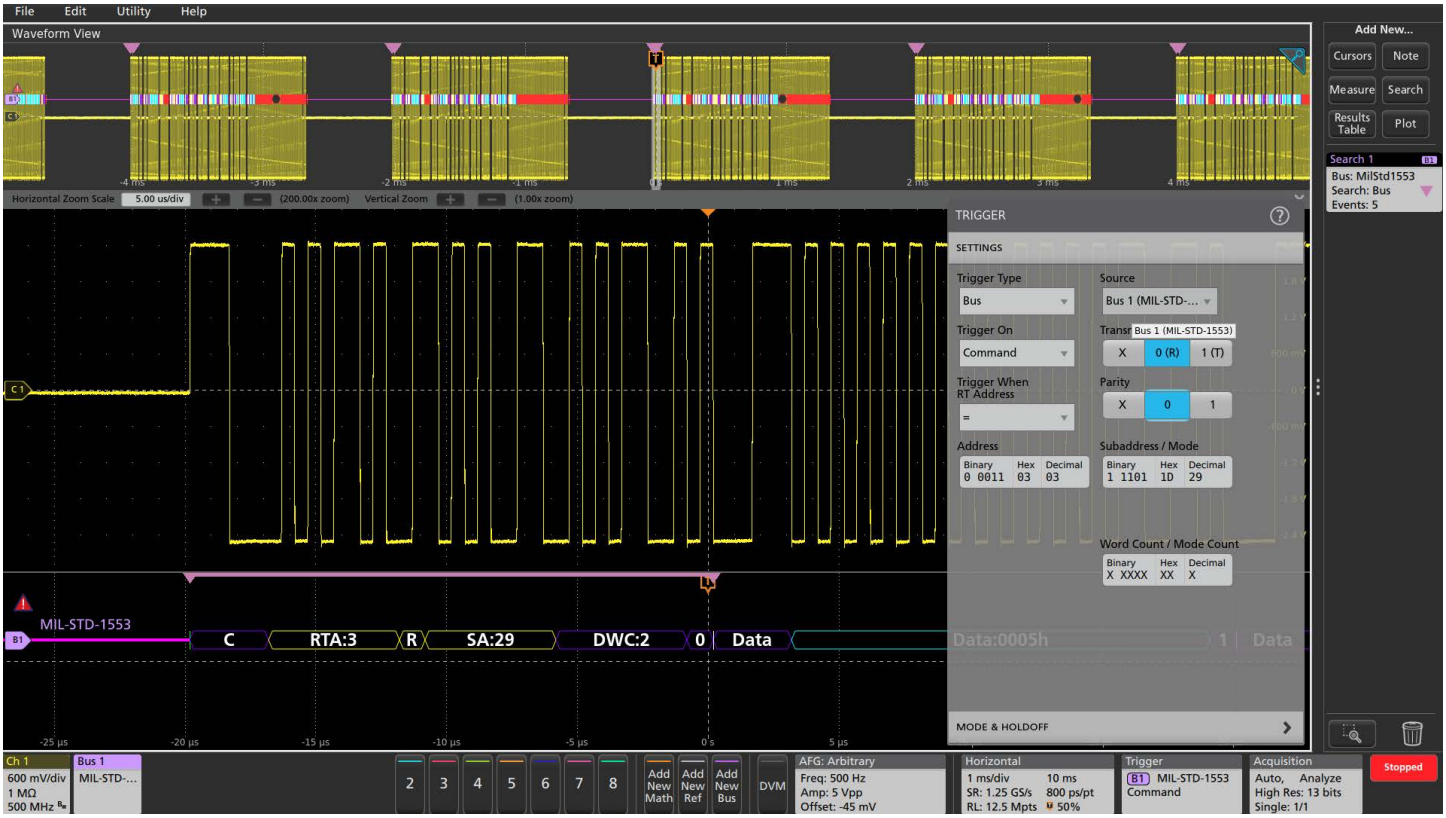
Characteristic	Description
Bus	Bus only
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Sync Command (Transmit/Receive Bit, Parity, Subaddress / Mode, Word Count / Mode Count, and RT Address =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) Status (Parity, Bit 9 - Message Error, Bit 10 - Instrumentation, Bit 11 - Service Request, Bit 15 - Broadcast Command Received, Bit 16 - Busy, Bit 17 - Subsystem Flag, Bit 18 - Dynamic Bus Control Acceptance, Bit 19 - Terminal Flag, and Data =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) Data (Parity, and Data =, ≠, <, ≤, >, ≥, Inside Range, Outside Range) Time (RT / IMG) (> Maximum, < Minimum, Inside range, Outside Range) Error (Parity Error, Sync Error, Manchester Error (trigger only), Non-contiguous Data)



Protocol Decode Results Table provides a time-stamped, tabular view of all captured MIL-STD-1553 packets.



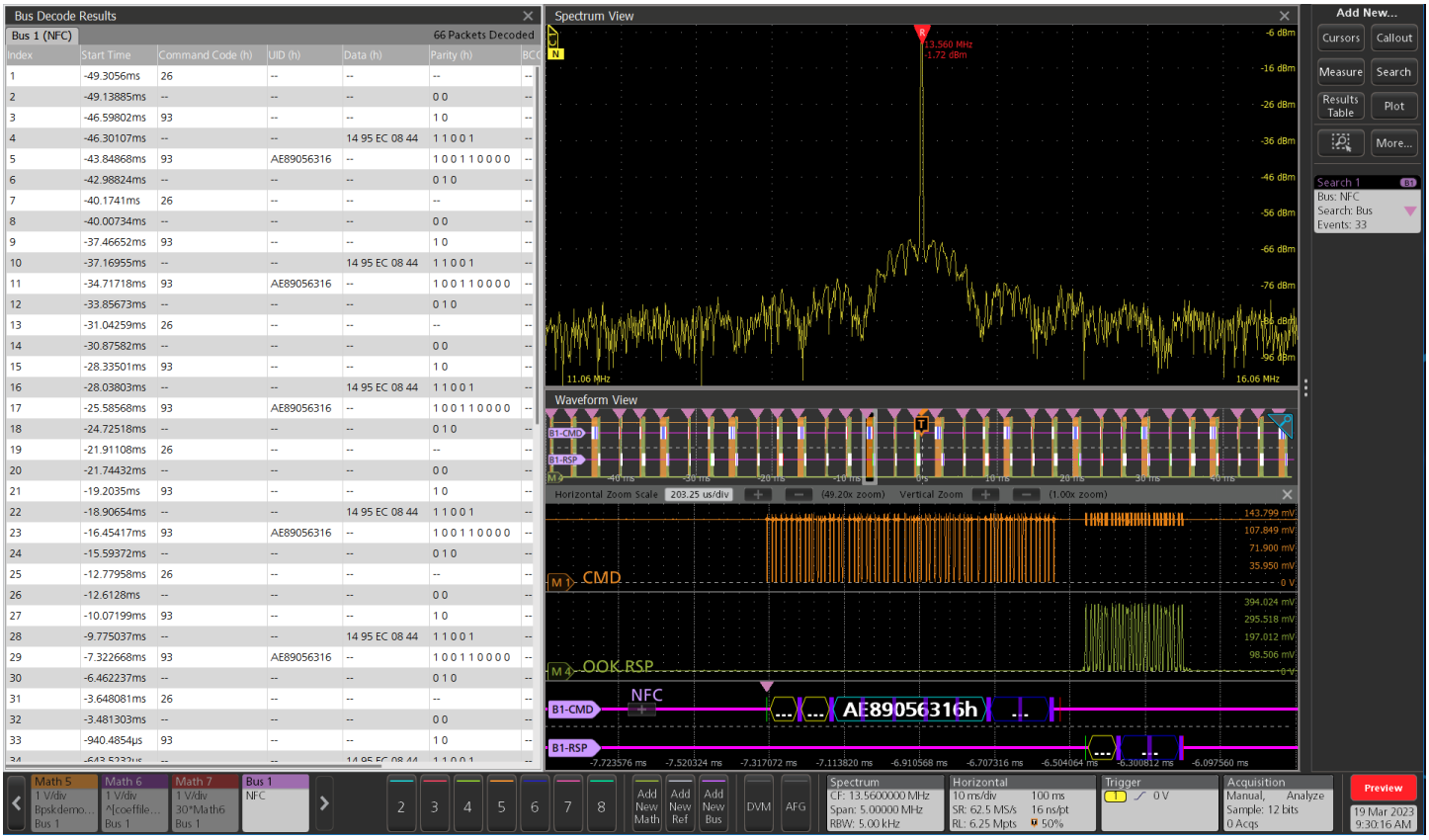
Triggering on a specific command pattern on the MIL-STD-1553 bus and automatically searching on the same pattern.

NFC characteristics

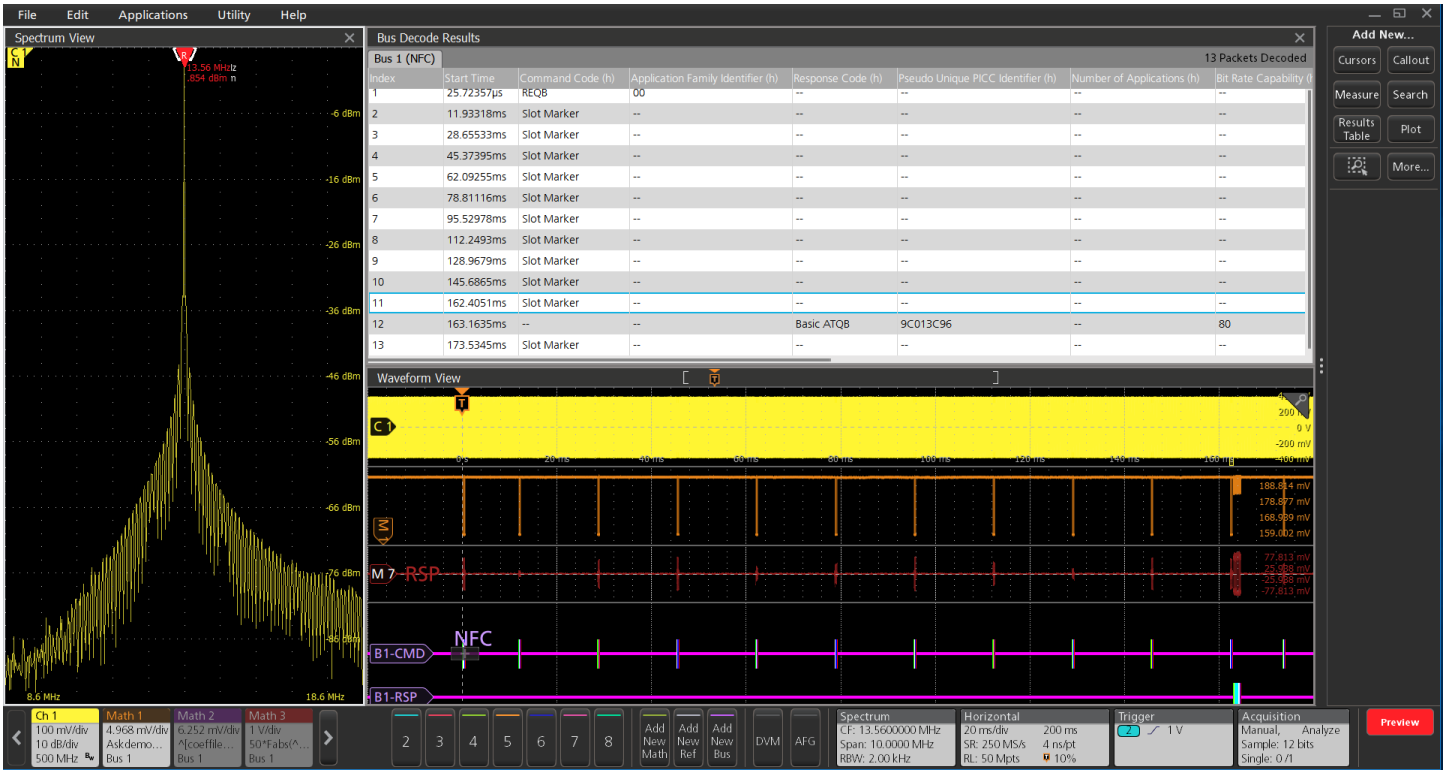
Characteristic	Description
NFC sources	▪ Analog channels (Spectrum View to be turned on) ▪ Active Math channels ▪ Active Reference channels
Salient features	▪ Decode capability for NFC protocol ▪ Decode capability for ISO/IEC 15693, ISO/IEC14443A, ISO/IEC14443B, and FeliCa Standards ▪ Decode capability for Command and Response packets ▪ Search capability for SOC, SOS, SYNC, EOC, SOF, AFI, PUPi, Identifier, Payload, UID, EOF, DATa, and EOS ▪ Search capability for different ISO/IEC 14443A commands like REQ, WUPA, Proprietary, SELECT, and HLTA ▪ Search capability for different ISO/IEC 15693, ISO/IEC 14443B, and FeliCa commands ▪ Search capability for Response packets ▪ Search capability for Errors like CRC, Parity
Product differentiators	▪ Perform NFC protocol decode and search seamlessly with a single oscilloscope instrument ▪ Analyze and correlate analog RF and digital signals simultaneously for enhanced insight ▪ Save transfer time and memory of large recordings with hardware DDC (digital downconverter) on each input ▪ Trigger on 13.56 MHz RF envelope using RF vs. Time traces and triggers, reducing the need to trigger on other I/O signals
Recommended probing	▪ EMI-NF-PROBE near-field probe set for contact-less probing and manual troubleshooting ▪ TPP1000 probe for conducted probing
Formats available	▪ Hex ▪ Binary ▪ Mixed Hex

Display modes

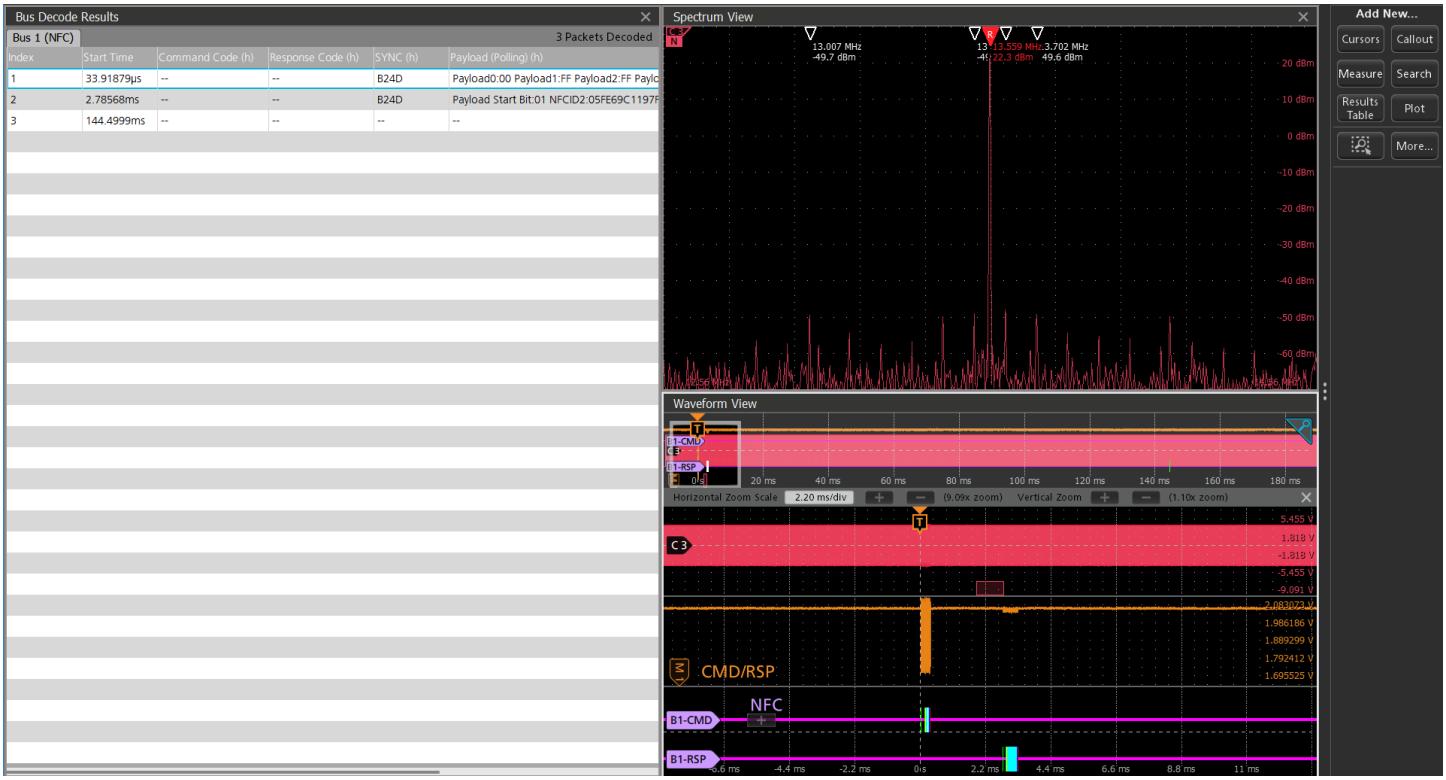
Characteristic	Description
Bus	Bus only
Result Table	Decoded packet data in a tabular view



The results table provides time-stamped, tabular view of all captured packets on the NFC bus. 33 occurrences of the event is searched on SOC on the command lane.



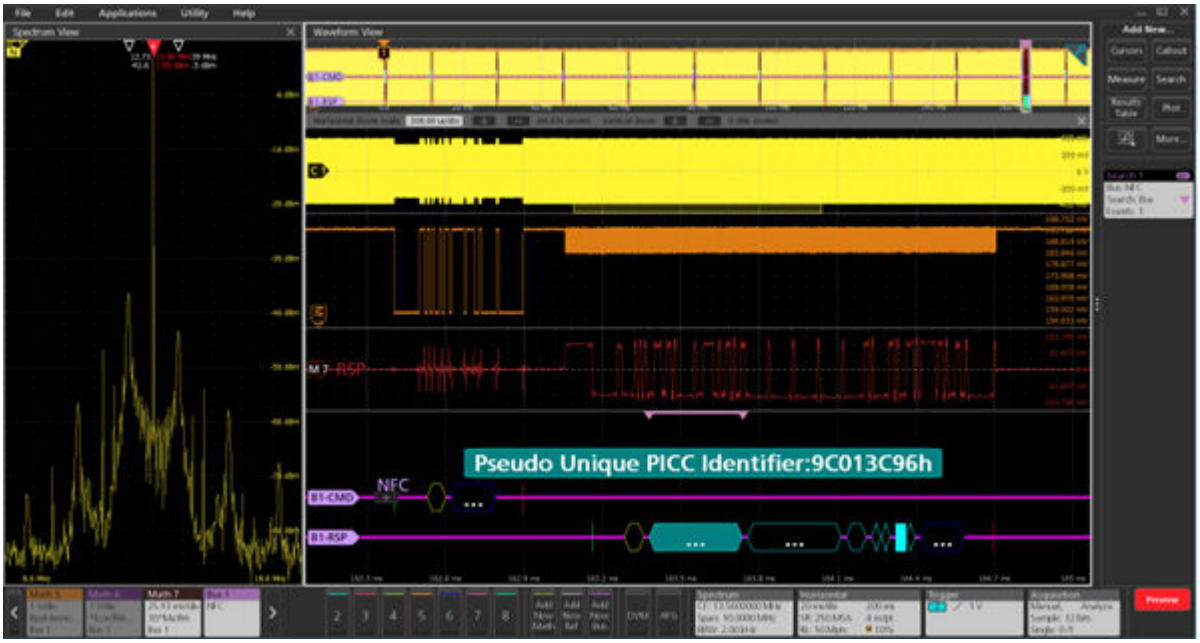
Result table for NFC 14443B.



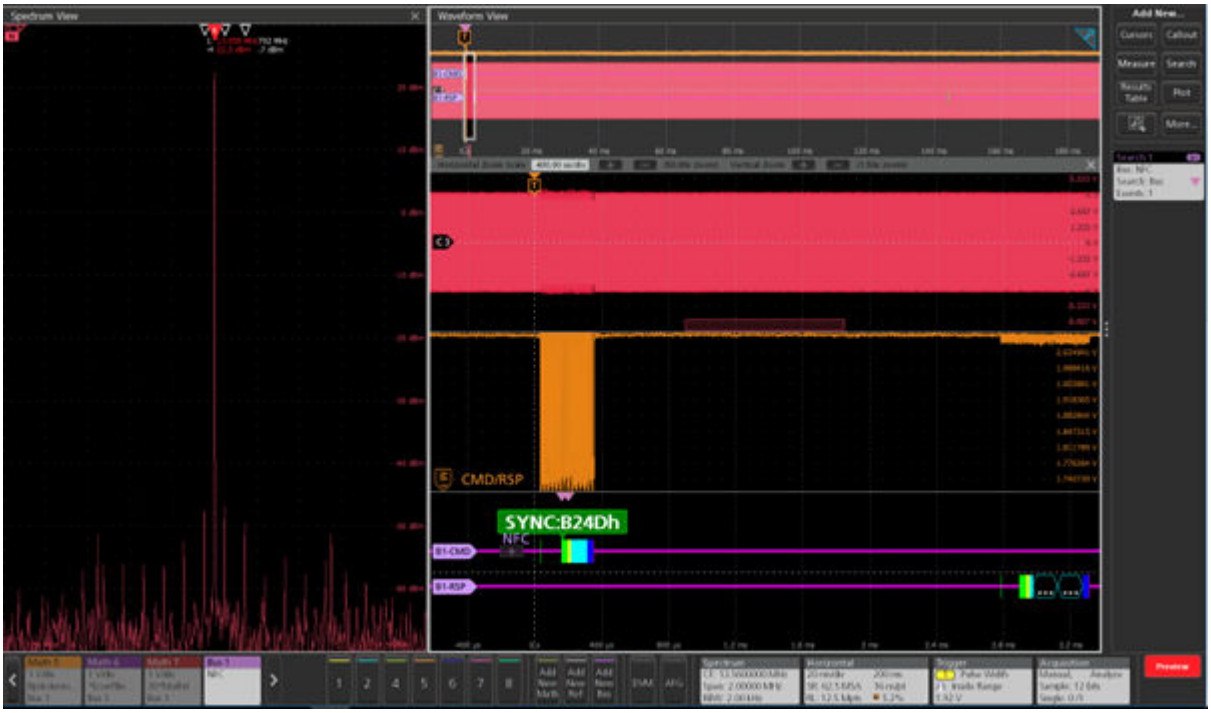
FeliCa decode with result table.

Bus Search

Characteristic	Description
Search ON	▪ SOF ▪ SOC ▪ SOS ▪ Data ▪ Payload ▪ Command Code ▪ Response Code ▪ UID ▪ AFI ▪ Identifier ▪ PUPI ▪ EOC ▪ EOF ▪ EOS ▪ Errors



NFC Search for PUPI.



NFC Search for SYNC.

NRZ Characteristics (Line encoding)

Bus setup options

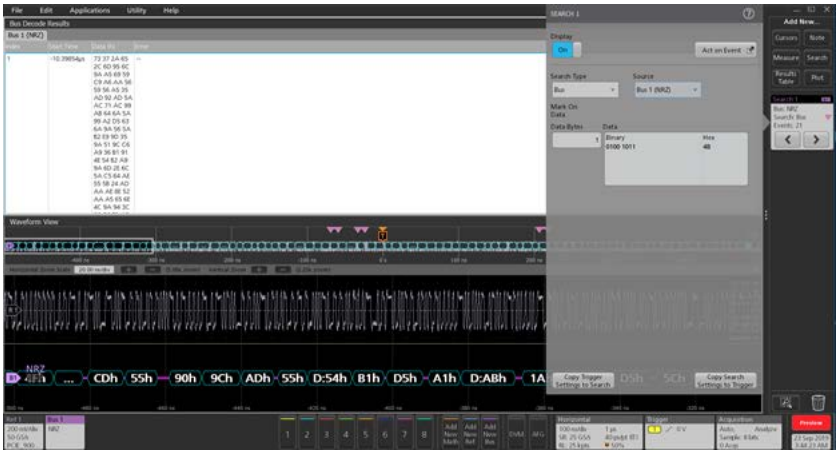
Characteristic	Description
NRZ Source(s)	Analog Channels Digital Channels Active Math Channels Active Reference Channels
Thresholds	Per-channel Thresholds
Recommended Probing	Differential
Bit Order	MSB First LSB First
Bit Rate	1b/s - 100Gb/s
Start Index	1st to 8th edge
Packet View	On Off
Formats Available	Hex Binary

Display modes

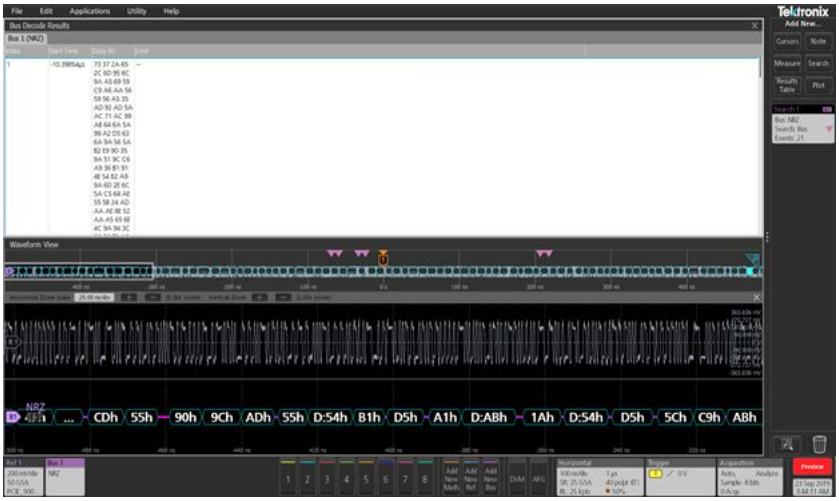
Characteristic	Description
Bus	Bus Only
Bus and Waveforms	Simultaneous display of bus and digital waveforms.
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	Data Bytes [Maximum 5]



Searching on a specific data symbol in symbol format in the NRZ bus.



The Protocol Decode results table provides time-stamped, tabular view of all captured packets on the NRZ bus.

PCIe Characteristics (Gen 1, Gen 2, Gen 3 and Gen 4)

Bus setup options

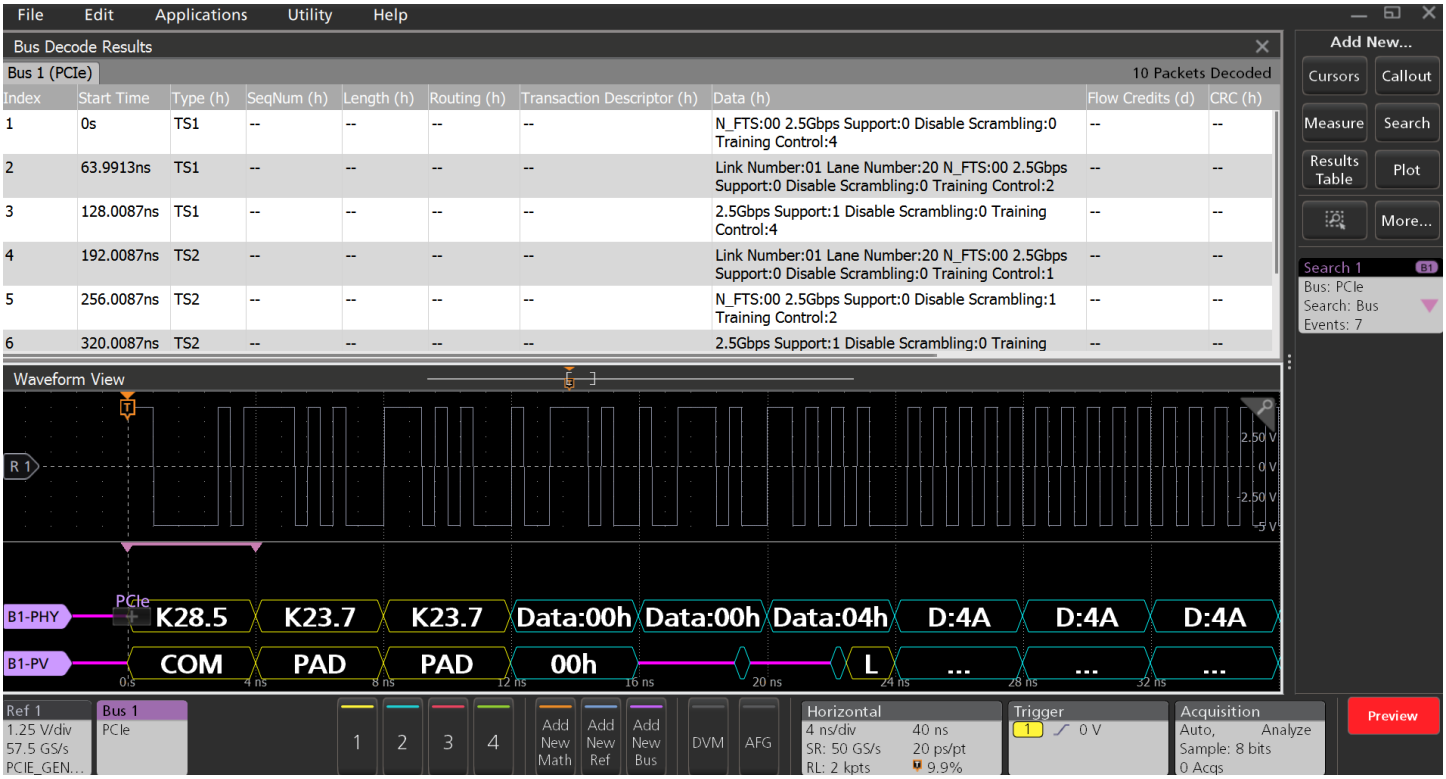
Characteristic	Description
PCIe Source(s)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Speed	2.5 Gbps 5 Gbps 8 Gbps 16 Gbps
Link Width	X1
Packet View	On Off
Formats Available	Hex Binary Mixed Hex

Display modes

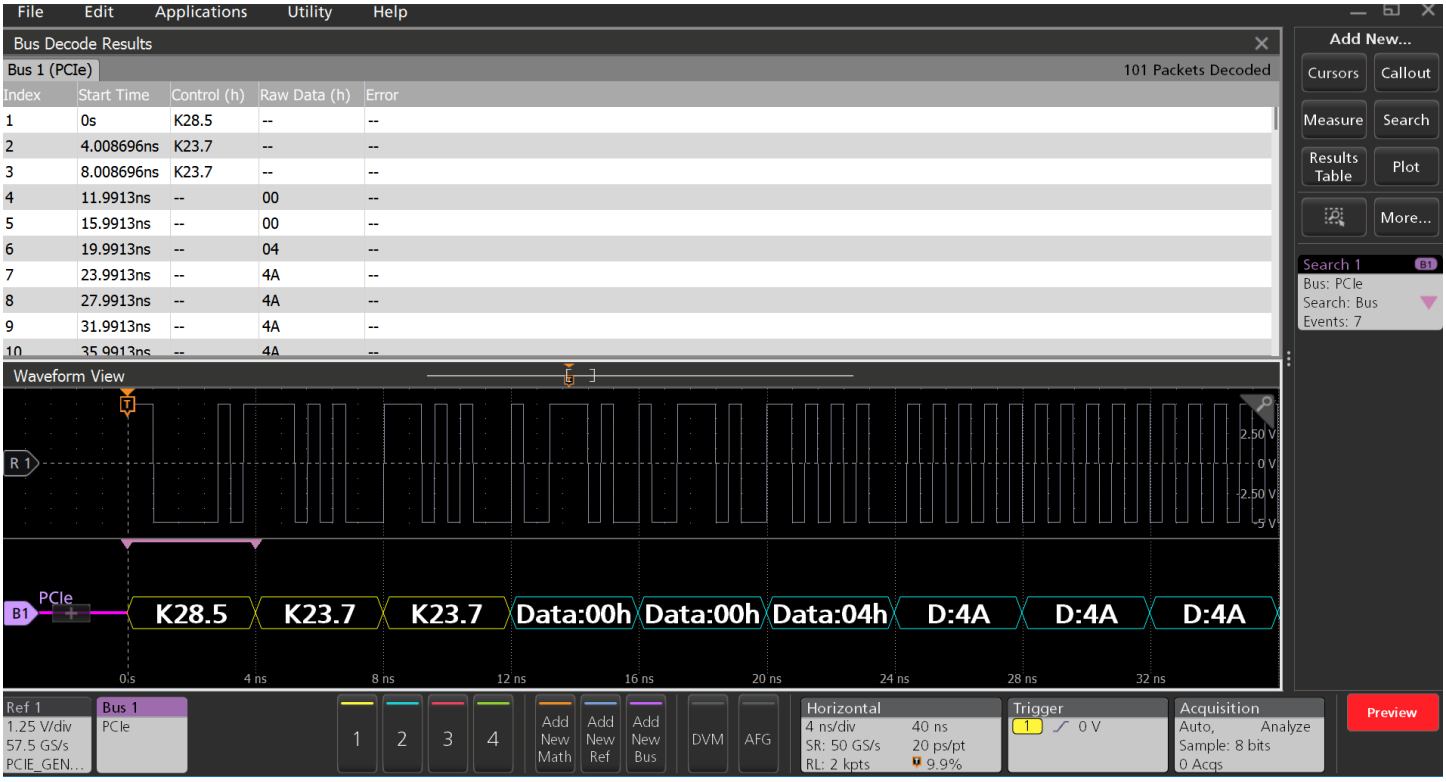
Characteristic	Description
Bus	Bus Bus and Waveforms
Results Table	Decoded packet data in a tabular view

Bus Search options

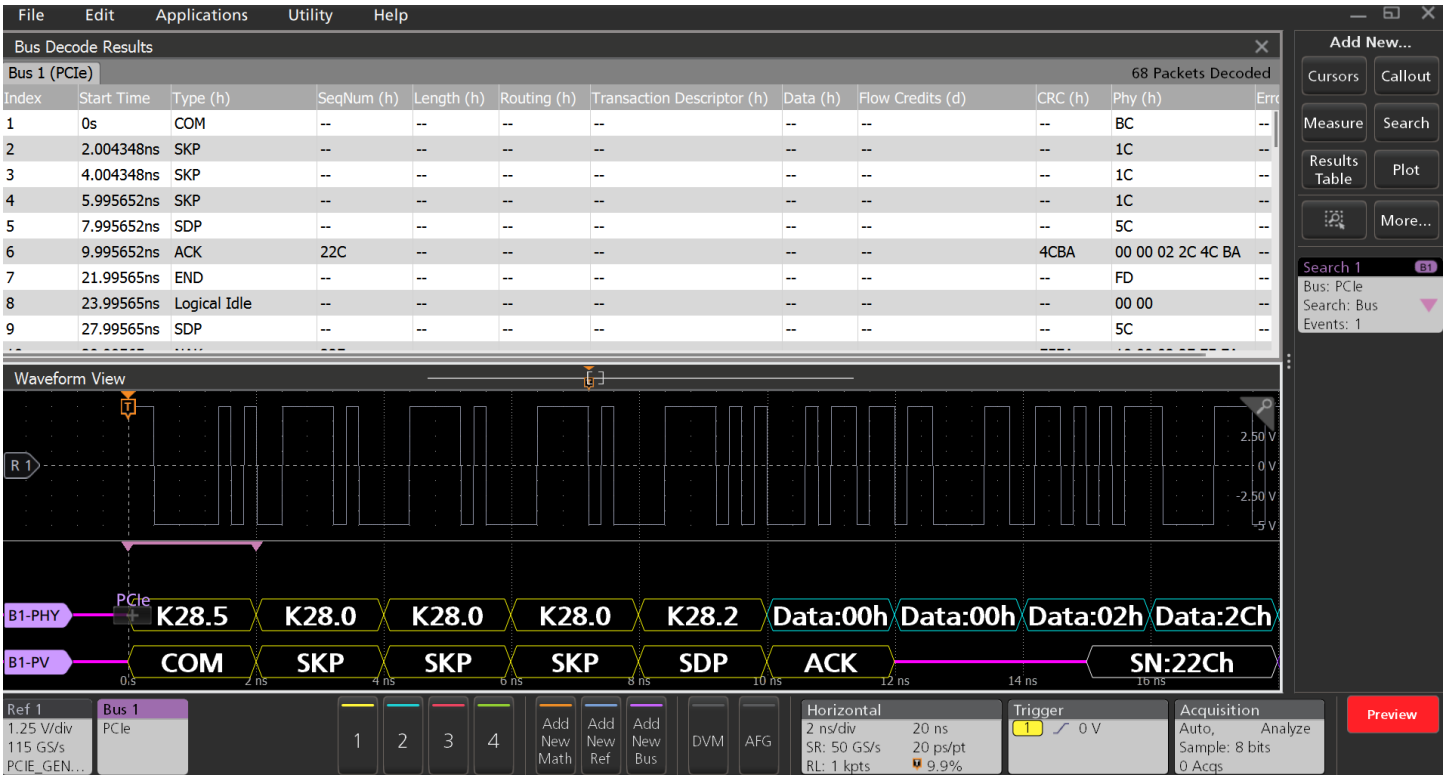
Characteristic	Description
Search On	▪ Control Characters: COM, EDB, END, FTS, IDL, PAD, SDP, SKP, STP, EIE, Any ▪ Sync Header ▪ DLLP: ACK/NAK, Power Management, Flow Control ▪ TLP: Memory, I/O, Config, Message, Completion, Atomic Operation, Prefix, Any ▪ Ordered Set: TS1, TS2, EIOS, EIEOS, FTS, SKP, SDS ▪ Custom Pattern ▪ Errors: CRC, LCRC, ECRC, Frame Length, Disparity, Symbol, Packet
Search On with Packet View Off	▪ Sync Header: Ordered Set, Data, Invalid ▪ Ordered Set: TS1, TS2, EIOS, EIEOS, FTS, SKP, SDS, CSKP ▪ Custom Pattern: Control, Data, Either ▪ Compliance Pattern: 36, 65792 (non-SRIS), 67585 (SRIS), 67585n (SRIS of pcieg4)
Search On with Packet View On	▪ Ordered Set: TS1, TS2, EIOS, EIEOS, FTS, SKP, SDS, CSKP ▪ DLLP: ACK/NAK, Power Management, Flow Control, Vendor-Specific, NOP, Data Link Feature, MR IOV ▪ TLP: Memory, I/O, Config, Message, Completion, Atomic Operation, Prefix, Any ▪ Errors: CRC, LCRC, ECRC, Frame Length, Disparity, Symbol, Packet



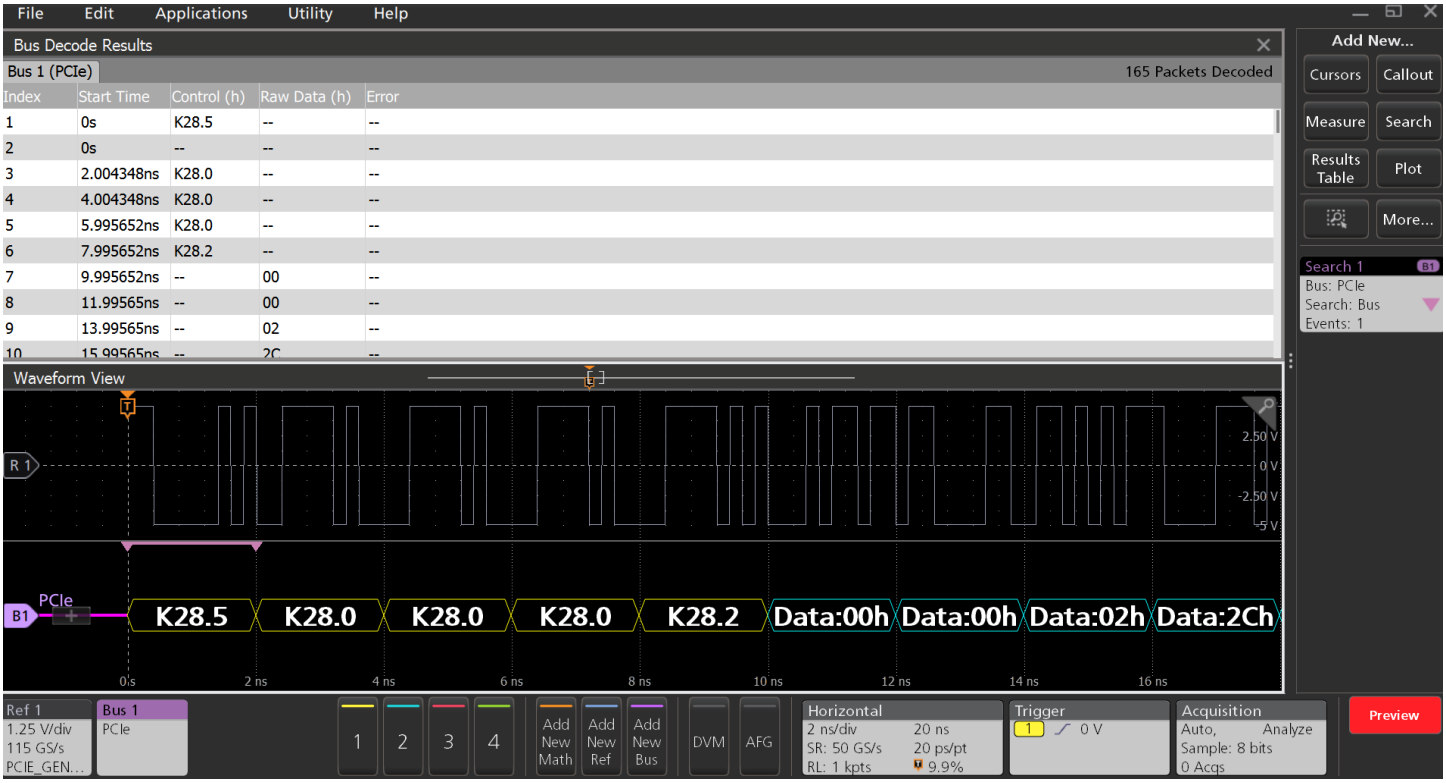
PCIe Gen 1 bus setup and MixedHex display, showing decode with packet view on. K28.5 searched with 7 occurrences.



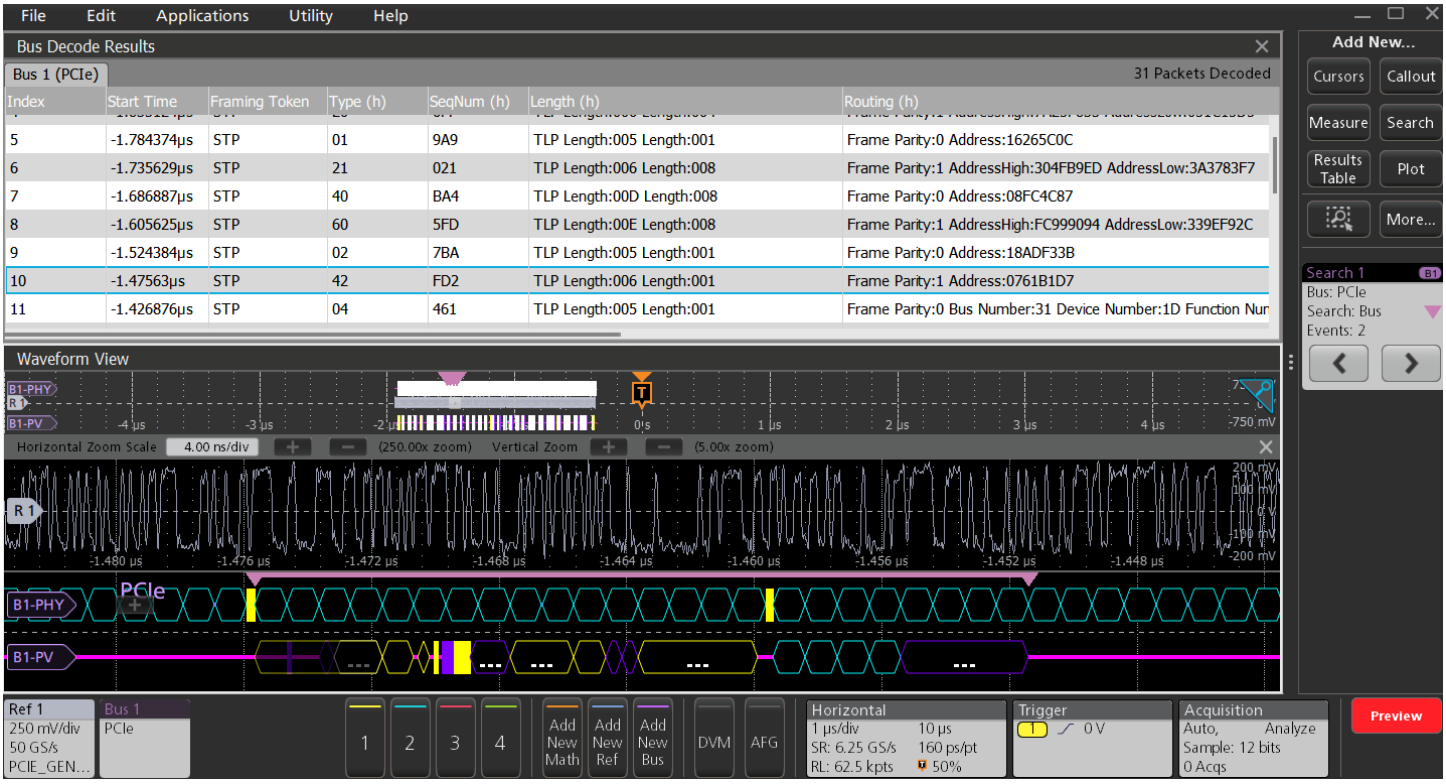
PCIe Gen 1 bus setup and MixedHex display, showing decode with packet view off. K28.5 searched with 7 occurrences.



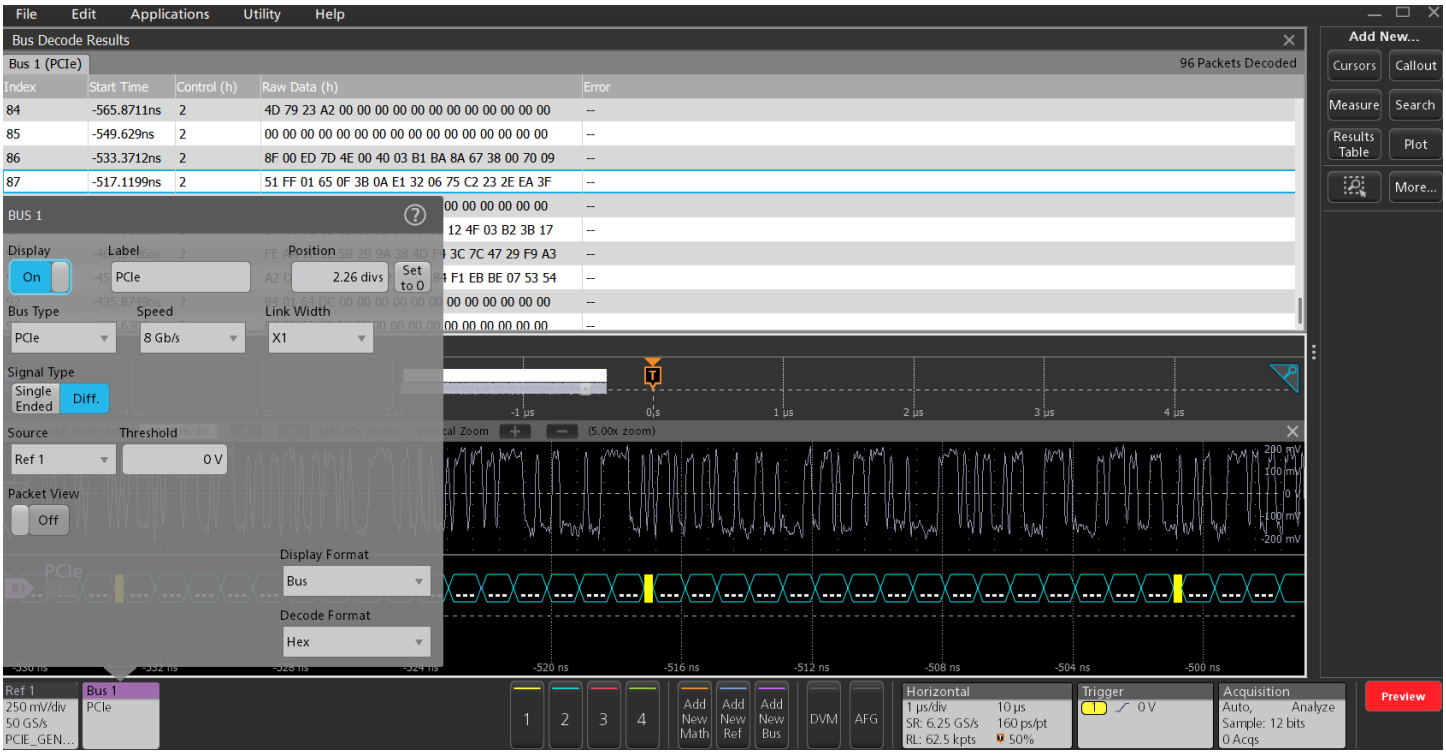
PCIe Gen 2 bus setup and MixedHex display, showing decode with packet view on. K28.5 searched with 1 occurrence.



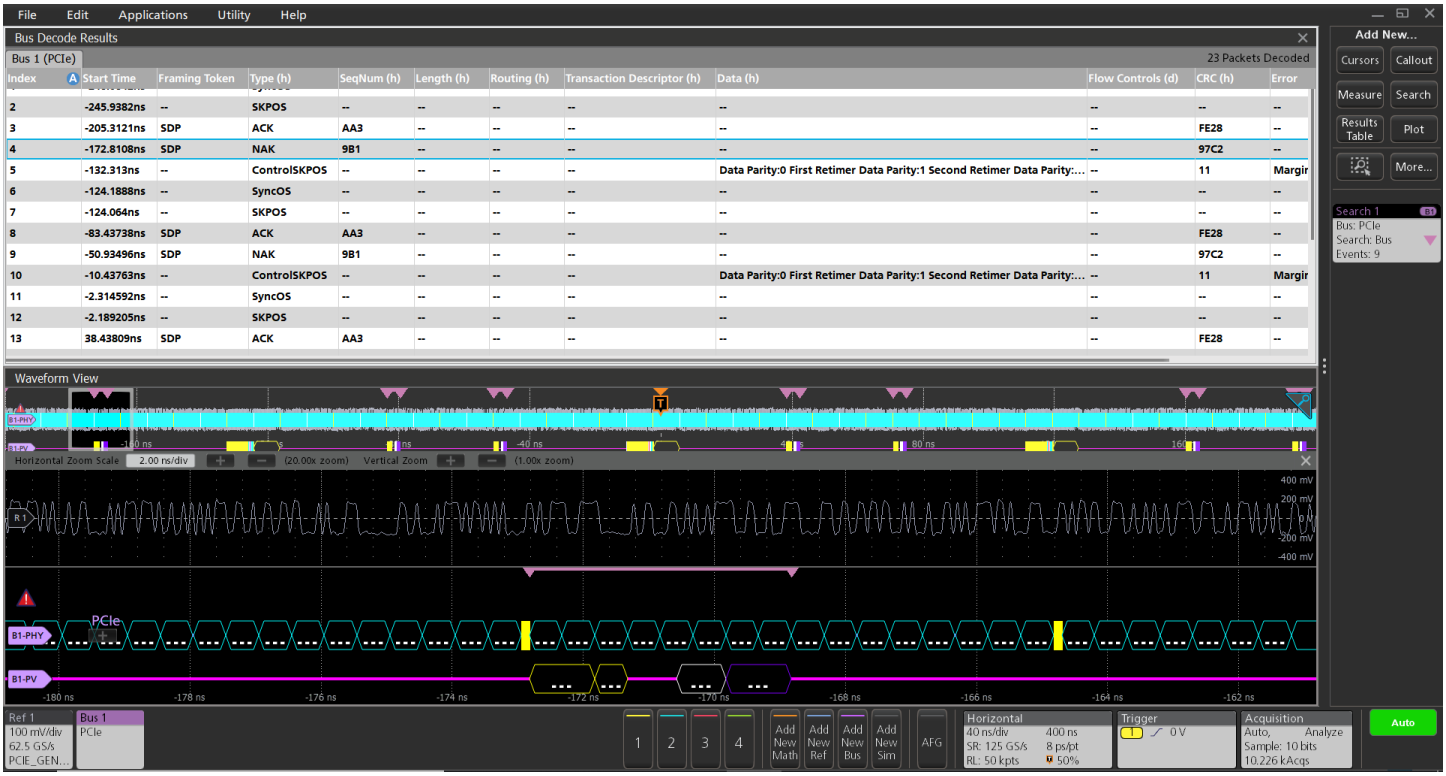
PCIe Gen 2 bus setup and MixedHex display, showing decode with packet view off. K28.5 searched with 1 occurrence.



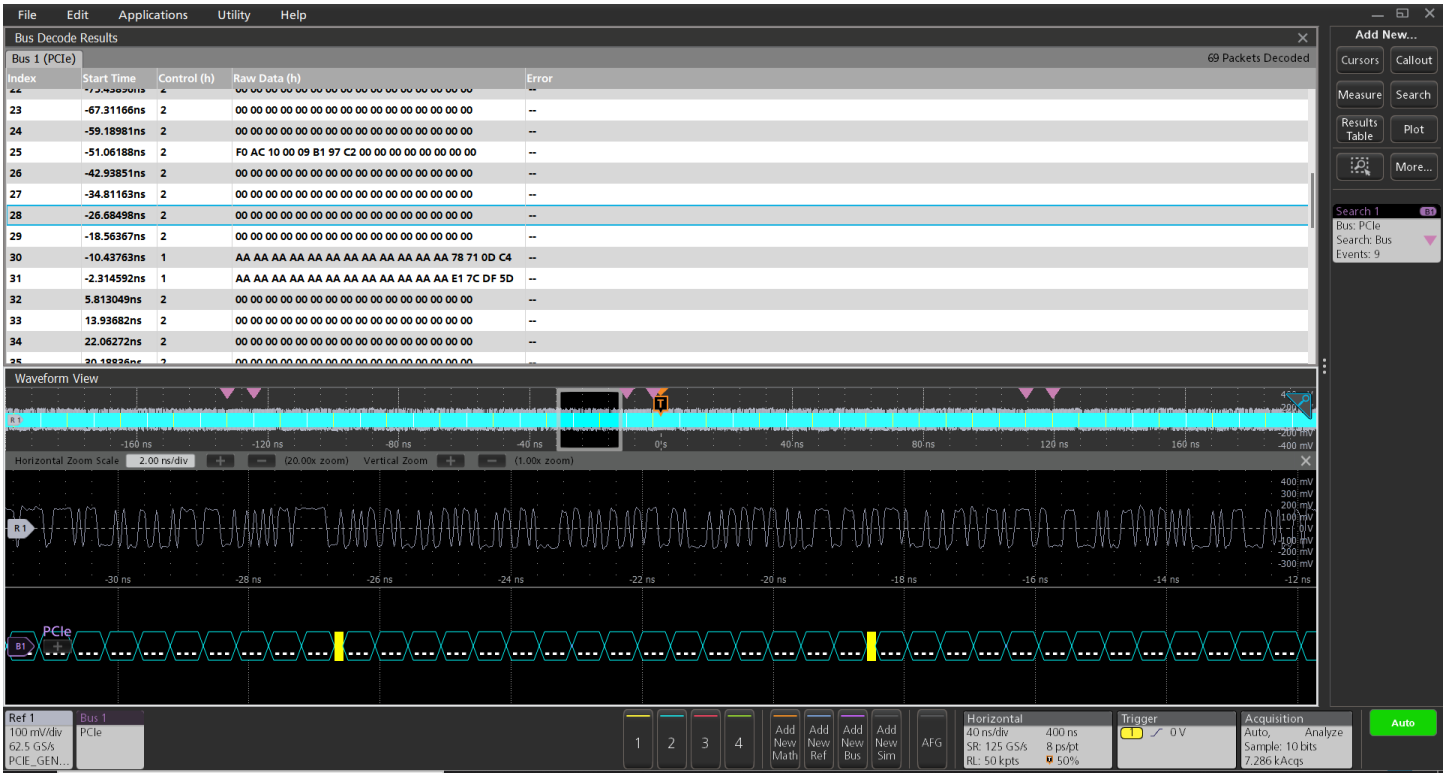
PCIe Gen 3 bus showing decode with packet view on. TLP packet of type I/O searched with 2 occurrences.



PCIe Gen 3 bus setup showing decode with packet view off.



PCIe Gen4 bus showing decode with packet view On. DLP packet of type ACK was searched with 9 occurrences.



PCIe Gen4 bus setup showing decode with packet view Off.

PSI5 characteristics (Version 2.1)

Bus setup options

Characteristic	Description	
PSI5 Sources	Analog channels Digital channels Active Math channels Active Reference channels	
Thresholds	Per-channel thresholds	
Recommended Probing	Sensor to ECU	Current probe with minimum current rating of less than 50mA - TCP2020, TCP202A
	ECU to Sensor	Differential Voltage probe - TDP1000, TDP1500, and TAP1500
Direction	ECU to Sensor Sensor to ECU	
Direction -Sensor to ECU	Mode	Slow (83.3 kbps) Standard (125 kbps) Fast (189 kbps)
	Data A	10 - 24 bits
	Data B	0 - 12 bits
	Frame Control	0 - 4 bits
	Status	0 - 3 bits
Direction - ECU to Sensor	Sync Bit Period	1 us to 300 us
	Sync Mode	Pulse Width Tooth Gap
	Data Format	Nibble Byte

Bus setup options

Characteristic	Description
Decode Format	Hex Binary Mixed Hex

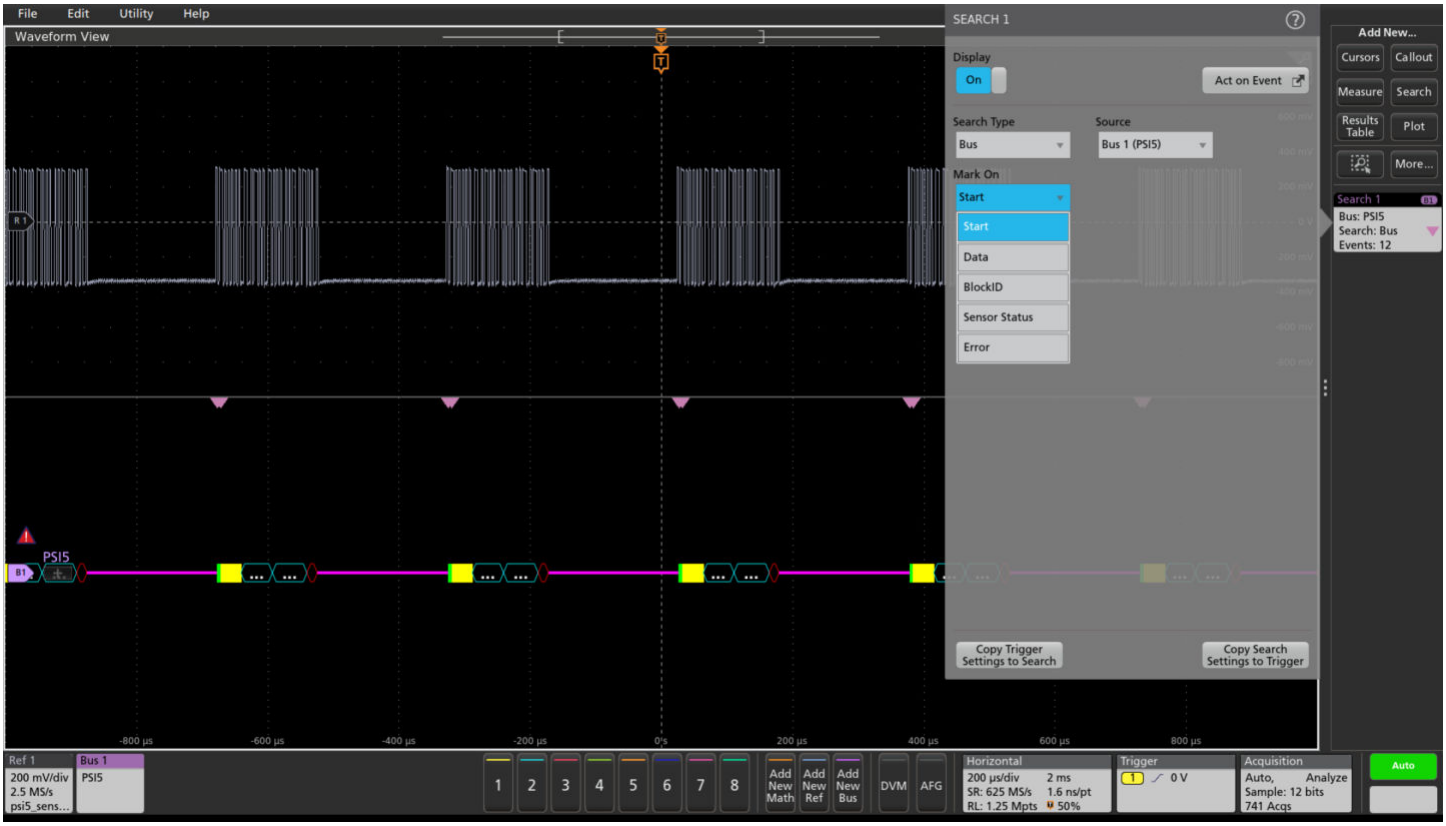
Display modes

Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous displays bus and digital waveforms
Results Table	Decoded packet data in a tabular view

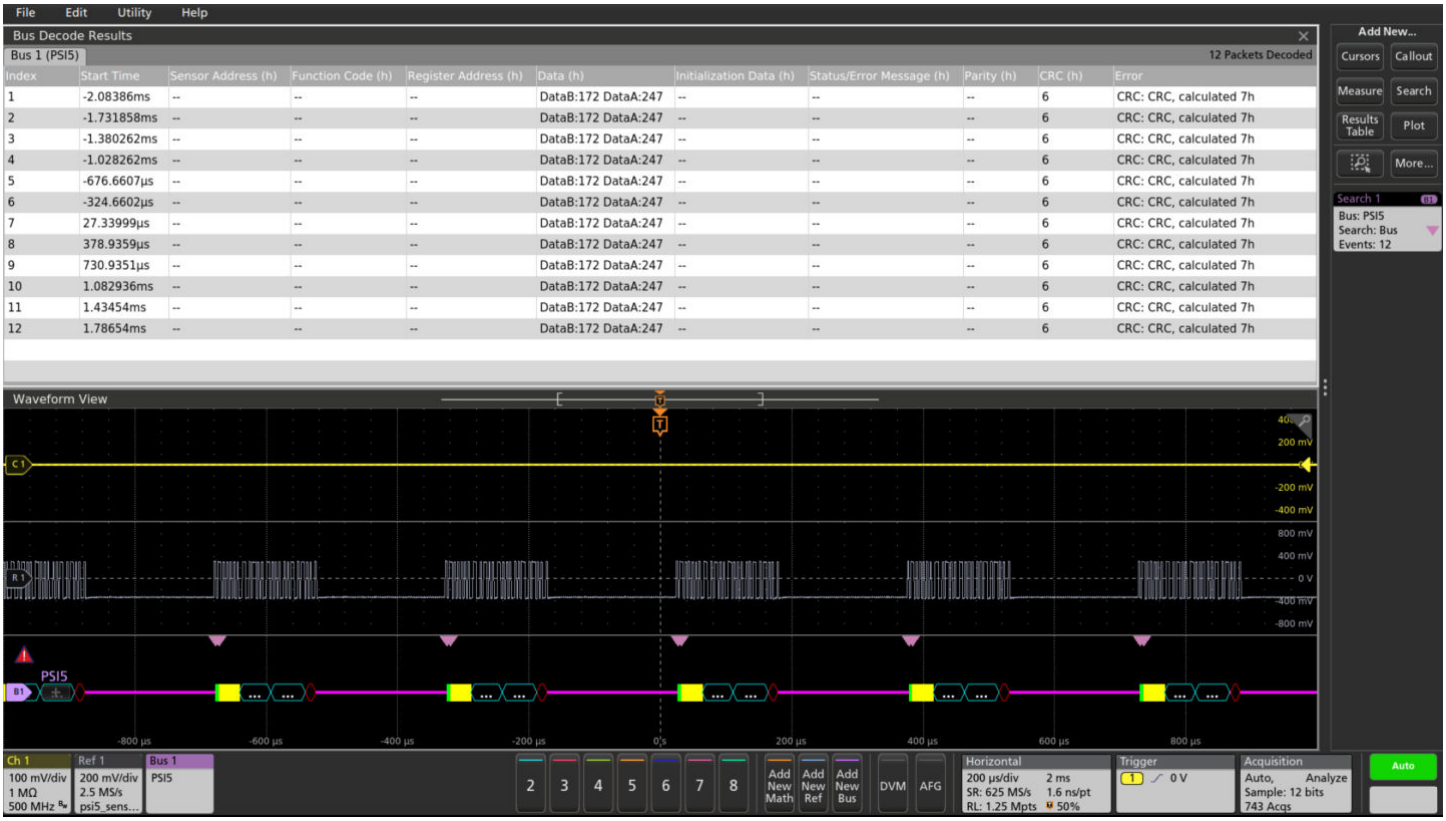
Bus search options

Characteristic	Description	
Mark On	Direction - Sensor to ECU	Start [Start of packet] Status Data [Region B and Region A] Block ID Sensor Status [5 different status] Errors [Parity CRC and any]
	Direction - ECU to Sensor	Start [Start of packet] Status Data [4 or 8 bits] Function Code Sensor Address Register Address CRC Error

NOTE: Bus Search option is depend on the direction in Bus Configuration.



PSI5 Search configuration.



PSI5 Results table.

RS-232, RS-422, RS-485, UART characteristics

Bus setup options

Characteristic	Description
Sources, RS-232, UART	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Sources, RS-422, RS-485	Analog channels Active Math channels[1] Active Reference channels[1]
Polarity	Normal (RS-232) Inverted (UART, RS-422, RS-485)
Parity	None Odd Even
Recommended Probing, RS-232, UART	Single-ended
Recommended Probing, RS-422, RS-485	Differential
Number of Bits	7 - 9
Formats Available	Hex Binary ASCII Packet View
Data Inputs	One, Two
Bit Order	MSB, LSB
Maximum Clock/Data Rate	Up to 15 Mb/s For 3 Series MDO: Up to 10 Mb/s

Bus setup options

Characteristic	Description
Bit Rate Selection	300 b/s 1,200 b/s 2,400 b/s 9,600 b/s 19,200 b/s 38,400 b/s 115,200 b/s 921,600 b/s Custom (All but 3 Series MDO: 50 b/s - 15 Mb/s Custom (for 3 Series MDO): 50 b/s - 10 Mb/s

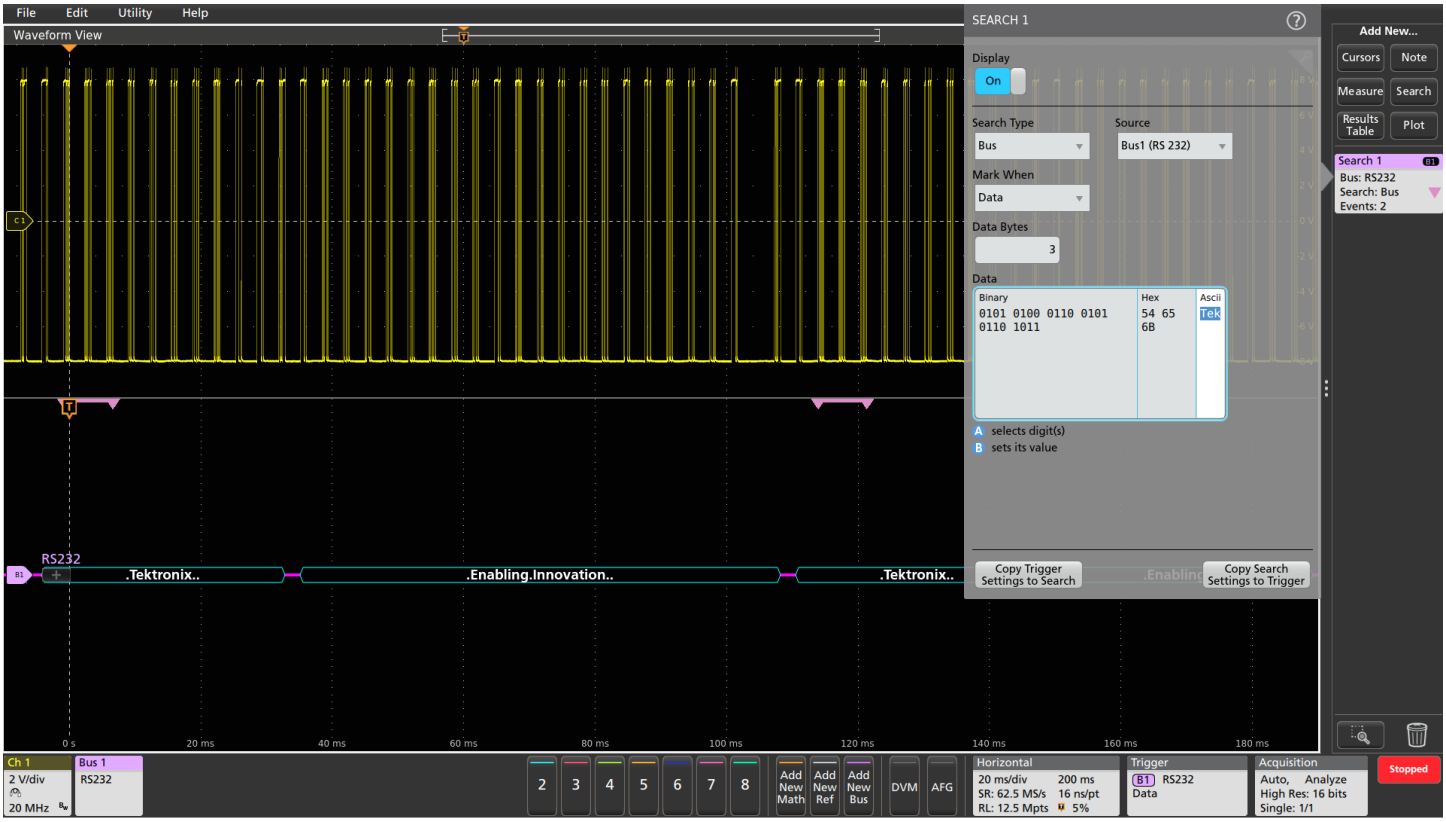
Display modes

Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	Start End of Packet Data (1 - 10 bytes) Parity Error

RS-232 bus setup and ASCII display, showing assignment of source signal, digital threshold, and polarity.



RS-232 bus shown in Packet View format, with the Wave Inspector search automatically searching for the data string "Tek".

SDLC Characteristics (Version GA27-3093-3)

Bus setup options

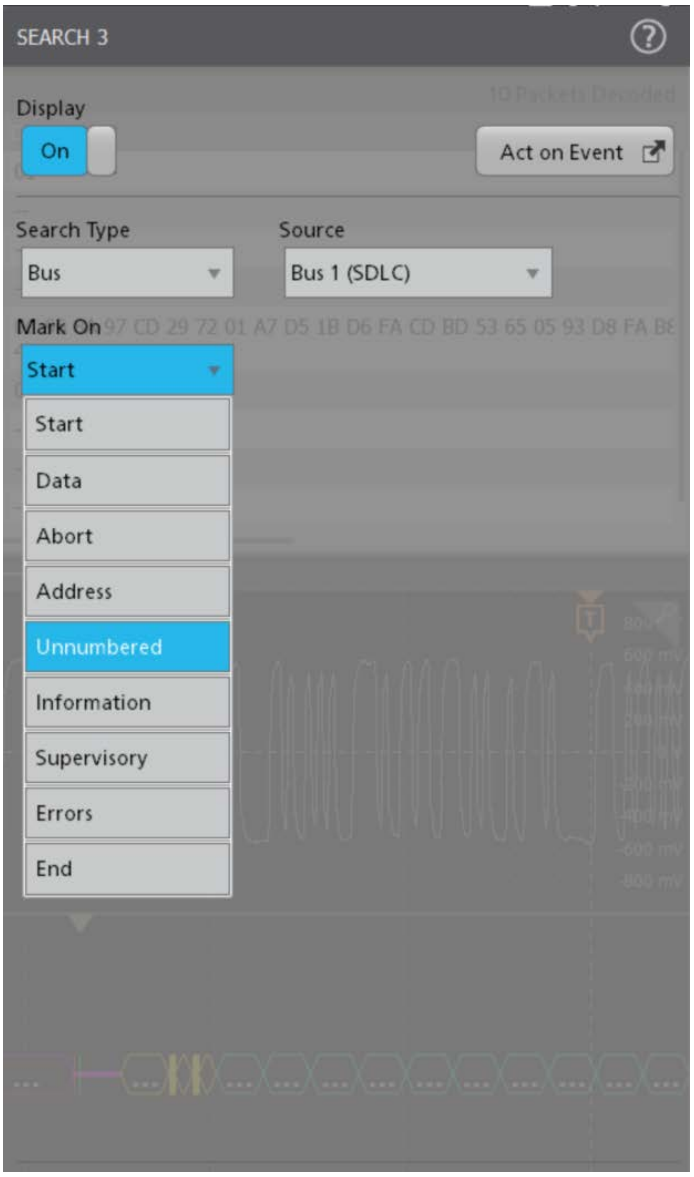
Characteristic	Description
SDLC Source(s)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Pre-channel thresholds
Recommended Probing	Differential
Modulo	8 [8-bit Control Word] 128 [16-bit Control Word]
Encoding	Discrete Transmission [NRZ] Invert On Zero [Inverted NRZi]
Formats Available	Hex Binary Mixed Hex

Display modes

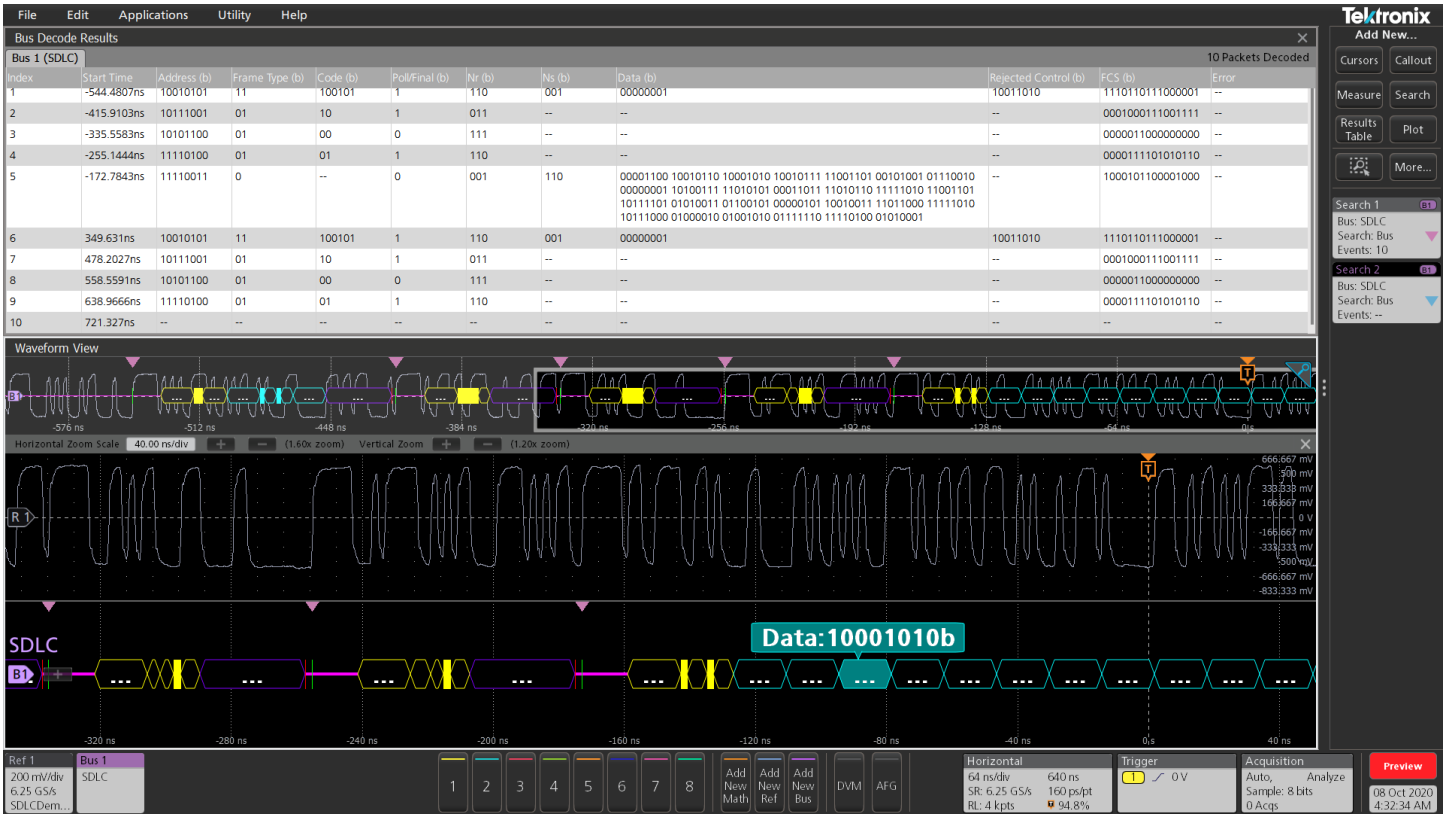
Characteristic	Description
Bus	Bus Only
Result Table	Decoded packet data in a tabular view

Bus search options

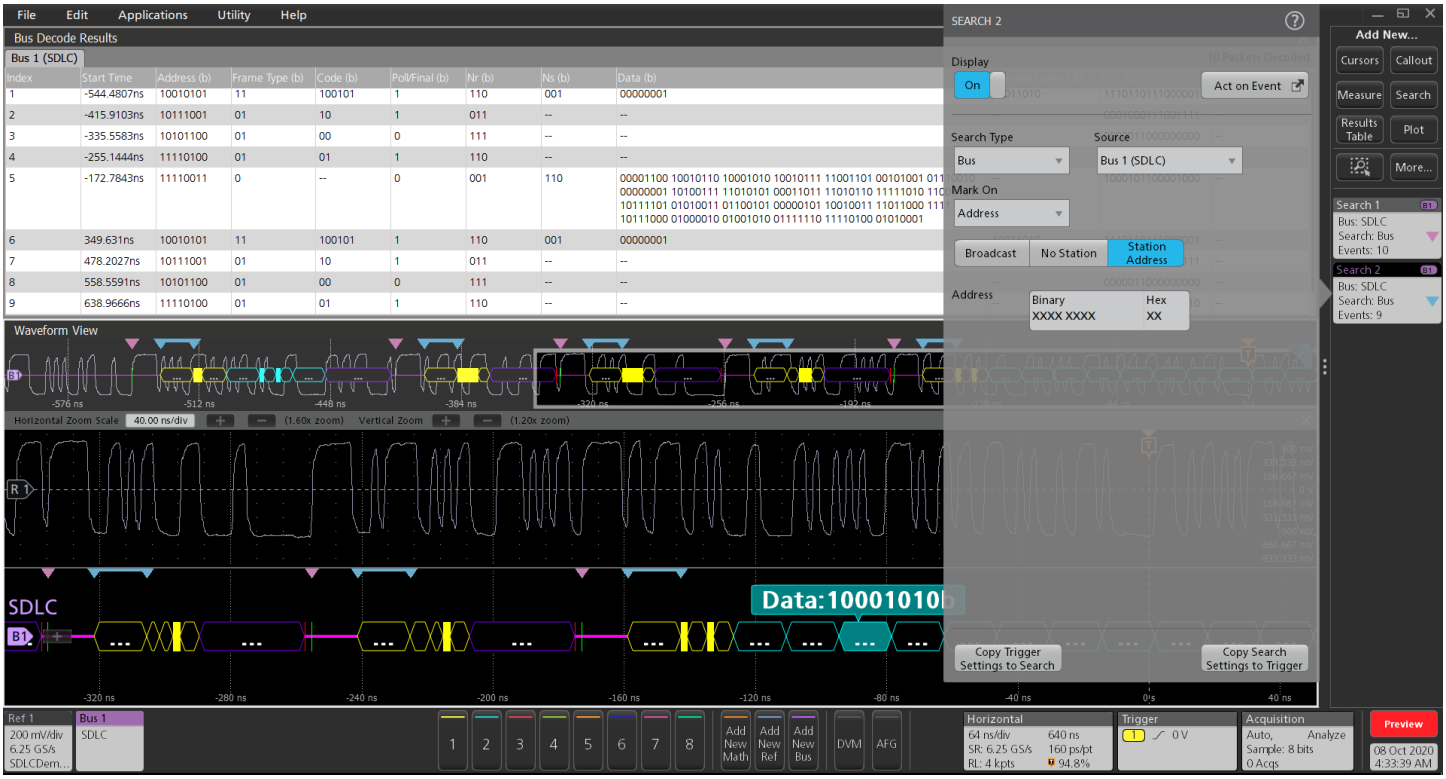
Characteristic	Description
Search On	Start [Searches for Start event] Data [Searches for Payload Data] Abort [Searches for Abort] Address Broadcast [Broadcast Packets] No Station [Packets not pertaining to secondary] Station [Valid Station Address] Unnumbered Commands [Searches for Primary Commands] Responses [Searches for Secondary Responses] Both Information [Searches for information frames] Supervisory [Searches for different receiver status] Receive Frame Ready Receive Frame Not Ready Reject frame Errors FCS [Searches for Frame Check Sequence errorrs] Out of Numeric Order [Searches for this frame] Stop



Bus search options



The Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the SDLC bus.



Searching on a station address on the SDLC bus.

SENT Characteristics1

Bus setup options

Characteristic	Description
SENT source	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Recommended Probing	Single-ended
Polarity	Normal Inverted
Clock Tick	1 µs - 300 µs
Tick Tolerance	1% - 30%
Fast Data Channels	1 or 2
Data Nibbles (1 Fast Data Channel)	3, 4, or 6 nibbles
Channel Widths (C1/C2) (2 Fast Data Channels)	12/12, 14/10, or 16/8 bits
Pause Pulse	Yes No
Slow Channel	None Enhanced w/ 4-bit ID Enhanced w/ 8-bit ID Short
Formats Available	Mixed Hex Binary Hex Mixed Decimal
Maximum Clock/Data Rate	Up to 10 Mb/s (for automated decoding of bus)

Display modes

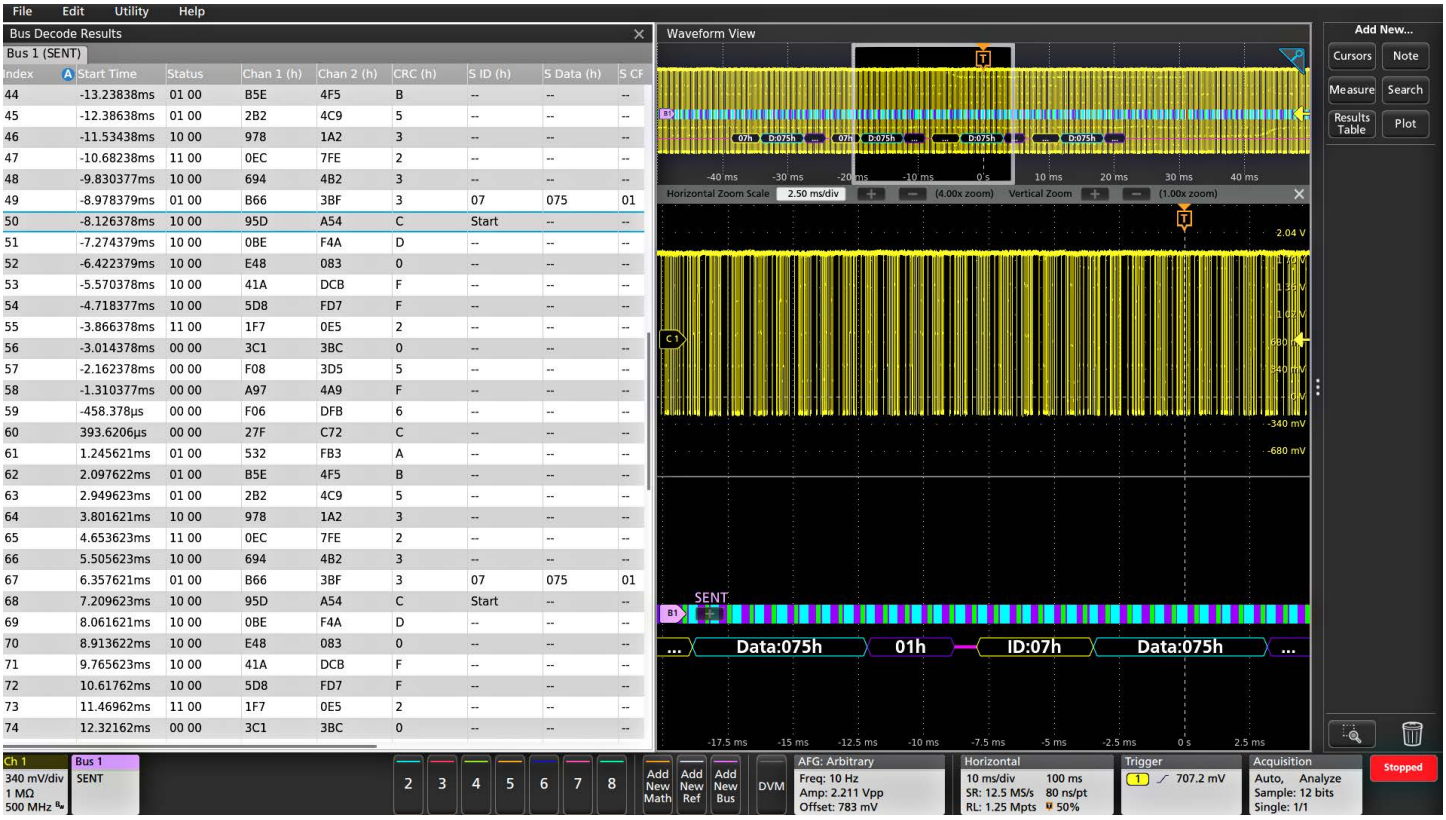
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger options

Characteristic	Description
Trigger On	Start of Packet Fast Channel(s) (Status/Communication, Data) Slow Channel (Message ID, Data) CRC Error (Fast channel, Slow channel)

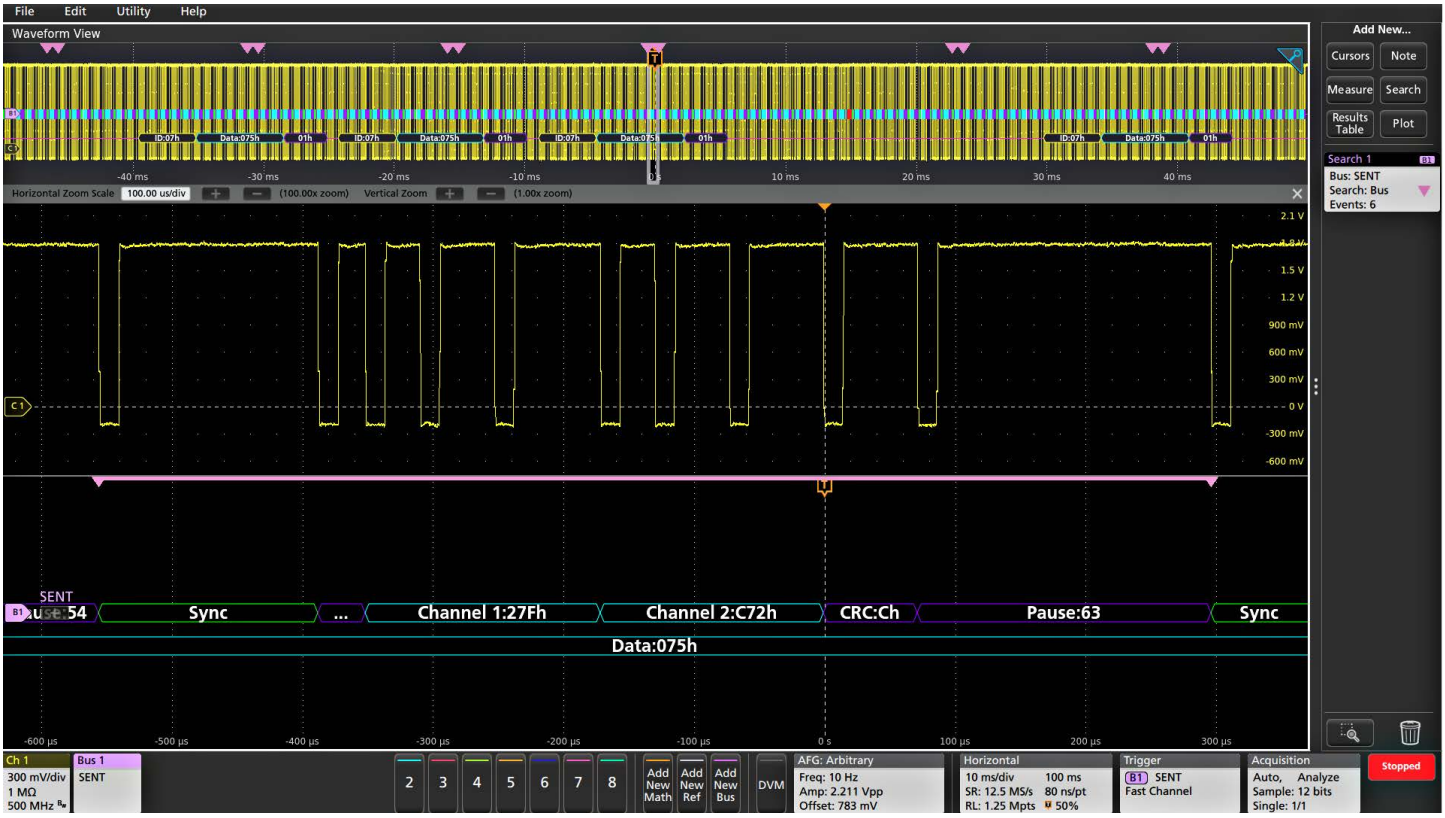
Bus search options

Characteristic	Description
Search On	Start of Packet Fast Channel(s) (Status/Communication, Data) Slow Channel (Message ID, Data) Pause Pulse (Number of Ticks) Error (Frame Length, Fast channel CRC, Slow channel CRC)



Index	Start Time	Status	Chan 1 (h)	Chan 2 (h)	CRC (h)	S ID (h)	S Data (h)	S CF
44	-13.23838ms	01 00	B5E	4F5	B	--	--	--
45	-12.38638ms	01 00	2B2	4C9	5	--	--	--
46	-11.53438ms	10 00	978	1A2	3	--	--	--
47	-10.68238ms	11 00	0EC	7FE	2	--	--	--
48	-9.830377ms	10 00	694	4B2	3	--	--	--
49	-8.978379ms	01 00	B66	3BF	3	07	075	01
50	-8.126378ms	10 00	95D	A54	C	Start	--	--
51	-7.274379ms	10 00	0BE	F4A	D	--	--	--
52	-6.422379ms	10 00	E48	083	0	--	--	--
53	-5.570378ms	10 00	41A	DCB	F	--	--	--
54	-4.718377ms	10 00	5D8	FD7	F	--	--	--
55	-3.866378ms	11 00	1F7	0E5	2	--	--	--
56	-3.014378ms	00 00	3C1	3BC	0	--	--	--
57	-2.162378ms	00 00	F08	3D5	5	--	--	--
58	-1.310377ms	00 00	A97	4A9	F	--	--	--
59	-458.378µs	00 00	F06	DFB	6	--	--	--
60	393.6206µs	00 00	27F	C72	C	--	--	--
61	1.245621ms	01 00	532	FB3	A	--	--	--
62	2.097622ms	01 00	B5E	4F5	B	--	--	--
63	2.949623ms	01 00	2B2	4C9	5	--	--	--
64	3.801621ms	10 00	978	1A2	3	--	--	--
65	4.653623ms	11 00	0EC	7FE	2	--	--	--
66	5.505623ms	10 00	694	4B2	3	--	--	--
67	6.357621ms	01 00	B66	3BF	3	07	075	01
68	7.209623ms	10 00	95D	A54	C	Start	--	--
69	8.061621ms	10 00	0BE	F4A	D	--	--	--
70	8.913622ms	10 00	E48	083	0	--	--	--
71	9.765623ms	10 00	41A	DCB	F	--	--	--
72	10.61762ms	10 00	5D8	FD7	F	--	--	--
73	11.46962ms	11 00	1F7	0E5	2	--	--	--
74	12.32162ms	00 00	3C1	3BC	0	--	--	--

Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the SENT bus.



Triggering on a specific Fast Channel Status and data pattern on the SENT bus and automatically searching on the same data pattern.

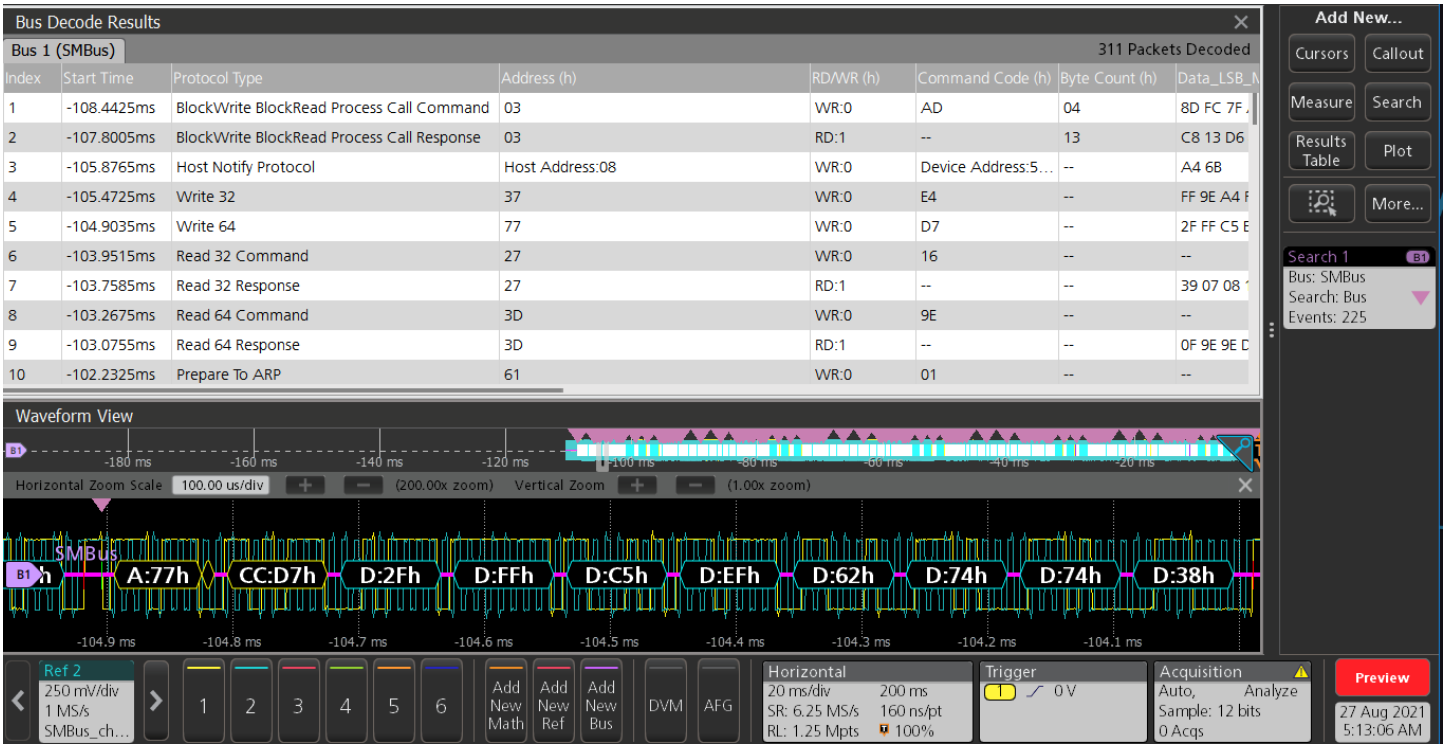
SMBus characteristics

Bus setup options

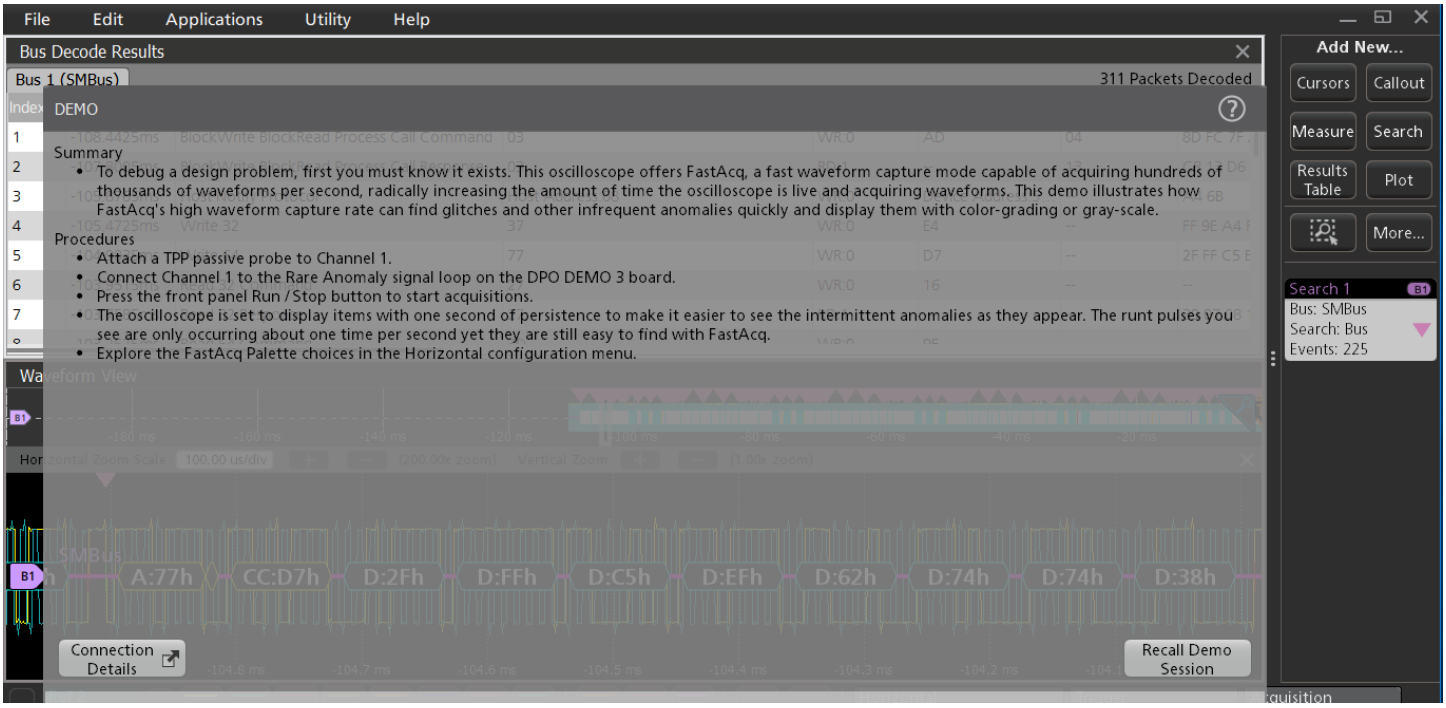
Characteristic	Description
SMBus sources	Analog channels Digital channels Active math channels Active reference channels
Salient features	Decode capability for SMBus protocol with PEC Byte as optional. Search capability for Start, Repeated Start, Stop, and Idle events. Search capability for addresses such as Host Address, Device Address, and Address. Search capability for Command Code, Data and UDID Data. Search capability for Errors – Any, ACK, NACK.
Formats available	Hex Binary Mixed
PEC	Optional PEC Byte – False as default

Bus search options

Characteristic	Description
Search On	**Start:** Select to search on the start events. **Repeated Start:** Select to search on the repeated start events. **Address:** Sets the 7-bit address pattern that you want to search. **Host Address:** Select to search on the host address. **Device Address:** Set the 7-bit device address that you want to search. **Command Code:** Sets the 8-bit command code that you want to search. **Data:** Sets the data pattern that you want to search. **Data Bytes:** Sets the number of data bytes that you want to search (1 to 8 bytes). **Field Bytes:** Sets the field bytes as 1, 2, or 4 for **UDID Data**. **UDID Data:** Sets the UDID data that you want to search. **Error Type:** Sets the error bytes that you want to search. You can search on ANY, ACK, NACK, and PEC errors (PEC error search is available only when the **PEC Byte** in SMBus bus configuration is set as True). **Stop:** Select to search on the stop events. **Idle:** Select to search on the idle events.



The Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the SMBus bus. (PEC Byte set to False).



The DEMO file content provides the information of the SMBus bus.

SpaceWire characteristics (Version: STAR-Dundee user guide)

Bus setup options

Characteristic	Description
SpaceWire Sources (Strobe and Data)	Analog Channels Digital Channels Active Math Channels Active Reference Channels
Thresholds	Per-Channel Thresholds
Recommended Probing	Differential
Address/Data Formats Available	Hex Binary
Maximum Clock/Data Rate	2 Mbits/sec to 200 Mbits/sec

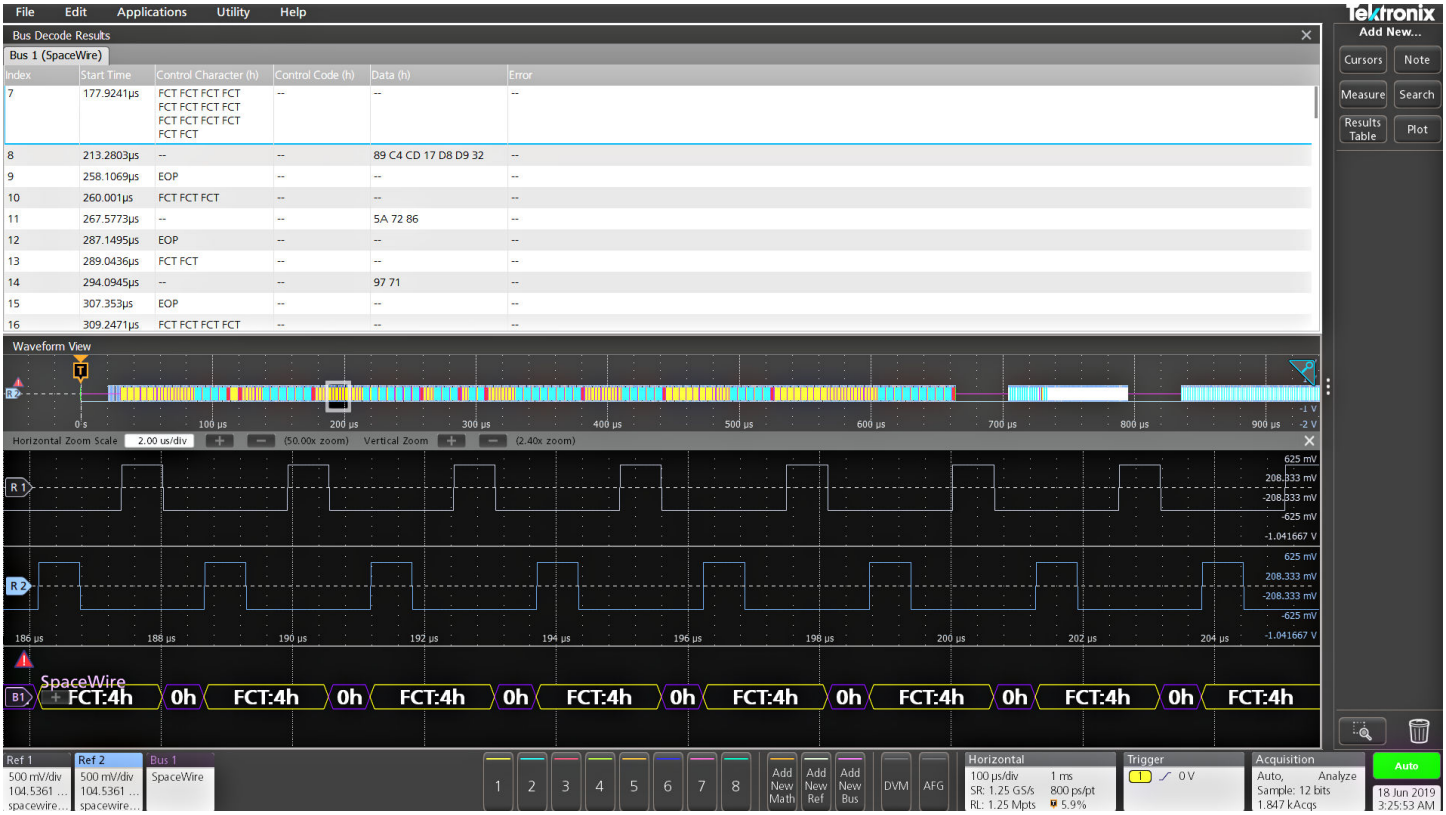
Display modes

Characteristic	Description
Bus	Bus Only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus search options

Characteristic	Description
Search On	Synchronization Control Code Control Character Data Errors

Searching on a specific data pattern on the SpaceWire bus and automatically searching on Sync.



The Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the SpaceWire bus.

SPI characteristics

Bus setup options

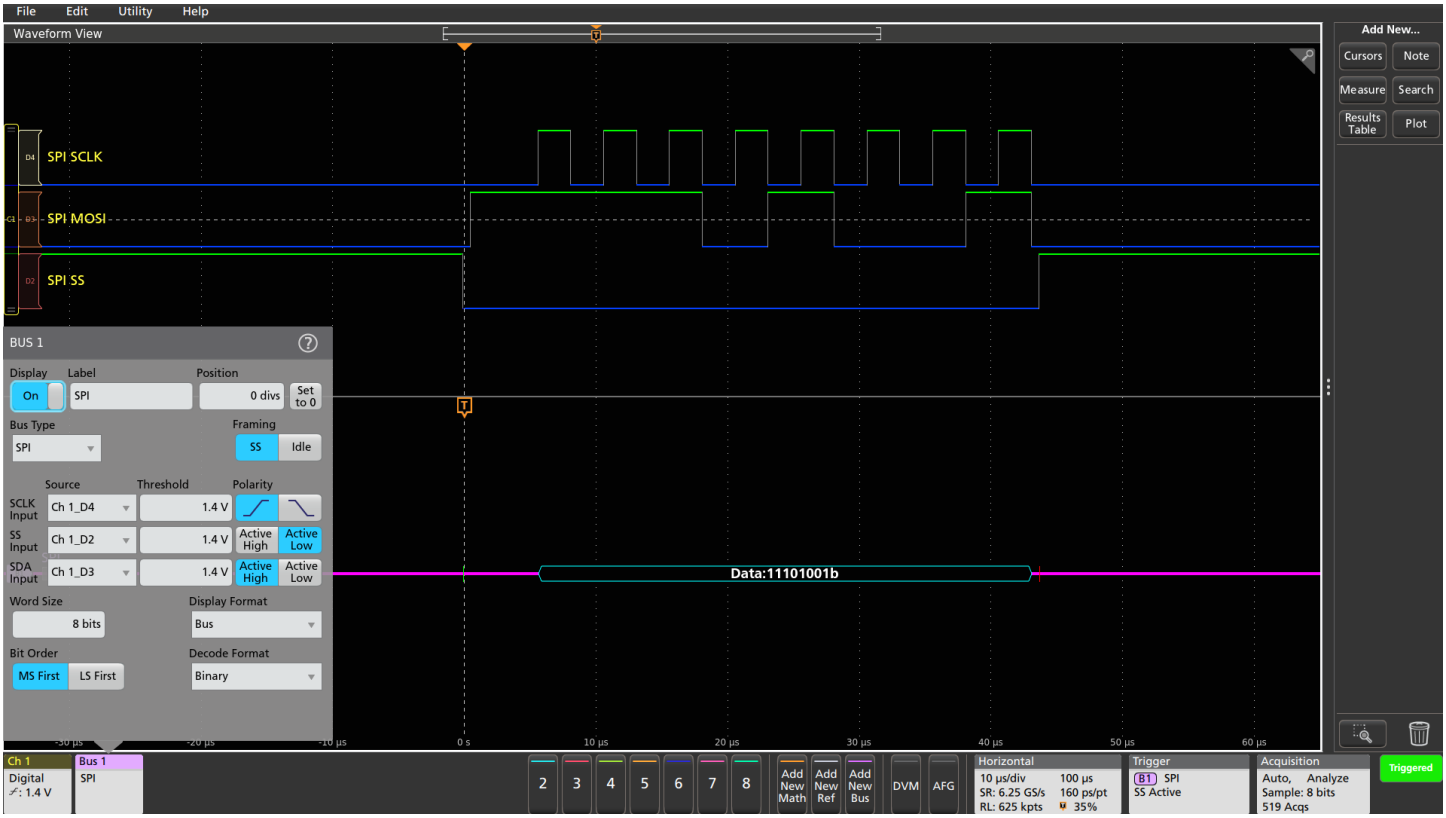
Characteristic	Description
SPI Sources (Clock, Data, and Slave Select)	Analog channels Digital channels Active Math channels[1] Active Reference channels[1]
Thresholds	Per-channel thresholds
Recommended Probing	Single-ended
Decode Configuration: Framing Clock Slave Select Data Word Size Bit Order	Slave Select (3-wire SPI), Idle Time (2-wire SPI) Rising or Falling Edge Active High or Active Low Active High or Active Low 4 - 32 bits Most Significant (MS) First, Least Significant (LS) First
Formats Available	Hex Binary
Maximum Clock/Data Rate	Up to 10 Mb/s (automatic selection)

Display modes

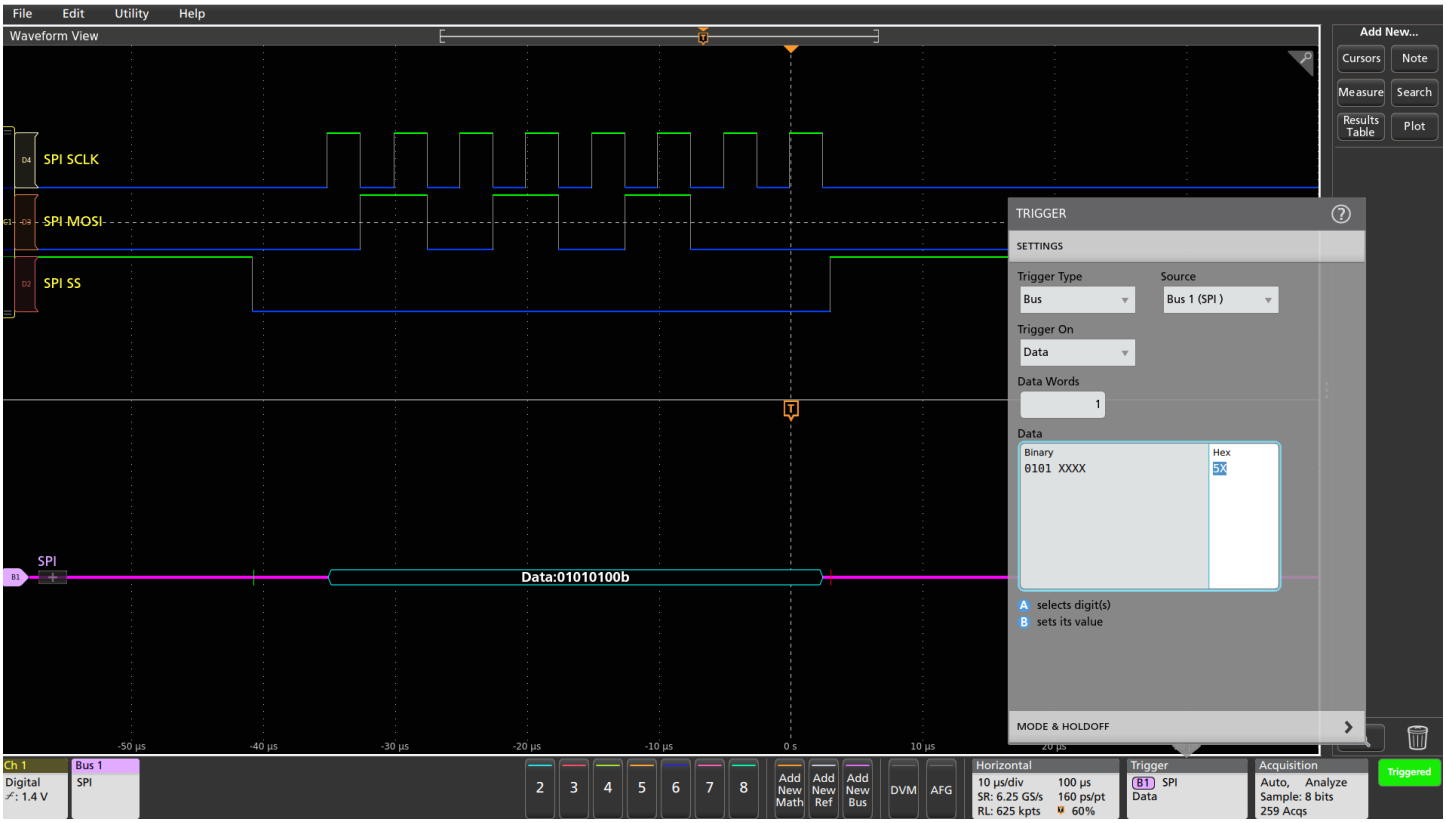
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger and search options

Characteristic	Description
Trigger and/or Search On	SS Active (3-wire SPI) Start of Frame (2-wire SPI) Data (1-16 bytes)



SPI bus, captured with digital channels, showing binary display format of the color-coded SPI bus decoding.



Triggering on a specific data value on the SPI bus.

SPMI characteristics1 (Version 2.0)

Bus setup options

Characteristic	Description
SPMI Sources (Clock and Data)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Recommended Probing	Single-ended
Formats Available	Mixed Hex Hex Binary

Display modes

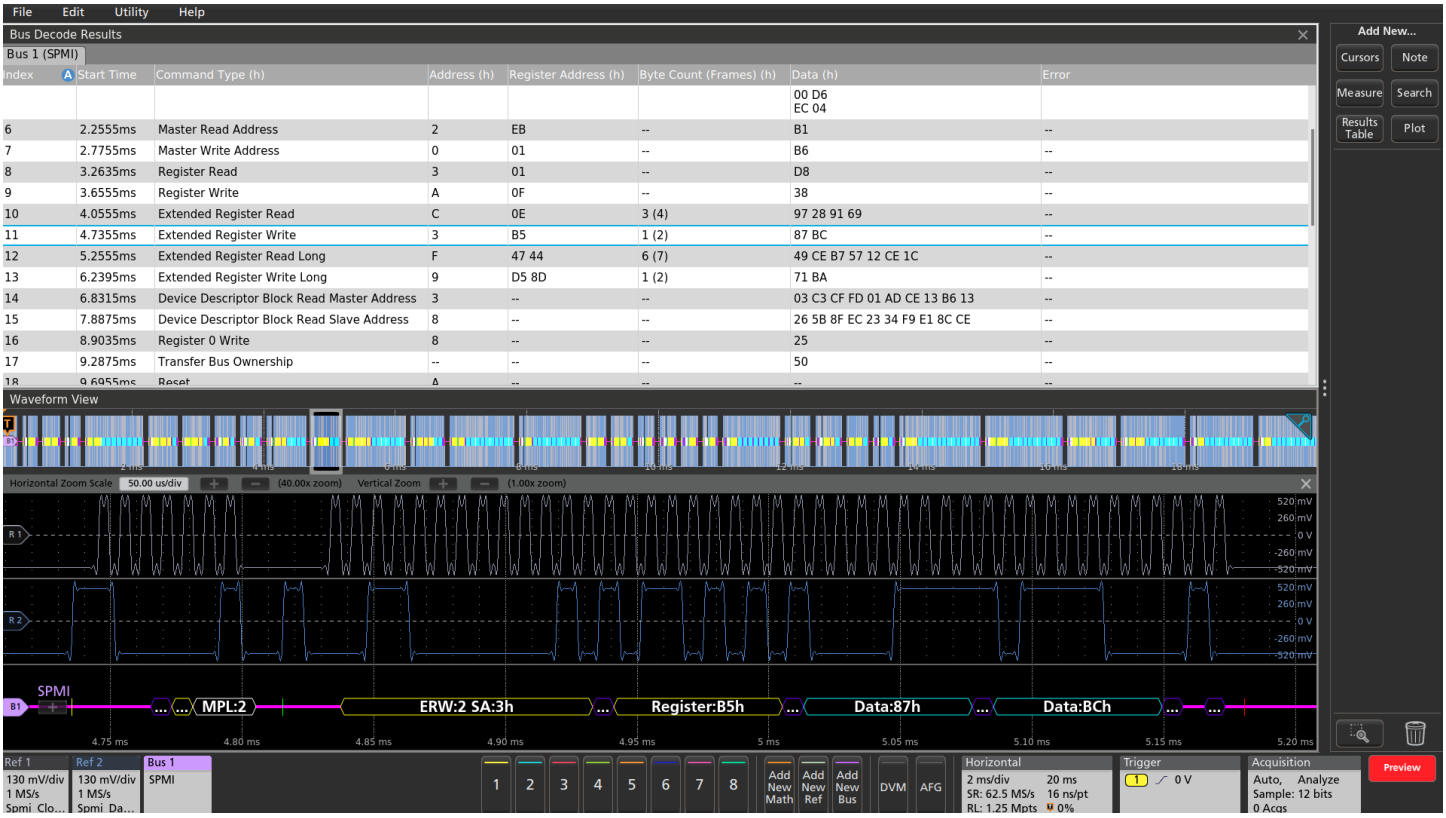
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger options

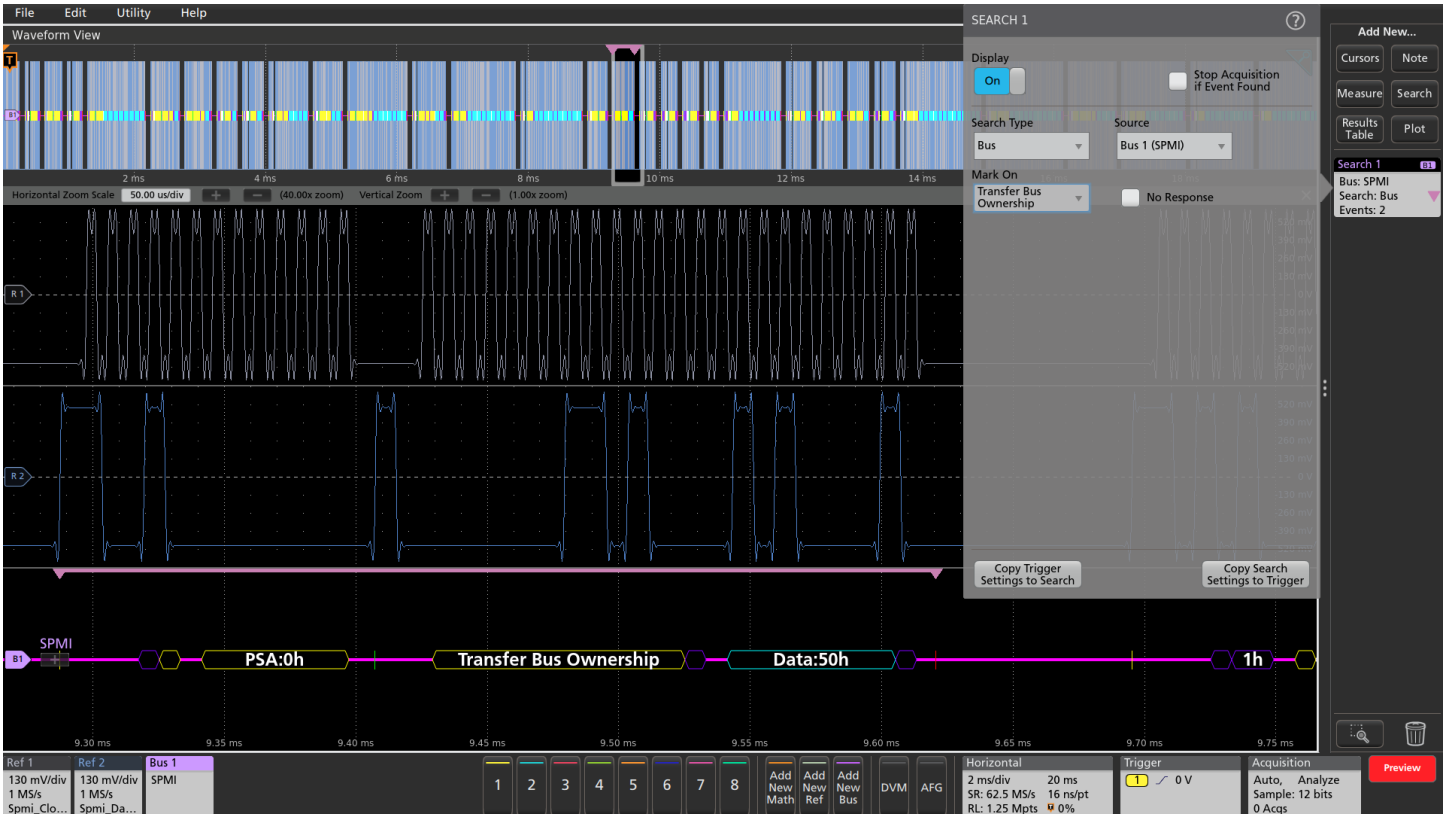
Characteristic	Description
Trigger On	Sequence Start Condition (SSC) Reset Sleep Shutdown Wakeup Authenticate Master Read Master Write Register Read Register Write Extended Register Read Extended Register Write Extended Register Read Long Extended Register Write Long Device Descriptor Block Master Read Device Descriptor Block Slave Read Register 0 Write Transfer Bus Ownership Parity Error

Bus search options

Characteristic	Description
Search On	Sequence Start Condition (SSC) Reset Sleep Shutdown Wakeup Authenticate Master Read Master Write Register Read Register Write Extended Register Read Extended Register Write Extended Register Read Long Extended Register Write Long Device Descriptor Block Master Read Device Descriptor Block Slave Read Register 0 Write Transfer Bus Ownership Parity Error



Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the SPMI bus.



Automatically searching the SPMI bus for the Transfer Bus Ownership command.

SVID characteristics (Version 1.9)

Bus setup options

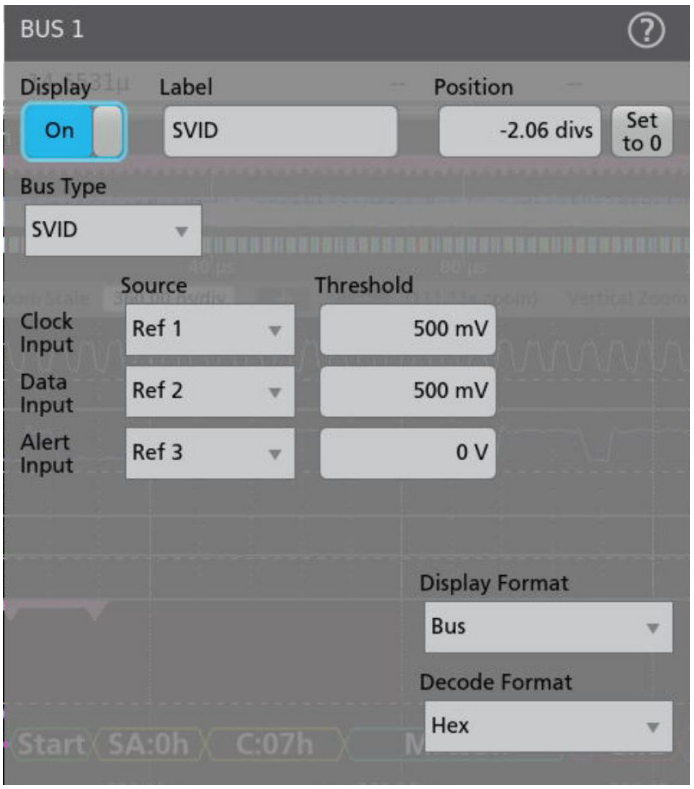
Characteristic	Description
SVID Sources (Clock, Data, Alert)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Pre-channel thresholds
Recommended Probing	Single-ended
Formats Available	Hex Binary Mixed Hex

Display modes

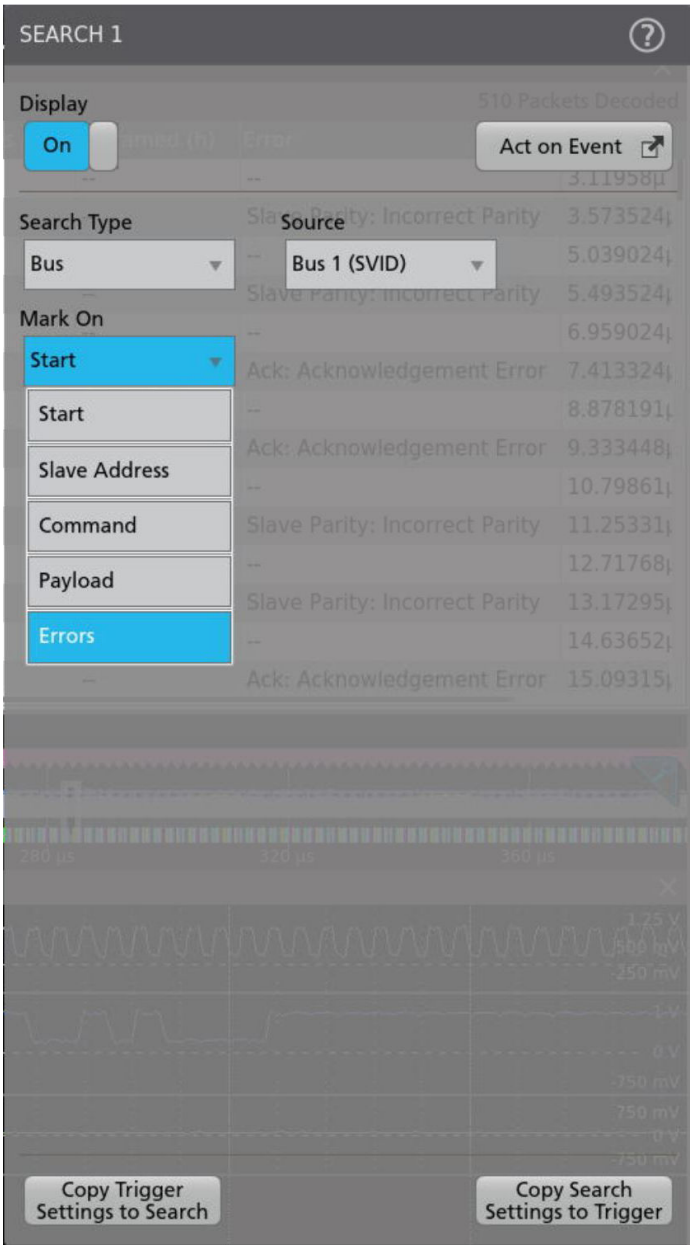
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Result Table	Decoded packet data in tabular view

Bus search options

Characteristic	Description
Search On	Start Slave Address Command Payload: Master, Slave, Either Errors: Any, Missing Ack, Parity End



Bus configuration



Search configuration

Bus decode

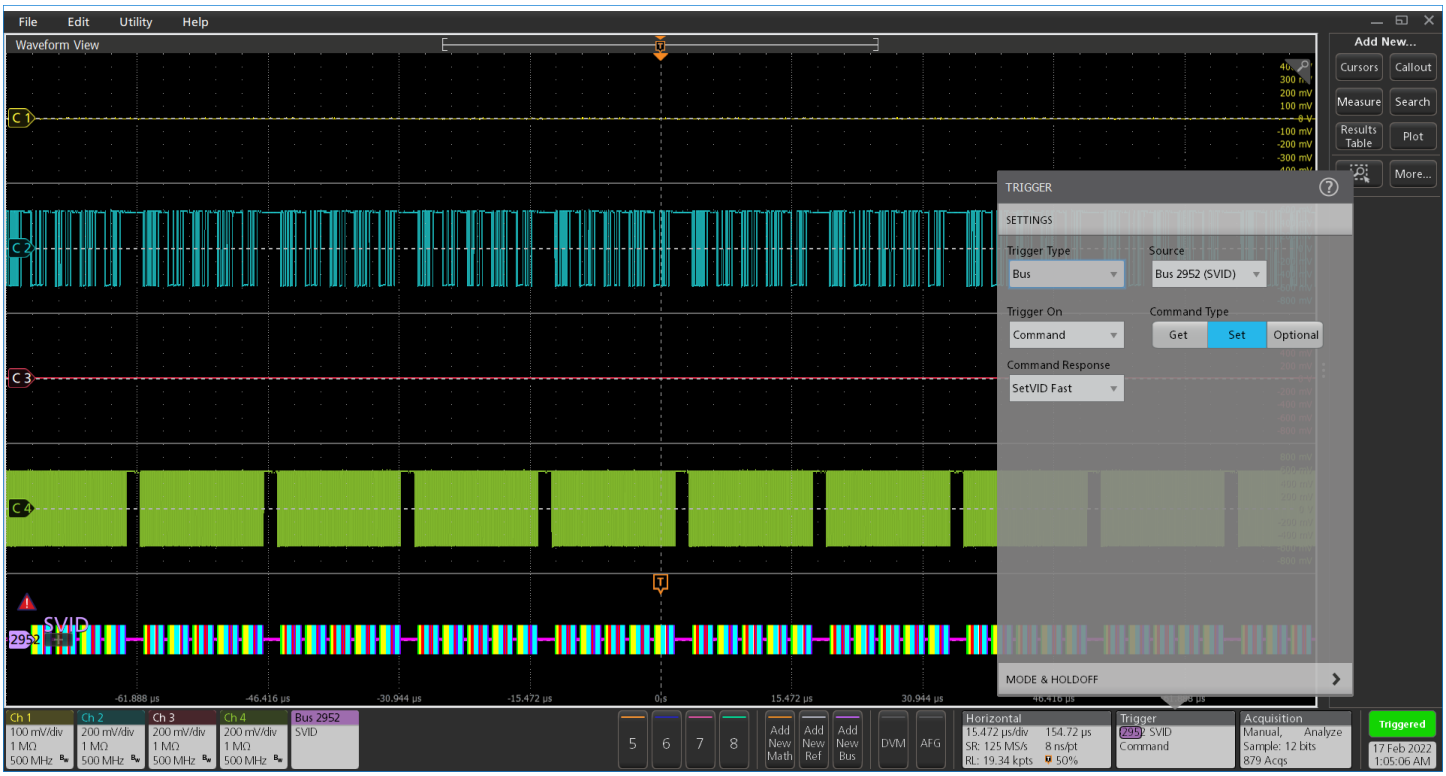
Characteristic	Description
Maximum Clock/Data Rate	Maximum frequency of 26.25 MHz

Bus decode

Characteristic	Description
Decode Display	Start (Green) Slave Address (Yellow) Command (Yellow) Master Payload (Cyan) Master Parity (Purple) End (Purple) Turnaround (Purple) Ack (Purple) Slave Payload (Cyan) Slave Parity (Purple)

SVID (Trigger) characteristics

Characteristic	Description
SVID Sources	▪ Select the **SVID** bus on which to trigger. ▪ **Trigger On** select the type of information on which to trigger.
Trigger On	▪ Start ▪ Slave Address ▪ Command ▪ Payload ▪ Errors



Triggering on a specific SetVID Fast command on the SVID bus.

USB 2.0 Characteristics (Version 2.0)

Bus setup options

Characteristic	Description
USB 2.0 Source(s)	Analog channels Digital channels (single-ended) Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Speed	USB 1.0 (1.5 Mbps) USB 1.1 (12 Mbps) USB 2.0 (480 Mbps)
Recommended Probing:	
USB 1.0 and USB 1.1 USB 2.0	Single-ended Differential
Formats Available for USB 1.0, USB 1.1, and USB 2.0	Mixed Hex Hex Binary Mixed ASCII

Display modes

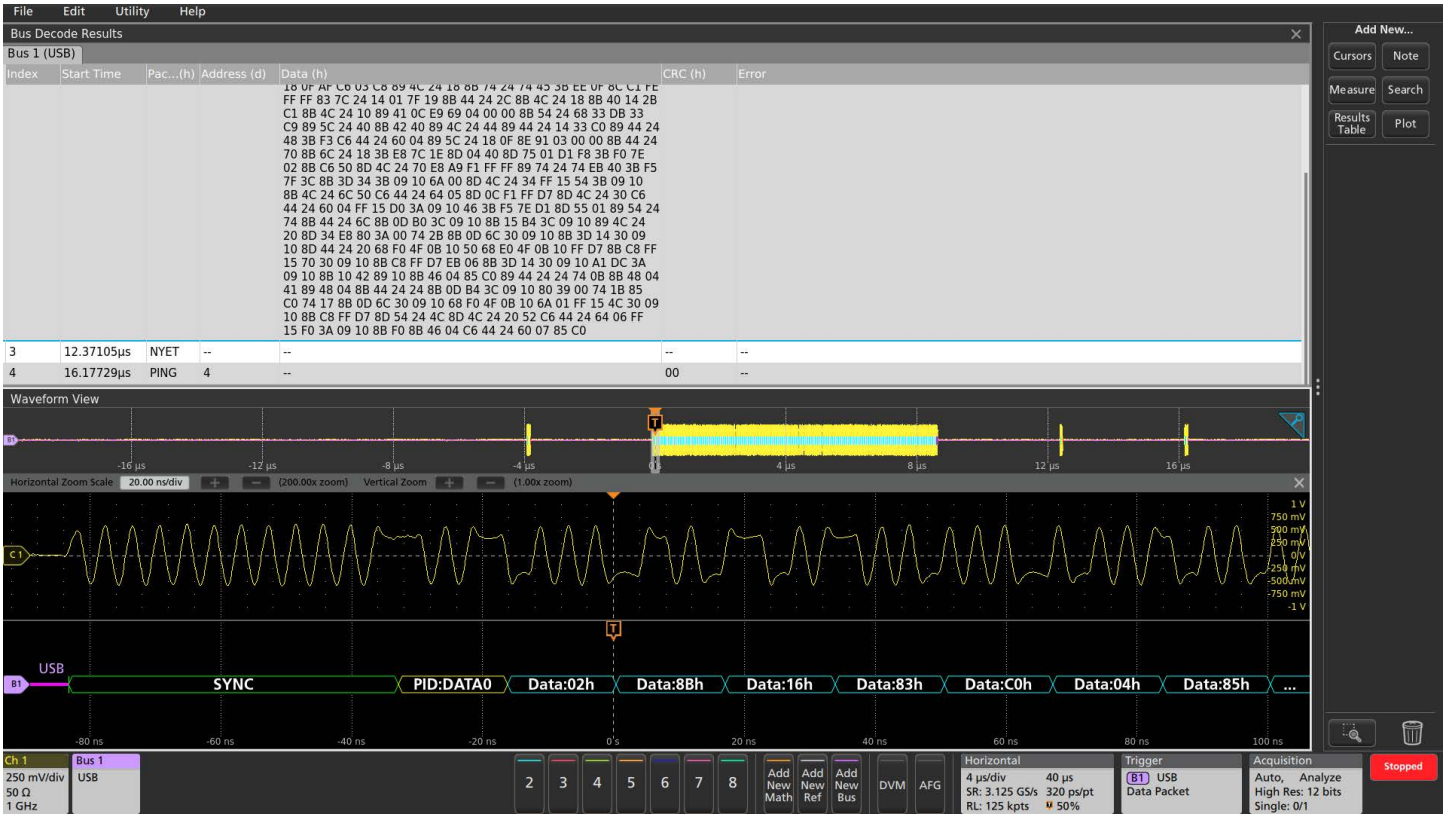
Characteristic	Description
Bus	Bus only
Bus and Waveforms	Simultaneous display of bus and digital waveforms
Results Table	Decoded packet data in a tabular view

Bus trigger options

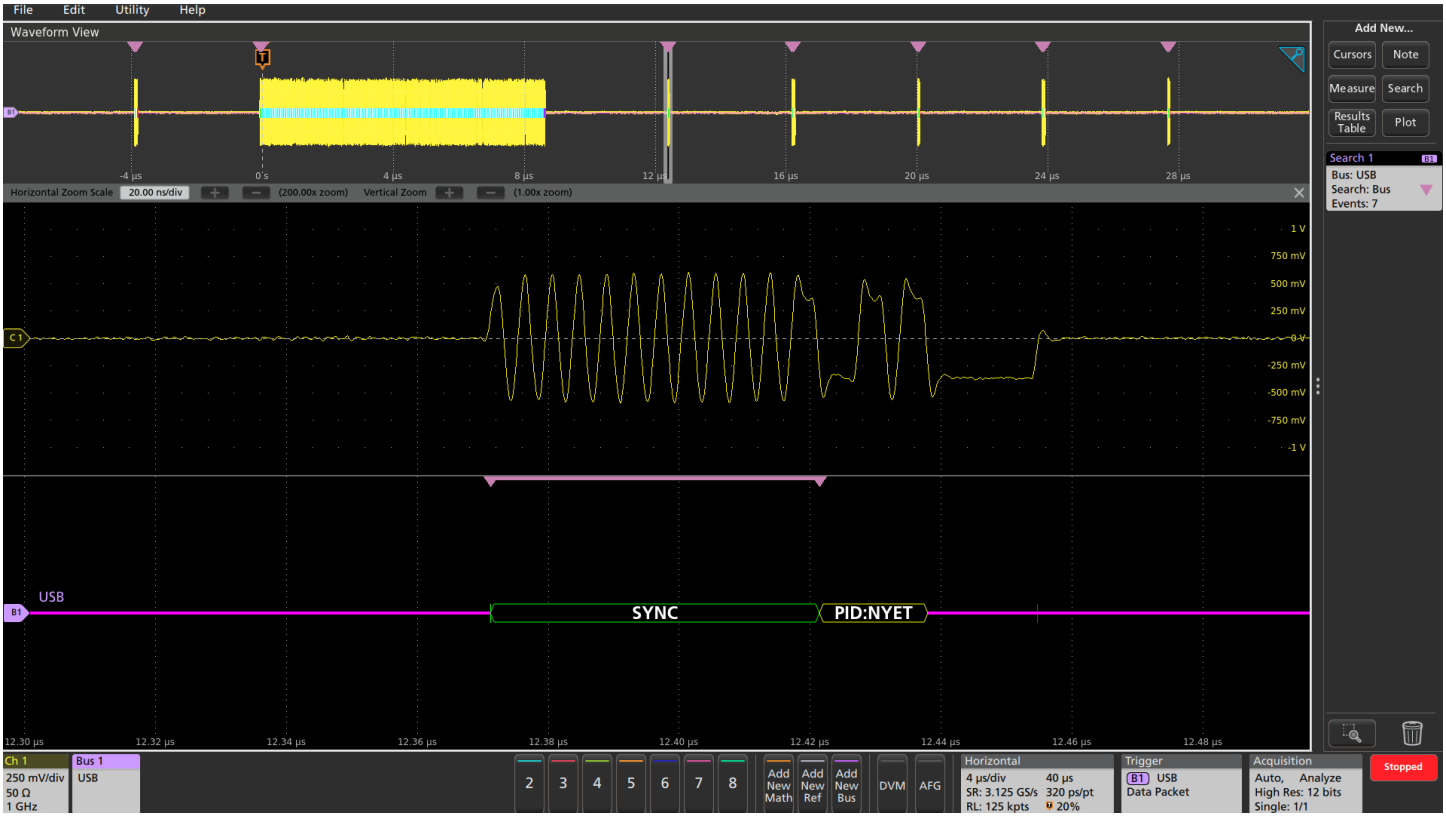
Characteristic	Description
Trigger On	Sync Reset Suspend Resume End of Packet Token (address) Packet Data Packet Handshake Packet: ACK, NAK, STALL, NYET (USB 2.0 only) Special Packet: PRE (USB 1.1 only), ERR, SPLIT, PING, Reserved Error: PID check, CRC5 or CRC16, Bit stuffing (USB 1.0 and USB 1.1 only)

Bus Search options

Characteristic	Description
Search On	Sync Reset Suspend Resume End of Packet Token (address) Packet Data Packet Handshake Packet: ACK, NAK, STALL, NYET (USB 2.0 only) Special Packet: PRE (USB 1.1 only), ERR, SPLIT, PING, Reserved Error: PID check, CRC5 or CRC16, Bit stuffing (USB 1.0 and USB 1.1 only)



Protocol Decode Results Table provides a time-stamped, tabular view of all captured packets on the USB bus.



Triggering on a specific data pattern on the USB 2.0 bus and automatically searching on Sync.

USB Characteristics (USB Version 3.0, 3.1 Gen 1, 3.2 Gen 1, 3.1 Gen 2, and 3.2 Gen 2)

Bus setup options

Characteristic	Description
USB Source(s)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Speed	USB 1.0 (1.5 Mbps) USB 1.1 (12 Mbps) USB 2.0 (480 Mbps) USB 3.0 (5 Gbps) USB 3.1 Gen 1 (5 Gbps) USB 3.1 Gen 2 (10 Gbps) USB 3.2 Gen 1 (5 Gbps) USB 3.2 Gen 2 (10 Gbps)
Lanes (available for USB 3.2 Gen 1, USB 3.2 Gen 2)	Single and Dual
Signal Type	Single Ended (Only for single lane) Differential
Recommended Probing:	
USB 1.0, USB 1.1, USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1, and USB 3.2 Gen 2	Single-ended
USB 2.0, USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1, and USB 3.2 Gen 2	Differential
Formats Available:	
USB 1.0, USB 1.1, and USB 2.0	Hex Binary Mixed Hex Mixed ASCII
USB 3.0, USB 3.1 Gen 1, and USB 3.2 Gen 1	Hex Binary Mixed Hex

Bus setup options

Characteristic	Description
Packet View for USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1 and USB 3.2 Gen 2	On Off

Display modes

Characteristic	Description
Bus	Bus only
Results Table	Decoded packet data in a tabular view

Bus Search options

Characteristic	Description
Lane	USB 3.2 Gen 1x2 (for speed USB 3.2 Gen 1) USB 3.2 Gen 2x2 (for speed USB 3.2 Gen 2) Lane 0 Lane 1

Bus Search options

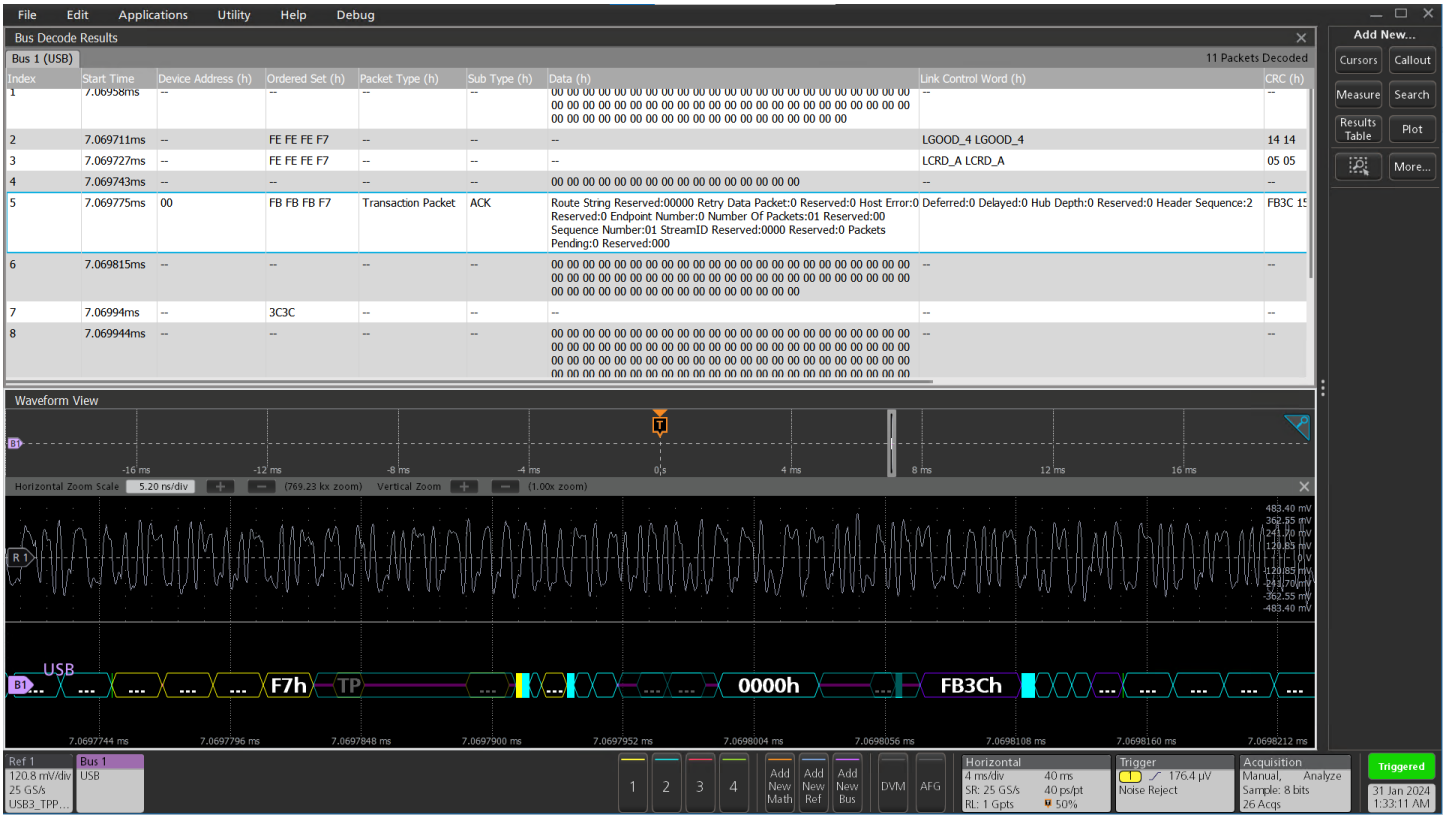
Characteristic	Description
Search On	When speed is USB 3.0, USB 3.1 Gen 1, or USB 3.2 Gen 1 and Packet View is On in the Bus configuration, or when speed is USB 3.2 Gen 1, Lanes is Dual, and search Lane in Search configuration is USB 3.2 Gen 1x2: ▪ Ordered Set: TSEQ, TS1, TS2, SKP, DPPSTART, DPPEND, DPPABORT, LCSTART, HPSTART ▪ Control Character: COM, EDB, END, EPF, SDP, SHP, SKP, SLC, SUB, Any ▪ Packet: • LMP: Port Capability, Port Configuration, Port Configuration Response, Precision Time Management, Set Link Function, U2 Inactivity Timeout, Vendor Device Test, ANY, • TP: ACK, DEV Notification, ERDY, NRDY, PING, PING Response, STATUS, STALL, ANY, • DP, ITP, ANY • Error: CRC (CRC5, CRC16, CRC32) When speed is USB 3.0, USB 3.1 Gen 1, or USB 3.2 Gen 1 and Packet View is Off in Bus configuration, or when speed is USB 3.2 Gen 1, Lanes is Dual, and search Lane in search configuration is set to Lane 0 or Lane 1: ▪ Ordered Set: TSEQ, TS1, TS2, SKP ▪ Control Character: COM, EDB, END, EPF, SDP, SHP, SKP, SLC, SUB, Any ▪ Compliance Pattern: CP1, CP3, CP4, CP5/6, CP7/8 ▪ Custom Pattern ▪ Error: Character, Disparity

Bus Search options

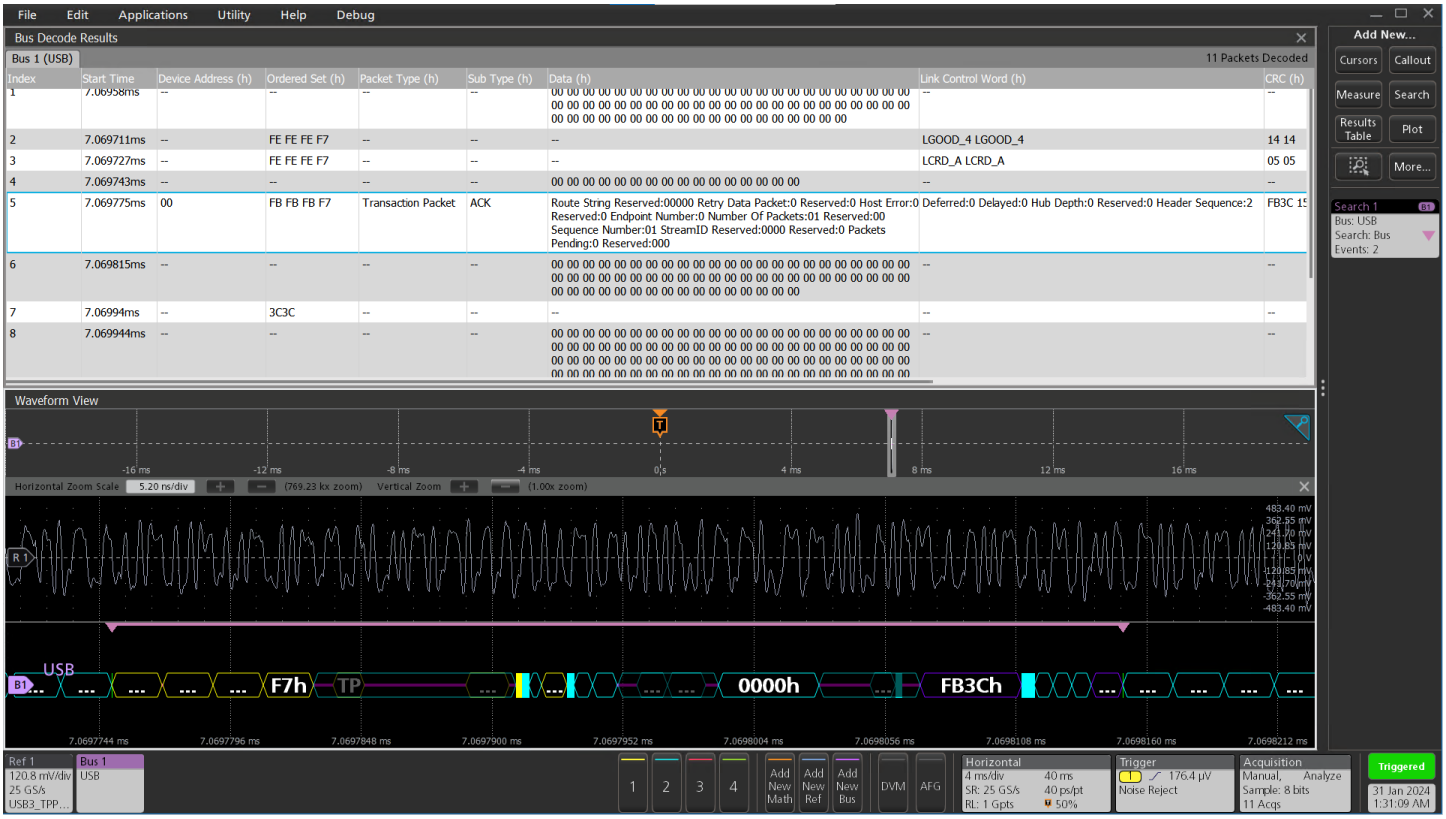
Characteristic	Description
	When speed (bus config) is USB 3.1 Gen 2 or USB 3.2 Gen 2 and Packet View is On, or when speed is USB 3.2 Gen 2, Lanes is Dual, and search Lane (search config) is USB 3.2 Gen 2x2: ▪ Ordered Set: TSEQ, TS1, TS2, SKP, DPPSTART, DPPEND, DPPABORT, LCSTART, HPSTART, DPHSTART, SDS, SYNC ▪ Control Character: EDB, END, EPF, DPHP, SDP, SDS, SHP, SKP, SKPEND, SLC, ANY ▪ Packet: ▪ LMP: Port Capability, Port Configuration, Port Configuration Response, Precision Time Management, Set Link Function, U2 Inactivity Timeout, Vendor Device Test, ANY, ▪ TP: ACK, DEV Notification, ERDY, NRDY, PING, PING Response, STATUS, STALL, ANY, ▪ DP, ITP, ANY ▪ Error: CRC (CRC5, CRC16, CRC32) When speed bus configuration is USB 3.1 Gen 2 or USB 3.2 Gen 2 and Packet View is Off, or when speed is USB 3.2 Gen 2, Lanes is Dual, and search Lane in search configuration is set to Lane 0 or Lane 1: ▪ Ordered Set: TSEQ, TS1, TS2, SKP, SDS, SYNC ▪ Control Character: EDB, END, EPF, DPHP, SDP, SDS, SHP, SKP, SKPEND, SLC, ANY ▪ Compliance Pattern: CP9, CP10, CP11, CP13, CP14, CP15, CP16 ▪ Custom Pattern ▪ Sync Header: Control, Data, Invalid

Fixture details

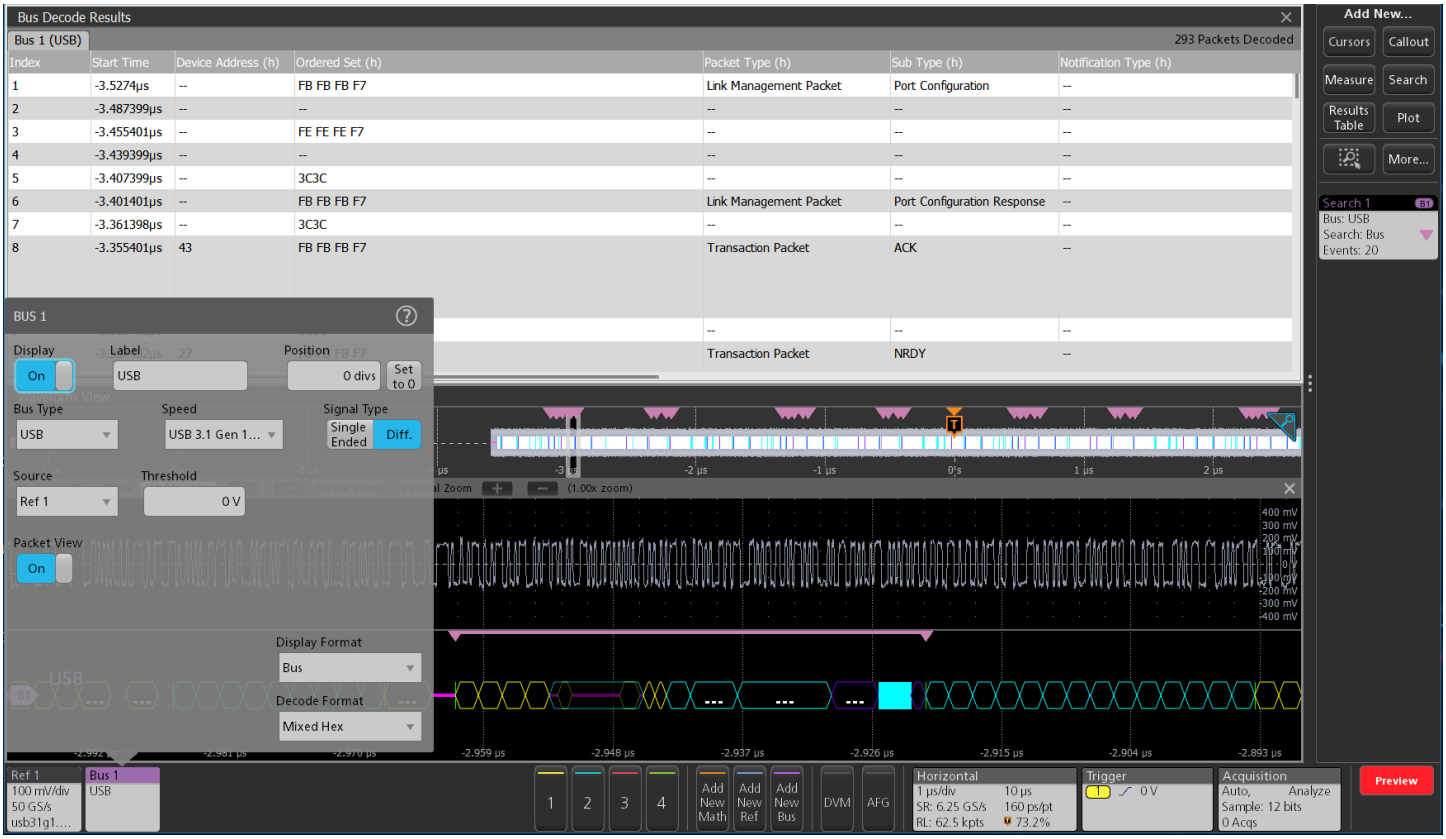
Characteristic	Description
Cable assembly coaxial	SMA to SMA RG-316 DS 9.843" (250.00 mm) Cable impedance-50 Ω Connector impedance-50 Ω Overall impedance-50 Ω Maximum frequency-12.4 GHz
SS-USB3.2C-SMA breakout board	The SS-USB3.2C-SMA breakout board, High-speed transmit and receive signals are routed to the SMA connectors via 50 Ω microstrip transmission lines over Rogers R04350 low-loss dielectric for excellent signal integrity at frequencies up to 30 GHz
Attenuators	6 dB
Connectors	T connectors



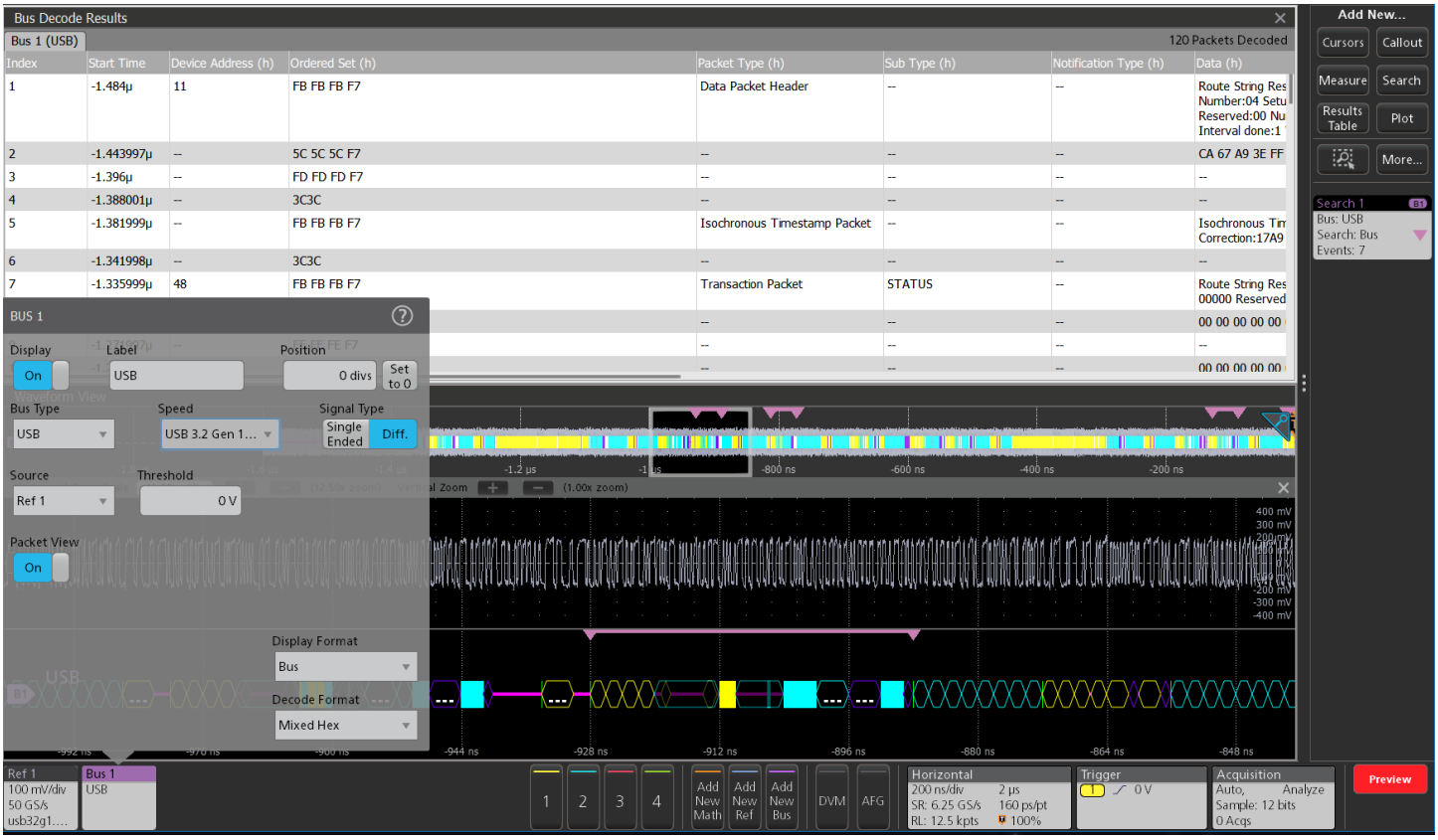
USB3 bus setup and MixedHex display, showing decode with speed USB 3.0 and packet view On.



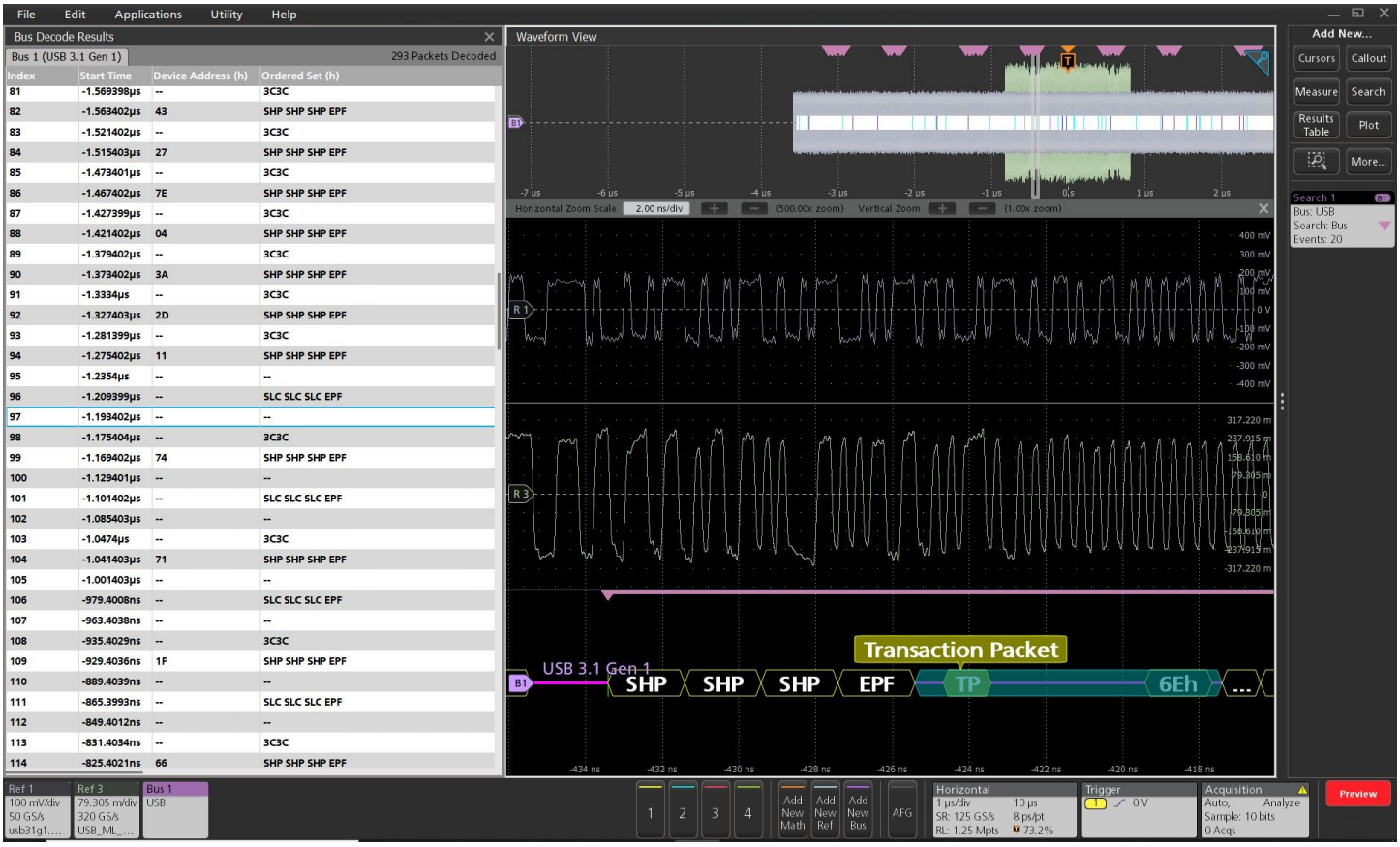
Searching the USB bus with decode speed USB 3.0 and packet view On for the TP packets.



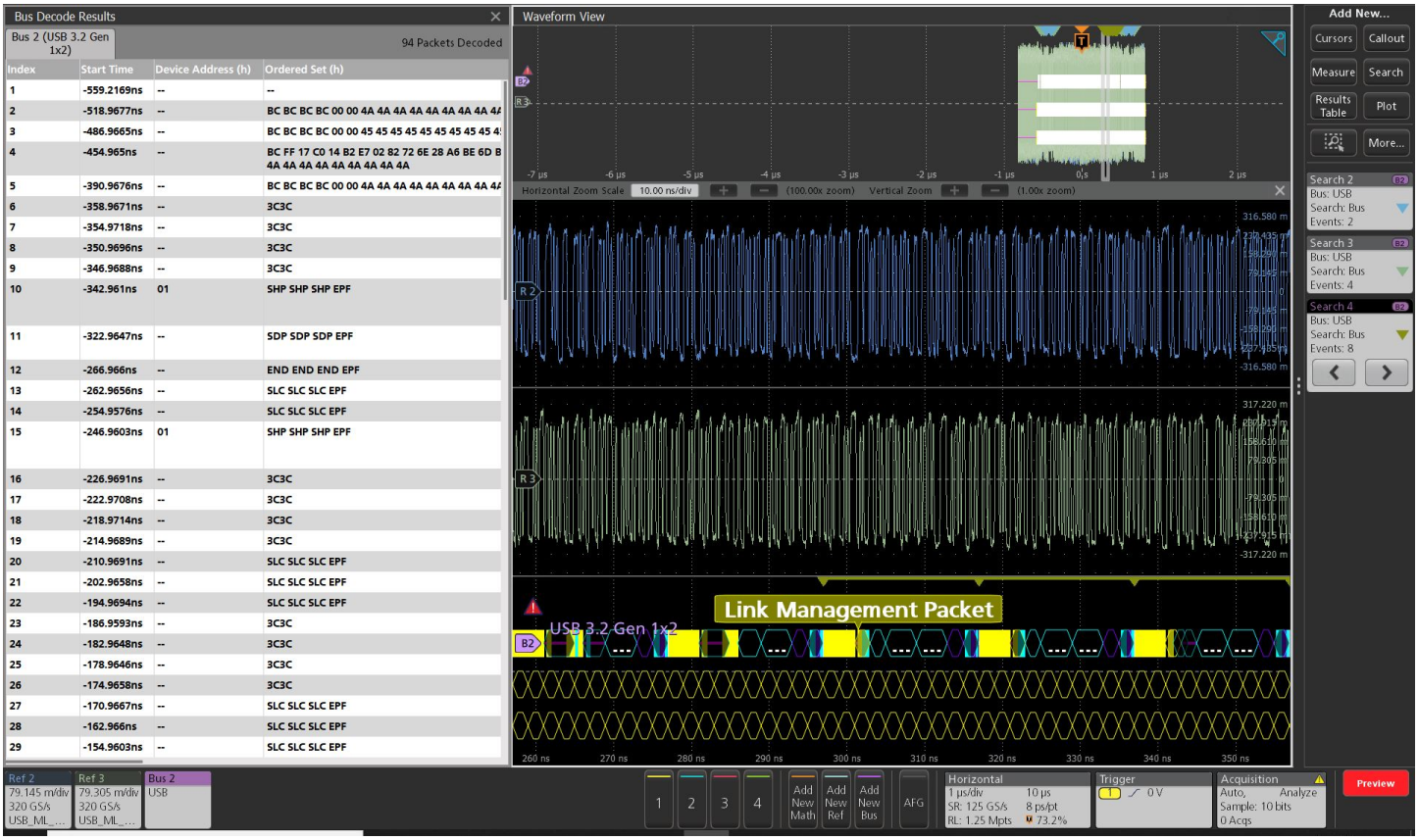
Searching the USB bus with decode speed USB 3.1 Gen 1 and packet view On for the TP packets. The results table at the top of the graticule.



Searching the USB bus with decode speed USB 3.2 Gen 1 and packet view On for the TP packets. The results table at the top of the graticule.



Searching the USB bus with decode speed USB 3.1 Gen 1 and packet view On for the TP packets with the results table on left of the graticule.



USB bus setup and MixedHex display, showing decode with speed USB 3.2 Gen 1x2 with the results table on left of the graticule, search on packet type LMP along with lane 0 and lane 1 search on training sequence is added.

Searching the USB bus with decode speed USB 3.1 Gen 2 and packet view On for the DP packets with the results table on top of the graticule.

Searching the USB bus with decode speed USB 3.2 Gen2x2 for the DP packets. The results table at the top of the graticule.

Bus configuration for the USB bus with decode speed USB 3.2 Gen2x2 for the DP packets. Search on packet type LMP along with lane 0 and lane 1 search on training sequence is added.

USB Characteristics (USB4 v1.0)

Bus setup options

Characteristic	Description
USB4 Source(s)	Analog channels Digital channels Active Math channels Active Reference channels
Thresholds	Per-channel thresholds
Signal Type	Single Ended Differential
Speed	USB 4.0 Gen 2 (10 Gbps) USB 4.0 Gen 3 (20 Gbps)
Formats Available	Hex Binary Mixed Hex
Packet View	On Off
Use RS-FEC	Yes No

Display modes

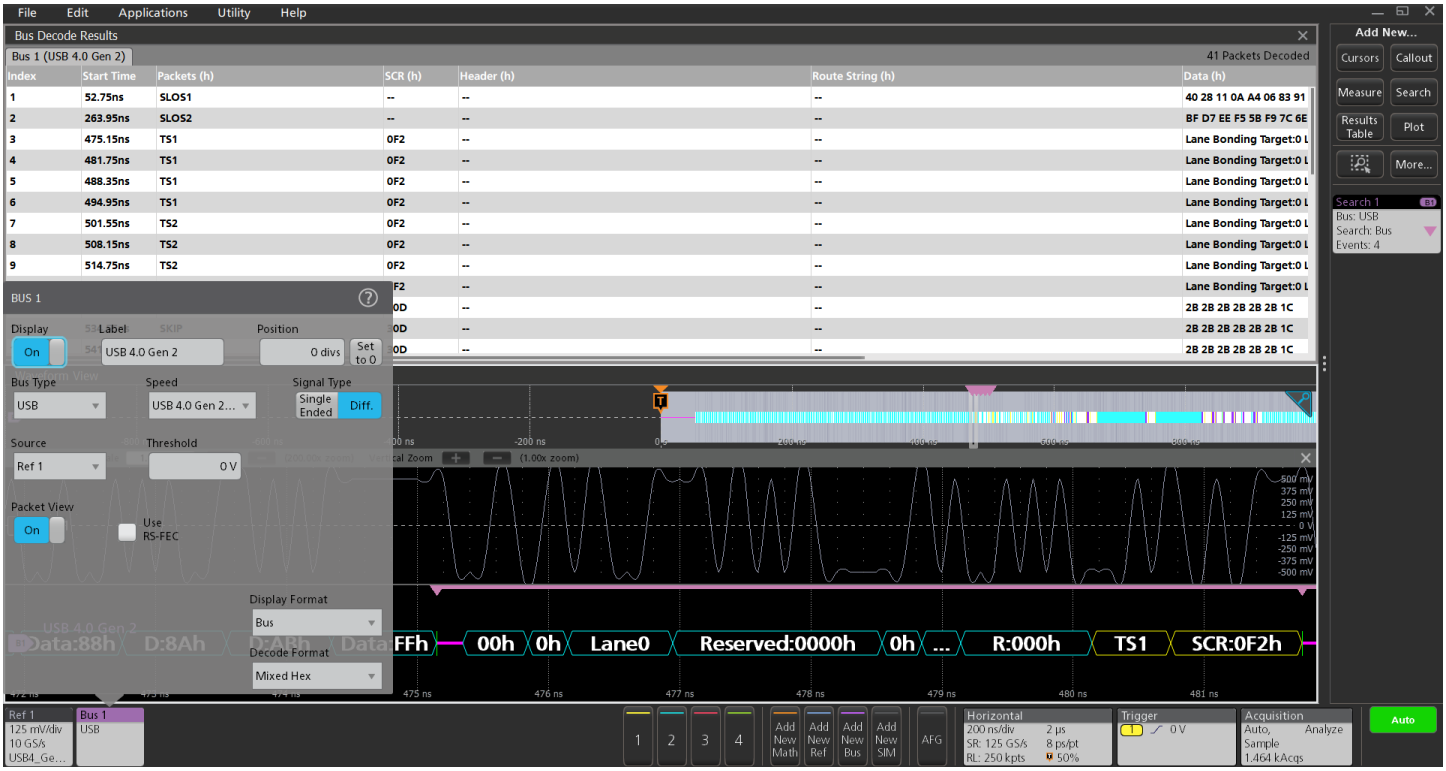
Characteristic	Description
Bus	Bus Only
Results Table	Decoded packet data in a tabular view

Bus Search options

Characteristic	Description
Search On with Packet View On	▪ Packet - LMP, Control, Read Write CS, Time-Sync, ANY ▪ Ordered Set - TS1, TS2, SLOS1, SLOS2, SKIP, TSNOS, LOW POWER ▪ Header Fields ▪ Error - HEC, ECC, CRC32

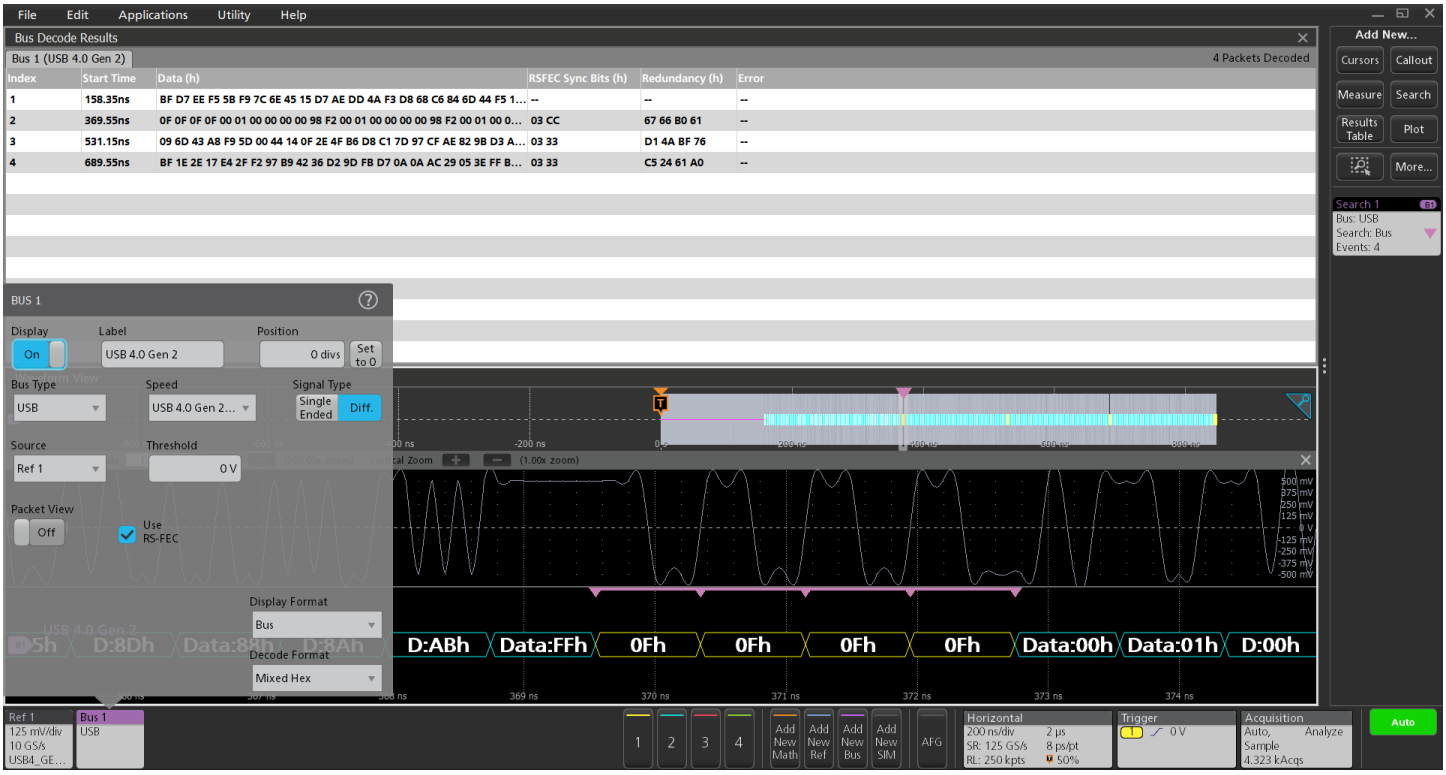
Bus Search options

Characteristic	Description
Search On with Packet View Off	▪ Custom Pattern ▪ Ordered Set - TS1, TS2, SLOS1, SLOS2, SKIP, TSNOS, LOW POWER ▪ Sync Bits
RSFEC with Packet View On	▪ Packet - LMP, Control, Read Write CS, Time-Sync, ANY ▪ Ordered Set - TS1, TS2, SLOS1, SLOS2, SKIP, TSNOS, LOW POWER ▪ Header Fields ▪ START_RS_FEC ▪ Error - HEC, ECC, CRC32
RSFEC with Packet View Off	▪ START_RS_FEC ▪ RSFEC Sync Bits ▪ Redundancy ▪ Custom Pattern ▪ Error

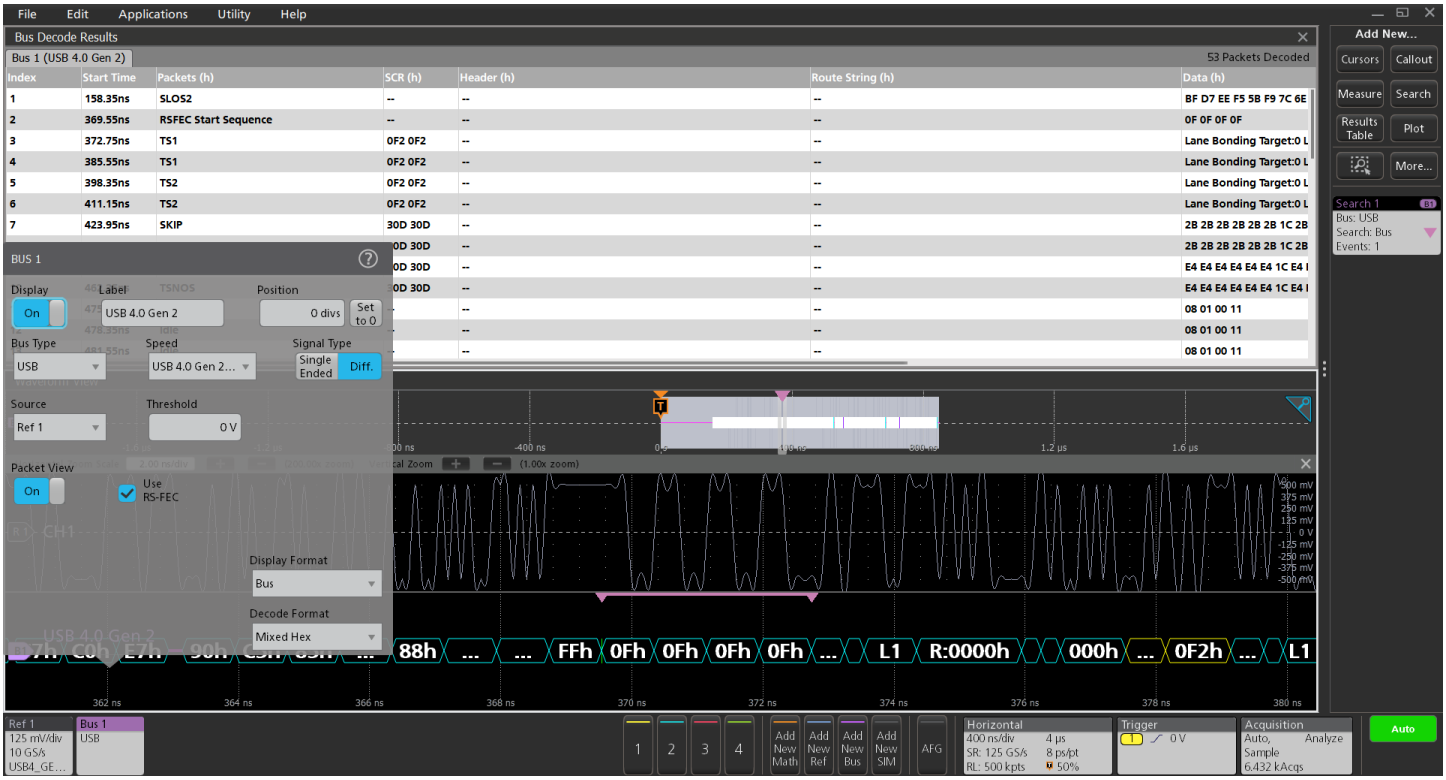


USB4 Gen2 bus showing decode with packet view ON. Ordered Set of type TS1 was searched with 4 occurrences.

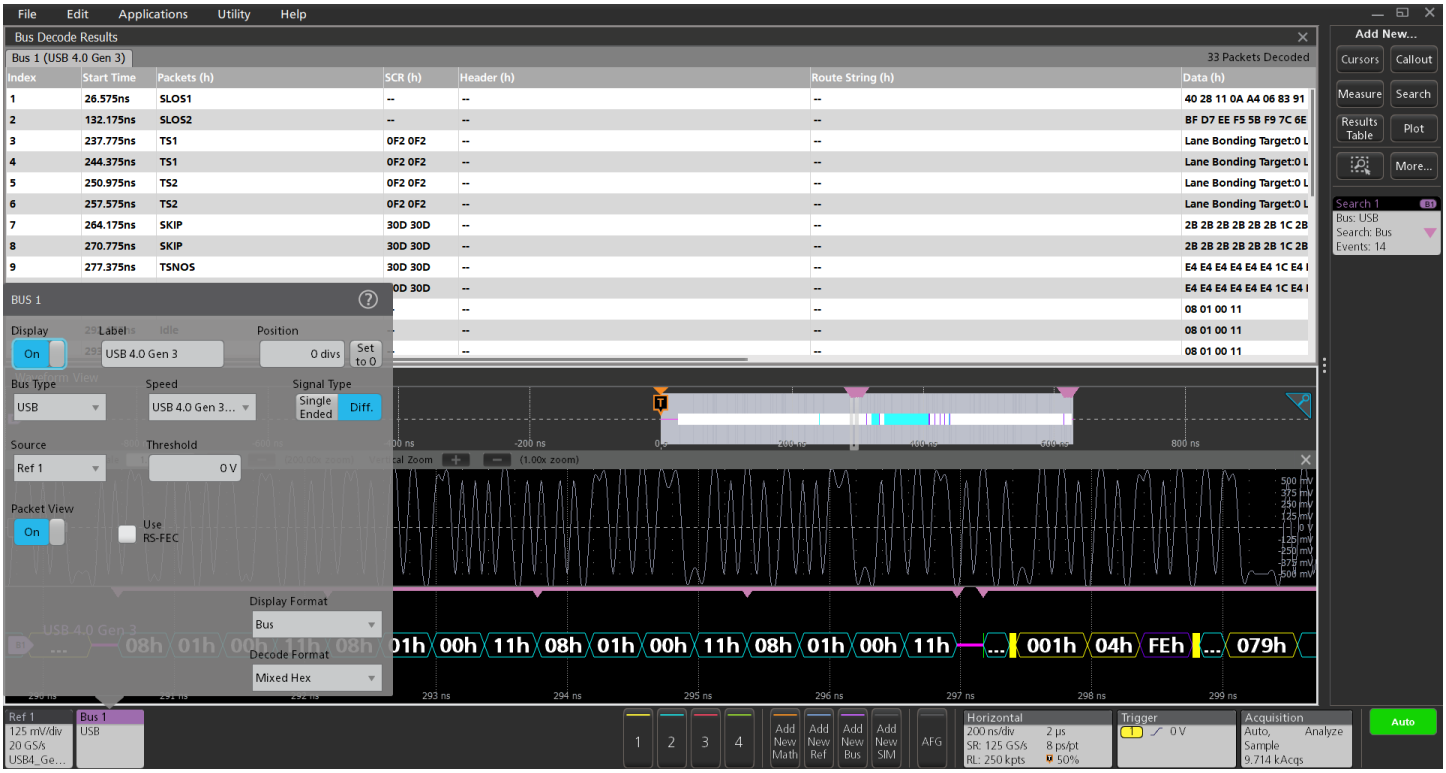
USB4 Gen2 bus setup showing decode with packet view Off. Sync Bits searched with 80 occurrences.



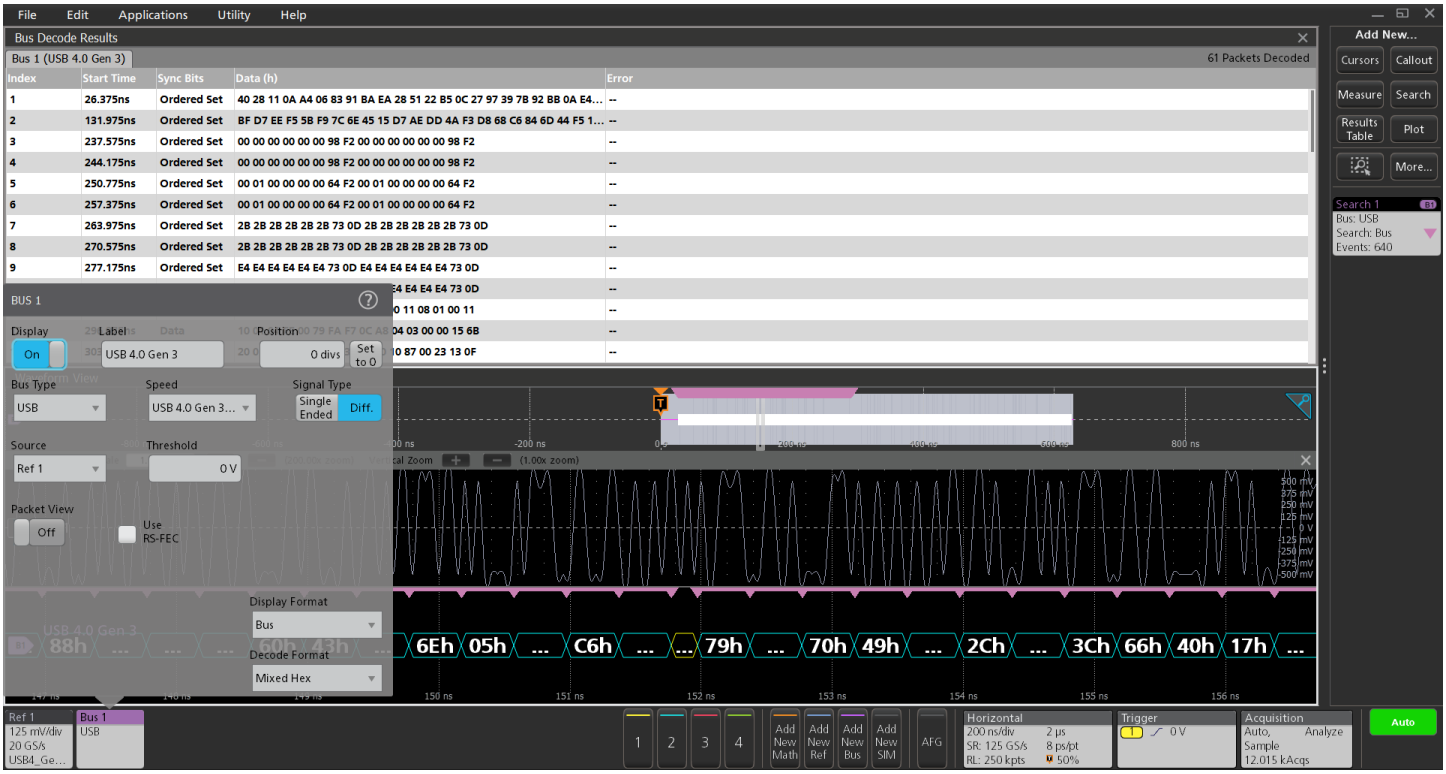
USB4 Gen2 bus setup showing decode with packet view Off with RSFEC. START_RSFEC searched with 4 occurrences.



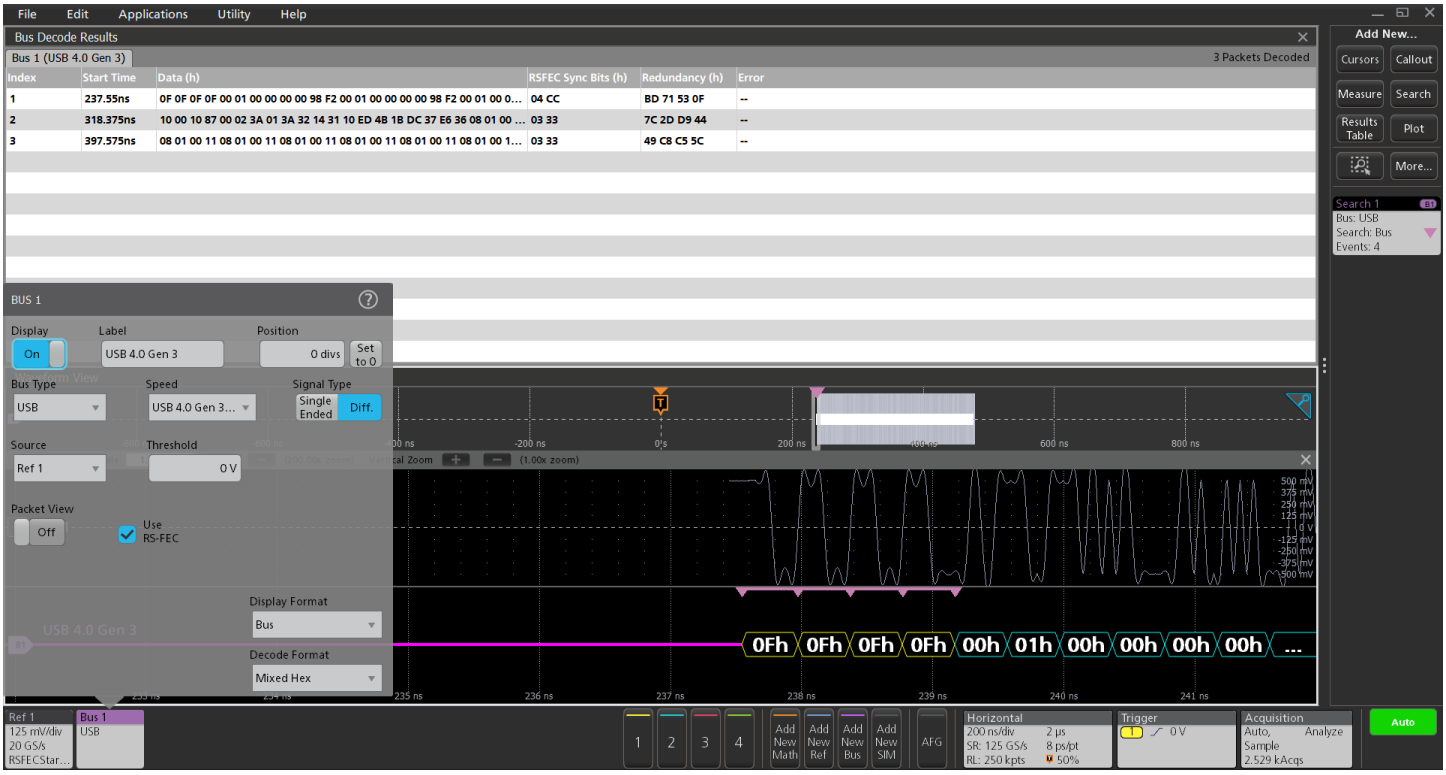
USB4 Gen2 bus setup showing decode with packet view ON with RSFEC. START_RSFEC searched with 1 occurrence.



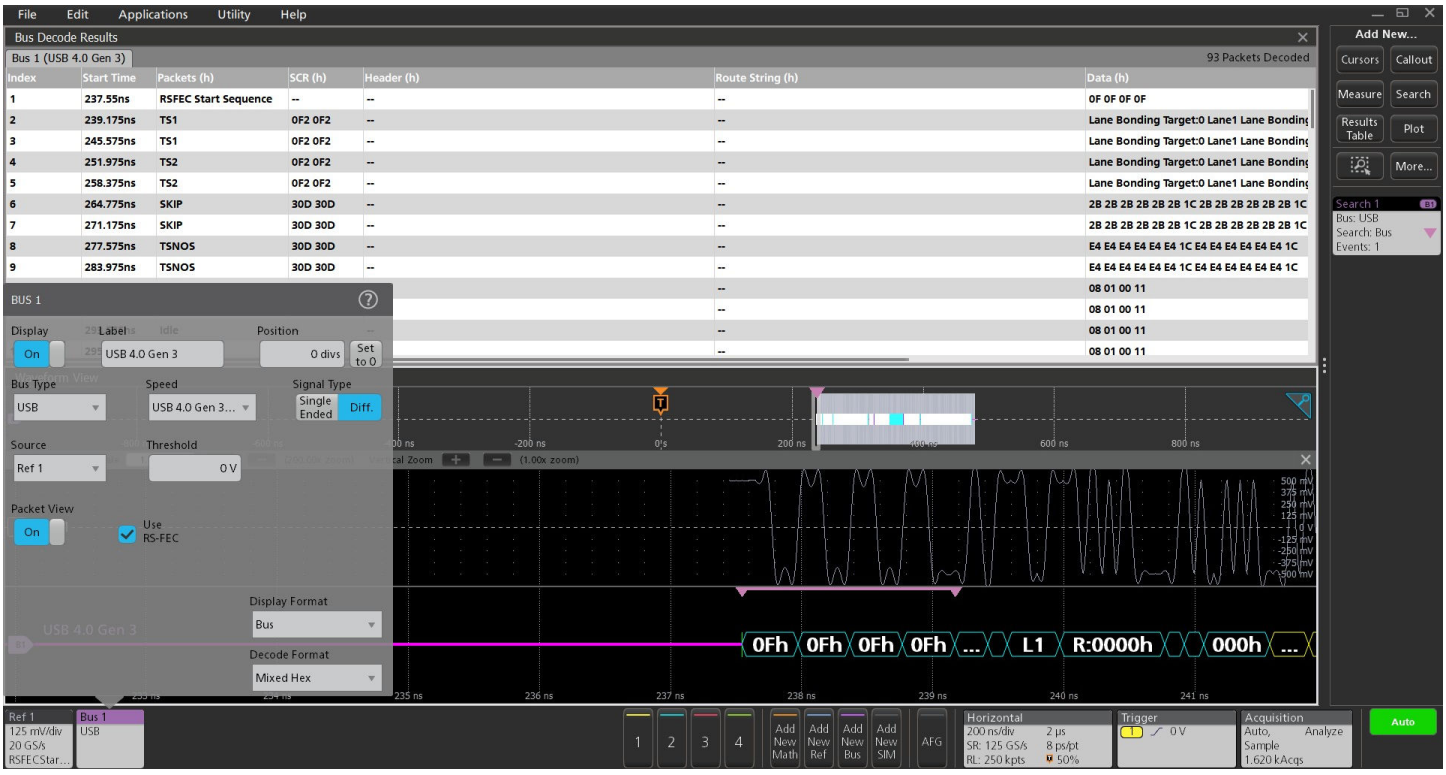
USB4 Gen3 bus showing decode with packet view ON. LMP Packet of type ANY was searched with 14 occurrences.



USB4 Gen3 bus setup showing decode with packet view Off. Custom Pattern searched with 640 occurrences.



USB4 Gen3 bus setup showing decode with packet view Off with RSFEC. START_RSFEC searched with 4 occurrences.



USB4 Gen3 bus setup showing decode with packet view ON with RSFEC. START_RSFEC searched with 1 occurrence.

Ordering information

Protocol Bundles

Specially designed software bundles are available for 4, 5 and 6 Series MSO with 1 year renewable and perpetual to suit your Design and validation needs.

Validate your Protocols with our industry standard Serial analysis software available for over 30 technologies.

Pro bundle for Serial Analysis teams. Our standards expertise and Integrated Protocol Decoders help you shorten your design cycle, gain greater technical insight and improve team productivity to bring new products and services to market much faster.

Serial Decode	Description	4 Series MSO	5 Series MSO	6 Series MSO
4-RL-1	Record length enhancement to 62.5 million sample points.	✔	✖	✖
5-RL-125M	Record length enhancement to 125 million sample points.	✖	✔	✖
6-RL-2	Record length enhancement to 250 million sample points.	✖	✖	✔
SRAUDIO	Audio Serial Triggering and Analysis (I2S, LI, RJ, TDM). Enables triggering on packet-level information on serial audio buses.	✔	✔	✔
SRAUTO	Automotive Serial Triggering and Analysis (CAN, CAN FD, CAN XL, LIN, FlexRay). Enables triggering on packet-level information on CAN/CAN FD/CAN XL/LIN/FelxRay.	✔	✔	✔
SRNET	Ethernet Serial Triggering and Analysis (10BASE-T, 100BASE-T). Enables decoding and analysis on Ethernet buses.	✔	✔	✔
SRI3C	I3C Serial Decoding and Analysis. Enables decoding and searching on packet-level information on MPI I3C.	✔	✔	✔
SRNRZ	NRZ Serial Decoding and Analysis. Supports NRZ with normal and inverted polarity with Bit order (MSB or LSB first).	✔	✔	✔
SRPM	Power Management Serial Triggering and Analysis. Enables triggering on packet-level information on SPMI buses.	✔	✔	✔
SRUSB2	USB 2.0 Serial Triggering and Analysis. Enables triggering on packet-level information on USB 2.0 buses.	✔	✔	✔
SRUSB3	USB 3.0, USB 3.1 Gen 1, USB 3.1 Gen 2, USB 3.2 Gen 1, USB 3.2 Gen 2 Serial Decoding and Analysis. Extensive search options.	✖	✖	✔
SRPCIE321	PCIe Serial Decoding and Analysis. Extensive search options.	✖	✖	✔
SRMDIO	MDIO Protocol Decoder and Search. Extensive search options.	✔	✔	✔
SRSVID	SVID Protocol Decoder and Search. Supports version rev.1.92. Extensive search options	✔	✔	✔

Serial Decode	Description	4 Series MSO	5 Series MSO	6 Series MSO
SR8B10B	8B10B Serial Decoding and Analysis. Finds and displays parity error if found in 4-bit or 6-bit for the 10-bit symbol in 8B10B	✖	✔	✔
SRETHERCAT	ETHERCAT Protocol Decoder and search. Enables decoding and analysis on EtherCAT buses.	✔	✔	✔
SRSMBUS	SMBUS Protocol Decoder and search. Enables decoding and analysis on SMbus buses.	✔	✔	✔
1 YearLicense		4-PRO-SERIAL-1Y	5-PRO-SERIAL-1Y	6-PRO-SERIAL-1Y
PerpetualLicense		4-PRO-SERIALPER	5-PRO-SERIALPER	6-PRO-SERIALPER

Pro Bundle for Military and Aerospace designers. Our Software design tools help you shorten your design cycle, gain greater technical insight and improve team productivity to bring new products and services to market much faster.

Serial Decode	Description	4 Series MSO	5 Series MSO	6 Series MSO
4-RL-1	Record length enhancement to 62.5 million sample points.	✔	✖	✖
5-Rl-125M	Record length enhancement to 125 million sample points.	✖	✔	✖
6-RL-2	Record length enhancement to 250 million sample points.	✖	✖	✔
SRAERO	Aerospace Serial Triggering and Analysis (MIL-STD-1553, ARINC429). Enables triggering on packet-level information.	✔	✔	✔
SRSPACEWIRE	SpaceWire serial analysis. Enables decoding and analysis on SpaceWire buses.	✔	✔	✔
MTM		✔	✔	✔
SRNRZ	NRZ Serial Decoding and Analysis. Supports NRZ with normal and inverted polarity with Bit order (MSB or LSB first).	✖	✔	✔
DJA	Jitter Analysis Package including TIE, Eye diagram, Histogram and other advanced analysis measurements.	✔	✔	✔
1 Year License		4-PRO MILGOV-1Y	5-PRO MILGOV-1Y	6-PRO MILGOV-1Y
Perpetual License		4-PRO-MGOVPER	5-PRO-MIGOVPER	6-PRO-MIGOVPER

To add to an instrument at purchase

Serial bus type	3 Series MDO Option	4 Series MSO Option	5 Series MSO Option	6 Series MSO Option	7 Series DPO Option	Description
MIL-STD-1553, ARINC 429	3-SRAERO	4-SRAERO	5-SRAERO	6-SRAERO	DPO714AX 7-SRAERO	Aerospace Serial Triggering and Analysis (MILSTD-1553, ARINC 429 (version ARINC Specs 429 PART 1-17)). Enables triggering on packet-level information on MIL-STD-1553 and ARINC 429 buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
I^2S, LJ, RJ, TDM	3-SRAUDIO	4-SRAUDIO	5-SRAUDIO	6-SRAUDIO	DPO714AX 7-SRAUDIO	Audio Serial Triggering and Analysis (I2S, LJ, RJ,TDM). Enables triggering on packet-level information on serial audio buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
128B132B	N/A	N/A	N/A	N/A	DPO714AX 7-SR128B132B	Enables decoding and searching of packet-levelinformation on high-speed serial buses with analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information. Identifies and displays disparity or alignment errors if found in the 4-bit sync header or the 128-bit data payload within the 132-bit block.
64B66B	N/A	N/A	N/A	N/A	DPO714AX 7- SR64B66B	Enables decoding and searching of packet-level information on high-speed serial buses with analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information. Identifies and displays disparity or alignment errors if found in the 2-bit sync header or the 64-bit data payload within the 66-bit block.
8B10B	N/A	N/A	5-SR8B10B	6-SR8B10B	DPO714AX 7-SR8B10B	8B10B Serial Decoding and Analysis. Enablesdecoding and searching the packet-level information on buses with analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information. Finds and displays parity error if found in 4-bit or 6-bit for the 10-bit symbol in 8B10B

Serial bus type	3 Series MDO Option	4 Series MSO Option	5 Series MSO Option	6 Series MSO Option	7 Series DPO Option	Description
NRZ	N/A	4-SRNRZ	5-SRNRZ	6-SRNRZ	DPO714AX 7-SRNRZ	NRZ Serial Decoding and Analysis. Enablesdecoding and searching the packet-level information on buses with analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information. Variants like NRZ-I, NRZ-M, NRZ-S, and NRZ-C are not supported currently. Supports only NRZ with normal and inverted polarity with Bit Order (MSB or LSB First).
CAN, CAN FD, CAN XL, LIN, FlexRay	3-SRAUTO (Except CAN XL)	4-SRAUTO	5-SRAUTO	6-SRAUTO	DPO714AX 7-SRAUTO	Automotive Serial Triggering and Analysis (CAN(version 2.0), CAN FD, CAN XL, LIN (version 2.0), FlexRay (version 2.0)). Enables triggering on packet-level information on CAN/CAN FD/CAN XL/LIN/ FlexRay buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
Automotive 10BASE-T1S, 100BASE-T1	N/A	N/A	5-SRAUTOEN1	6-SRAUTOEN1	DPO714AX 7-SRAUTOEN1	10BASE-T1S Serial trigger with decoder and100Base-T1 Automotive Ethernet serial decode (version BRR 3.2). Includes analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
Automotive Ethernet Signal separation	N/A	N/A	5-AUTOEN-SS	6-AUTOEN-SS	DPO714AX 7-AUTOEN-SS	Automotive Ethernet Signal Separation. Required for100BAST-T1 Tx solution.
SENT	N/A	4-SRAUTOSEN	5-SRAUTOSEN	6-SRAUTOSEN	DPO714AX 7-SRAUTOSEN	Automotive Sensor Serial Triggering and Analysis(SENT). Enables triggering on packet-level information on SENT buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
RS-232/422/485,UART	3-SRCOMP	4-SRCOMP	5-SRCOMP	6-SRCOMP	DPO714AX 7-SRCOMP	Computer Serial Triggering and Analysis (RS-232,RS-422, RS-485, UART). Enables triggering on packet-level information on RS-232/422/485 buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.

Serial bus type	3 Series MDO Option	4 Series MSO Option	5 Series MSO Option	6 Series MSO Option	7 Series DPO Option	Description
I ^{2}C, SPI	3-SREMBD	4-SREMBD	5-SREMBD	6-SREMBD	DPO714AX 7-SREMBD	Embedded Serial Triggering and Analysis (I2C, SPI).Enables triggering on packet-level information on I2C and SPI buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
Ethernet	N/A	4-SRENET	5-SRENET	6-SRENET	DPO714AX 7-SRENET	Ethernet Serial Triggering and Analysis (10BASET, 100BASE-T). Enables triggering on packet-level information on Ethernet buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
I3C	N/A	4-SRI3C	5-SRI3C	6-SRI3C	DPO714AX 7-SRI3C	I3C (version 1.0) Serial Decoding and Analysis.Enables decoding and searching on packet-level information on MIPI I3C buses with analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
SPMI	N/A	4-SRPM	5-SRPM	6-SRPM	DPO714AX 7-SRPM	Power Management Serial Triggering and Analysis(SPMI) (version 2.0). Enables triggering on packet level information on SPMI buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
Spacewire	N/A	4-SRSPACEWIRE	5-SRSPACEWIRE	6-SRSPACEWIRE	DPO714AX 7-SRSPACEWIRE	Spacewire serial analysis (version STAR-Dundeeuser guide). Enables decoding and analysis on Spacewire buses.
Parallel	-	-	-	-	-	Parallel Serial Triggering and Analysis. Enablesdecoding, search and packet decode tables with time stamp information. Enables triggering on 4/5/6 Series MSO.
USB 1.0, 1.1, 2.0	3-SRUSB2	4-SRUSB2	5-SRUSB2	6-SRUSB2	DPO714AX 7-SRUSB2	USB 1.0, 1.1, 2.0 Serial Triggering and Analysis.Enables triggering on packet-level information on USB buses as well as analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
USB 3.0	N/A	N/A	N/A	6-SRUSB3	DPO714AX 7-SRUSB3	USB serial triggering and analysis (USB 3.0) for 6Series B MSO and 7 Series DPO.

Serial bus type	3 Series MDO Option	4 Series MSO Option	5 Series MSO Option	6 Series MSO Option	7 Series DPO Option	Description
USB 3.1/3.2 Gen 1	N/A	N/A	N/A	6-SRUSB3	DPO714AX 7-SRUSB3	USB serial triggering and analysis (USB 3.1 (Gen 1,2)) for 6 Series B MSO and 7 Series DPO.
USB 3.1/3.2 Gen 2	N/A	N/A	N/A	N/A	DPO714AX 7-SRUSB3	USB serial triggering and analysis (USB 3.2 (Gen 1,2)) for 7 Series DPO.
USB4 v1.0	N/A	N/A	N/A	N/A	DPO714AX 7-SRUSB4	USB4V1 Protocol Decode and Search for DPO 7 Series
Serial options bundle	3-BND	N/A	N/A	N/A	N/A	Adds all serial analysis options and the poweranalysis option available for an instrument.
PCIe Gen1 – Gen4	N/A	N/A	N/A	6-SRPCIE321	DPO714AX 7-SRPCIE321	PCIe serial decoding and analysis (PCIe Gen 1/Gen2/Gen 3/Gen 4) for 6 Series B MSO and 7 Series DPO.
PSI5	N/A	4-SRPSI5	5-SRPSI5	6-SRPSI5	DPO714AX 7-SRPSI5	PSI5 Serial Decoding (v1.3 and 2.1) and analysis.Enables decoding and Search Packet level information with analytical tools such as digital views of the signal, bus views, packet decoding, search tools, and packet decode tables with time stamp information.
MDIO	N/A	4-SRMDIO	5-SRMDIO	6-SRMDIO	DPO714AX 7-SRMDIO	MDIO Protocol Decoder and Search, No HardwareTrigger; Node locked.
SVID	N/A	4-SRSVID	5-SRSVID	6-SRSVID	DPO714AX 7-SRSVID	SVID (version 1.9) Protocol Decoder and Search, NoHardware Trigger; Node locked.
e-USB2	N/A	4-SREUSB2	5-SREUSB2	6-SREUSB2	DPO714AX 7-SREUSB2	eUSB2 (version 2.0) Protocol Decoder and Search;Node locked.
DPHY	N/A	N/A	5- SRDPHY	6- SRDPHY	DPO714AX 7-SRDPHY	DPHY characteristics version 1.2 CSI/DSI (DSI2.0/CSI2.0) protocols decoder. Supports HS data transmission burst, and escape mode functionality. Data transmission can be with 8-bit raw data or using 8b9b encoded symbol
MANCHESTER	N/A	4-SRMANCH	5-SRMANCH	6-SRMANCH	DPO714AX 7-SRMANCH	Supports Generic Manchester decode. Decode ofpackets as per packet structure defined. Decode of Errors like Sync, Parity, Manchester.
SDLC		4-SRSDLC	5-SRSDLC	6-SRSDLC	DPO714AX 7-SRSDLC	SDLC (version: GA27-3093-3) decoder and Search.Extensive search options on captured waveforms like unnumbered, Supervisory, address etc.
CPHY	N/A	N/A	5-SRCPHY	6-SRCPHY	DPO714AX 7-SRCPHY	MIPI C-PHY characteristics version 1.2 CSI/DSI(DSI2.0/CSI2.0) Protocol Decoder and Search.

Serial bus type	3 Series MDO Option	4 Series MSO Option	5 Series MSO Option	6 Series MSO Option	7 Series DPO Option	Description
1-Wire	N/A	4-SRONEWIRE	5-SRONEWIRE	6-SRONEWIRE	DPO714AX 7-SRONEWIRE	1-Wire Protocol Decoder and search .
eSPI	N/A	4-SRESPI	5-SRESPI	6-SRESPI	DPO714AX 7-SRESPI	eSPI Protocol Decoder and search.
CXPI	N/A	4-SRCXPI	5-SRCXPI	6-SRCXPI	DPO714AX 7-SRCXPI	CXPI (version JASO D015-3:2014) Protocol Decoderand search.
ETHERCAT	N/A	4-SRETHERCAT	5-SRETHERCAT	6-SRETHERCAT	DPO714AX 7-SRETHERCAT	ETHERCAT Protocol Decoder and search.
SMBUS	N/A	4-SRSMBUS	5- SRSMBUS	6- SRSMBUS	DPO714AX 7-SRSMBUS	SMBUS Protocol Decoder and search.
NFC	N/A	4-RFNFC	5-RFNFC	6-RFNFC	N/A	NFC protocol decode and search.

To upgrade an already purchased instrument

Serial bus	3 Series MDO Node-Locked License	4 Series MSO Node-Locked/Floating License	5 Series MSO Node-Locked/Floating License	6 Series MSO Node-Locked/Floating License	7 Series DPO Node-Locked/Floating License
MIL-STD-1553, ARINC 429	SUP3 SRAERO	SUP4-SRAERO SUP4-SRAERO-FL	SUP5-SRAERO SUP5-SRAERO-FL	SUP6-SRAERO SUP6-SRAERO-FL	7-SRAERO 7-SRAERO-FL
I^2S, LJ, RJ, TDM	SUP3 SRAUDIO	SUP4-SRAUDIO SUP4-SRAUDIO-FL	SUP5-SRAUDIO SUP5-SRAUDIO-FL	SUP6-SRAUDIO SUP6-SRAUDIO-FL	7-SRAUDIO 7-SRAUDIO-FL
CAN, CAN FD, CAN XL, LIN, FlexRay	SUP3 SRAUTO (Except CAN XL)	SUP4-SRAUTO SUP4-SRAUTO-FL	SUP5-SRAUTO SUP5-SRAUTO-FL	SUP6-SRAUTO SUP6-SRAUTO-FL	7-SRAUTO 7-SRAUTO-FL
128B132B	N/A	N/A	N/A	N/A	7-SR128B132B 7-SR128B132B-FL
64B66B	N/A	N/A	N/A	N/A	7-SR64B66B 7 SR64B66B-FL
8B10B	N/A	N/A	SUP5-SR8B10B SUP5-SR8B10B-FL	SUP6-SR8B10B SUP6-SR8B10B-FL	7-SR8B10B 7-SR8B10B-FL
NRZ	N/A	SUP4-SRNRZ SUP4-SRNRZ-FL	SUP5-SRNRZ SUP5-SRNRZ-FL	SUP6-SRNRZ SUP6-SRNRZ-FL	7-SRNRZ 7-SRNRZ-FL
10BASE-T1S, 100BASE-T1 Automotive Ethernet	N/A	N/A	SUP5-SRAUTOEN1 SUP5-SRAUTOEN1-FL	SUP6-SRAUTOEN1 SUP6-SRAUTOEN1-FL	7-SRAUTOEN1 7-SRAUTOEN1-FL
Automotive Ethernet Signal separation	N/A	N/A	SUP5-AUTOEN-SS	SUP6-AUTOEN-SS	7-AUTOEN-SS

Serial bus	3 Series MDO Node-Locked License	4 Series MSO Node-Locked/Floating License	5 Series MSO Node-Locked/Floating License	6 Series MSO Node-Locked/Floating License	7 Series DPO Node-Locked/Floating License
SENT	N/A	SUP4-SRAUTOSEN SUP4-SRAUTOSEN-FL	SUP5-SRAUTOSEN SUP5-SRAUTOSEN-FL	SUP6-SRAUTOSEN SUP6-SRAUTOSEN-FL	7-SRAUTOSEN 7-SRAUTOSEN-FL
RS-232/422/485, UART	SUP3 SRCOMP	SUP4-SRCOMP SUP4-SRCOMP-FL	SUP5-SRCOMP SUP5-SRCOMP-FL	SUP6-SRCOMP SUP6-SRCOMP-FL	7-SRCOMP 7-SRCOMP-FL
I^2C, SPI	SUP3 SREMBD	SUP4-SREMBD SUP4-SREMBD-FL	SUP5-SREMBD SUP5-SREMBD-FL	SUP6-SREMBD SUP6-SREMBD-FL	7-SREMBD 7-SREMBD-FL
Ethernet	N/A	SUP4-SRENET SUP4-SRENET-FL	SUP5-SRENET SUP5-SRENET-FL	SUP6-SRENET SUP6-SRENET-FL	7-SRENET 7-SRENET-FL
I3C	N/A	SUP4-SRI3C SUP4-SRI3C-FL	SUP5-SRI3C SUP5-SRI3C-FL	SUP6-SRI3C SUP6-SRI3C-FL	7-SRI3C 7-SRI3C-FL
SPMI	N/A	SUP4-SRPM SUP4-SRPM-FL	SUP5-SRPM SUP5-SRPM-FL	SUP6-SRPM SUP6-SRPM-FL	7-SRPM 7-SRPM-FL
Spacewire	N/A	SUP4-SRSPACEWIRE SUP4-SRSPACEWIRE	SUP5-SRSPACEWIRE SUP5-SRSPACEWIRE-FL	SUP6-SRSPACEWIRE SUP6-SRSPACEWIRE-FL	7-SRSPACEWIRE 7-SRSPACEWIRE-FL
USB 2.0	SUP3 SRUSB2	SUP4-SRUSB2 SUP4-SRUSB2-FL	SUP5-SRUSB2 SUP5-SRUSB2-FL	SUP6-SRUSB2 SUP6-SRUSB2-FL	7-SRUSB2 7-SRUSB2-FL
USB 3.0	N/A	N/A	N/A	SUP6-SRUSB3 SUP6-SRUSB3-FL	7-SRUSB3 7-SRUSB3-FL
USB 3.1 Gen 1	N/A	N/A	N/A	SUP6-SRUSB3 SUP6-SRUSB3-FL	7-SRUSB3 7-SRUSB3-FL
USB 3.2 Gen 1	N/A	N/A	N/A	SUP6-SRUSB3 SUP6-SRUSB3-FL	7-SRUSB3 7-SRUSB3-FL
USB 3.1 Gen 2 and USB 3.2 Gen 2	N/A	N/A	N/A	N/A	7-SRUSB3 7-SRUSB3-FL
USB4 v1.0	N/A	N/A	N/A	N/A	7-SRUSB4 7-SRUSB4-FL
Serial analysis bundle	SUP3 BND	N/A	N/A	N/A	N/A
PCIe Gen 1/Gen 2/Gen 3	N/A	N/A	N/A	SUP6-SRPCIE321 SUP6-SRPCIE321-FL	7-SRPCIE321 7-SRPCIE321-FL

Serial bus	3 Series MDO Node-Locked License	4 Series MSO Node-Locked/Floating License	5 Series MSO Node-Locked/Floating License	6 Series MSO Node-Locked/Floating License	7 Series DPO Node-Locked/Floating License
PSI5	N/A	SUP4-SRPSI5 SUP4-SRPSI5-FL	SUP5-SRPSI5 SUP5-SRPSI5-FL	SUP6-SRPSI5 SUP6-SRPSI5-FL	7-SRPSI5 7-SRPSI5-FL
MDIO	N/A	SUP4-SRMDIO SUP4-SRMDIO-FL	SUP5-SRMDIO SUP5-SRMDIO-FL	SUP6-SRMDIO SUP6-SRMDIO-FL	7-SRMDIO 7-SRMDIO-FL
SVID	N/A	SUP4-SRSVID SUP4-SRSVID-FL	SUP5-SRSVID SUP5-SRSVID-FL	SUP6-SRSVID SUP6-SRSVID-FL	7-SRSVID 7-SRSVID-FL
e-USB2	N/A	SUP4-SREUSB2 SUP4-SREUSB2-FL	SUP5-SREUSB2 SUP5-SREUSB2-FL	SUP6-SREUSB2 SUP6-SREUSB2-FL	7-SREUSB2 7-SREUSB2-FL
DPHY	N/A	N/A	SUP5-SRDPHY SUP5-SRDPHY -FL	SUP6-SRDPHY SUP6-SRDPHY-FL	7-SRDPHY 7-SRDPHY-FL
MANCHESTER	N/A	SUP4-SRMANCH SUP4-SRMANCH-FL	SUP5-SRMANCH SUP5-SRMANCH-FL	SUP6- SRMANCH SUP6- SRMANCH -FL	7- SRMANCH 7- SRMANCH -FL
SDLC	N/A	SUP4-SRSDLC SUP4- SRSDLC -FL	SUP5- SRSDLC SUP5- SRSDLC -FL	SUP6- SRSDLC SUP6- SRSDLC -FL	7- SRSDLC 7- SRSDLC -FL
CPHY 1.2	N/A	N/A	SUP5-SRCPHY	SUP6-SRCPHY	7-SRCPHY
1-Wire	N/A	SUP4-SRONEWIRE	SUP5-SRONEWIRE	SUP6-SRONEWIRE	7-SRONEWIRE
eSPI	N/A	SUP4-SRESPI SUP4-SRESPI-FL	SUP5-SRESPI SUP5-SRESPI-FL	SUP6-SRESPI SUP6-SRESPI-FL	7-SRESPI 7-SRESPI-FL
CXPI	N/A	SUP4-SRCXPI SUP4-SRCXPI-FL	SUP5-SRCXPI SUP5-SRCXPI-FL	SUP6-SRCXPI SUP6-SRCXPI-FL	7-SRCXPI 7-SRCXPI-FL
ETHERCAT	N/A	SUP4-SRETHERCAT SUP4-SRETHERCAT-FL	SUP5-SRETHERCAT SUP5-SRETHERCAT-FL	SUP6-SRETHERCAT SUP6-SRETHERCAT-FL	7-SRETHERCAT 7-SRETHERCAT-FL
SMBUS	N/A	SUP4-SRSMBUS SUP4-SRSMBUS-FL	SUP5-SRSMBUS SUP5-SRSMBUS-FL	SUP6-SRSMBUS SUP6-SRSMBUS-FL	7-SRSMBUS 7-SRSMBUS-FL
NFC	N/A	SUP4-RFNFC SUP4-RFNFC-FL	SUP5-RFNFC SUP5-RFNFC-FL	SUP6-RFNFC SUP6-RFNFC-FL	N/A

NOTE:

- Software is supplied with the instrument firmware. Always download and install the latest version of the firmware. Option documentation is part of theapplication Help.
- 3 Series MDO option license names do not have a dash in the option number.

Recommended probes

Please refer to http://www.tek.com/probes for further information on the recommended models of probes and any necessary probe adapters.

Partner products ordering information

To add to an instrument at purchase (Supports Windows Option)

Serial bus type	Minimum Bandwidth	Recommended Probes	5 Series MSO Option	6 Series/6B Series MSO Option	Description
PGY-eMMC (Windows Option Only)	2 GHz	Standard probes of 5/6 Series MSO	PGY-eMMC	PGY-eMMC	eMMC and SD (UHS-I) electrical measurements and Protocol decoding. software conforms to eMMC version 4.41,4.51,5.0, 5.1 specification. Supports Boot, SDR, DDR, HS200 and HS400 mode for electrical measurement and protocol Decode
PGY-SDIO(Windows Option Only)	2 GHz	Standard probes of 5/6 Series MSO	PGY-I2C	PGY-I2C	I2C Electrical Validation and Protocol decode software
PGY-QSPI(Windows Option Only)	500 MHz	Standard probes of 5/6 Series MSO	PGY-SPI	PGY-SPI	Electrical measurements compliance testing and protocol decoding as specified in QSPI specification. Supports Single and Dual Transfer rate (STR/DTR), electrical measurements and compliance testing for Ext SPI, Dual SPI and Quad SPI. Supports Triggering on command index and on S# falling edge. Supports Analog and Digital Channels of Tektronix MSO Series

Reference Selling of List of protocols supported on MSO series (please note: Windows only)

Serial bus type	Minimum Bandwidth	Recommended Probes	Ordering	5 Series MSO Option	6 Series/6B Series MSO Option	Description
RFFE	500 MHz	Standard probes of MSO5/6 series	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-RFFE	PGY-RFFE	RFFE Protocol Trigger & Decode Analysis Software. PGY-RFFE utilizes the hardware based real-time RFFE protocol aware trigger, protocol analysis of long acquisition record length up to 125MB to provide superior RFFE Protocol Analysis result at press of button.
I2S	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-I2S	PGY-I2S	I2S Electrical, Audio and Protocol Testing software
I2C	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-I2C	PGY-I2C	I2C Electrical Validation and Protocol decode software
SPI	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-SPI	PGY-SPI	SPI Electrical Validation and Protocol decode software
I3C	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-I3C	PGY-I3C	I3C Electrical Validation, Protocol trigger and Decode software
JTAG	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-JTAG	PGY-JTAG	JTAG Protocol decode Software

Serial bus type	Minimum Bandwidth	Recommended Probes	Ordering	5 Series MSO Option	6 Series/6B Series MSO Option	Description
ONFI	4 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-ONFI	PGY-ONFI	ONFI Electrical Timing Analysis software
SPMI	500 MHz and above	standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-SPMI	PGY-SPMI	SPMI Protocol Decode Software
MPHY	16 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-UPRO PGY-LLI PGY-UFS(needs PGY-UPRO)	PGY-UPRO PGY-LLI PGY-UFS(needs PGY-UPRO)	MIPI MPHY -UniPro/LLI/UFS Protocol Decode software
USB 2.0	2 GHz	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-USB	PGY-USB	USB 2.0 Protocol Decode software
USB-PD	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-PD	PGY-PD	USB PD (CC) Protocol Analysis software
UART	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-UART	PGY-UART	UART Electrical Validation and Protocol Decode Software
KX/KR	12 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-NEGO	PGY-NEGO	KX/KR DME and Line Training Analysis software
10Base-T1S	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-10Base T1S	PGY-10Base T1S	10 Base-T1S Protocol Decode software
100Base-T1	2 GHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-100Base T1	PGY-100Base T1	100 Base-T1 Protocol Decode software
SVID	500 MHz and above	Standard probes	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-SVID	PGY-SVID	SVID Protocol Decode software
USB3 Gen 1	23 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-USB3 Gen1	PGY-USB3 Gen1	USB3 Gen 1 5 Gbps Protocol Decode software
USB3 Gen 2	23 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-USB3 Gen1	PGY-USB3 Gen1	USB3 Gen 2 Protocol Decode software
8B10B	4 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-8B10B	PGY-8B10B	8B10B Protocol Decode software
1000T1-LT	4 GHz and above	Contact Prodigy	Reference Selling. Contact: http:// contact@prodigytechno.com	PGY-1000T1-LT	PGY-1000T1-LT	1000BaseT1 Line Training Decode Software

Terms and Conditions

Lead time of 2-3 Weeks ARO.

Certifications

Tektronix is registered to ISO 9001:2015 and ISO 14001:2015.

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